

The screenshot shows the Logic Friday IDE interface. At the top, there is a toolbar with icons for file operations, a logic symbol, and a component selection menu. The component selection menu is open, showing a list of components. The 'tb ALU' component is selected and highlighted in blue. Below the menu, the main workspace is empty.

Type	Signals
wire	op[1:0]
wire	rd_addr_a[2:0]
wire	rd_addr_b[2:0]
wire	reg_write_ip[15:0]
wire	reset
wire	ryt_sft_sel
wire	sel
wire	sft_op[3:0]
wire	sft_sel
wire	slt_sel
wire	wr
wire	wr_addr[2:0]

Append Insert Replace

[illegible][illegible]