Vincent Cheng

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Education

Toronto Metropolitan University Toronto, ON
BEng, Computer Engineering (Co-op) *Sept 2022 – Present*

- Achieved Dean's List recognition (2023)
- Relevant Courses: Electronic Circuits, Object Oriented Engineering Analysis, Signals and Systems, Microprocessor Systems

Skills and Proficiencies

Languages: Java, C, C++, Python, VHDL, HTML, CSS, JavaScript, Dart, SQL
Frameworks: React.js, Bootstrap, Node.js, Express.js
Tools: Git, GitHub, Android Studio, VSCode, Firebase, MongoDB, Quartus II, KiCad, Flutter, Microsoft Office, Figma

Experience

Front End Web Developer - Internship March 2024 – July 2024
Anvil Business Center *React.js, Bootstrap, HTML5, CSS3, JavaScript*

- Developed an interactive, responsive website using HTML, JavaScript, and CSS, improving user traffic by 31% through SEO implementation
- Collaborated with the CEO to prototype designs using Figma

Projects

Personal Portfolio Website | *React.js, Bootstrap, HTML5, CSS3, JavaScript* vincentcheng1618.com

- Developed a personal portfolio to showcase projects, professional experience, and skills

Full Stack Financial Tracker | *MongoDB, Express.js, React.js, Node.js, TypeScript*

- Developed a full-stack finance tracker application using the MERN stack to track and manage personal expenses and budgets
- Implemented user authentication and authorization with Clerk
- Designed a responsive and interactive frontend with React.js, utilizing RESTful APIs to fetch and display real-time financial data from a MongoDB database
- Integrated backend logic using Node.js and Express.js to handle CRUD operations, enabling users to add, update, and delete transactions

Android App Development | *Dart, Flutter*

- Utilized the Flutter UI software development kit and coded in Dart to create a user-friendly app tailored to the needs of 1000+ high school students
- Led a team of 5 developers

Stock Ticker Display | *C++, ESP32*

- Uses an ESP32 to retrieve real-time stock data using the Finnhub Stock API
- Displays results on an I2C LCD display and includes LED price change indicators
- Built and 3D printed a custom case, and soldered components for the project

Simple General-Purpose Processor Design | *VHDL, Quartus II, FPGA*

- Designed and implemented a simple general-purpose processor using VHDL, Quartus II, and an FPGA board
- Developed key components, including an Arithmetic and Logic Unit (ALU), a Control Unit with a Finite State Machine (FSM), and a 4x16 Decoder
- Utilized microcode operations to control arithmetic and logic processes, with outputs displayed on seven-segment displays