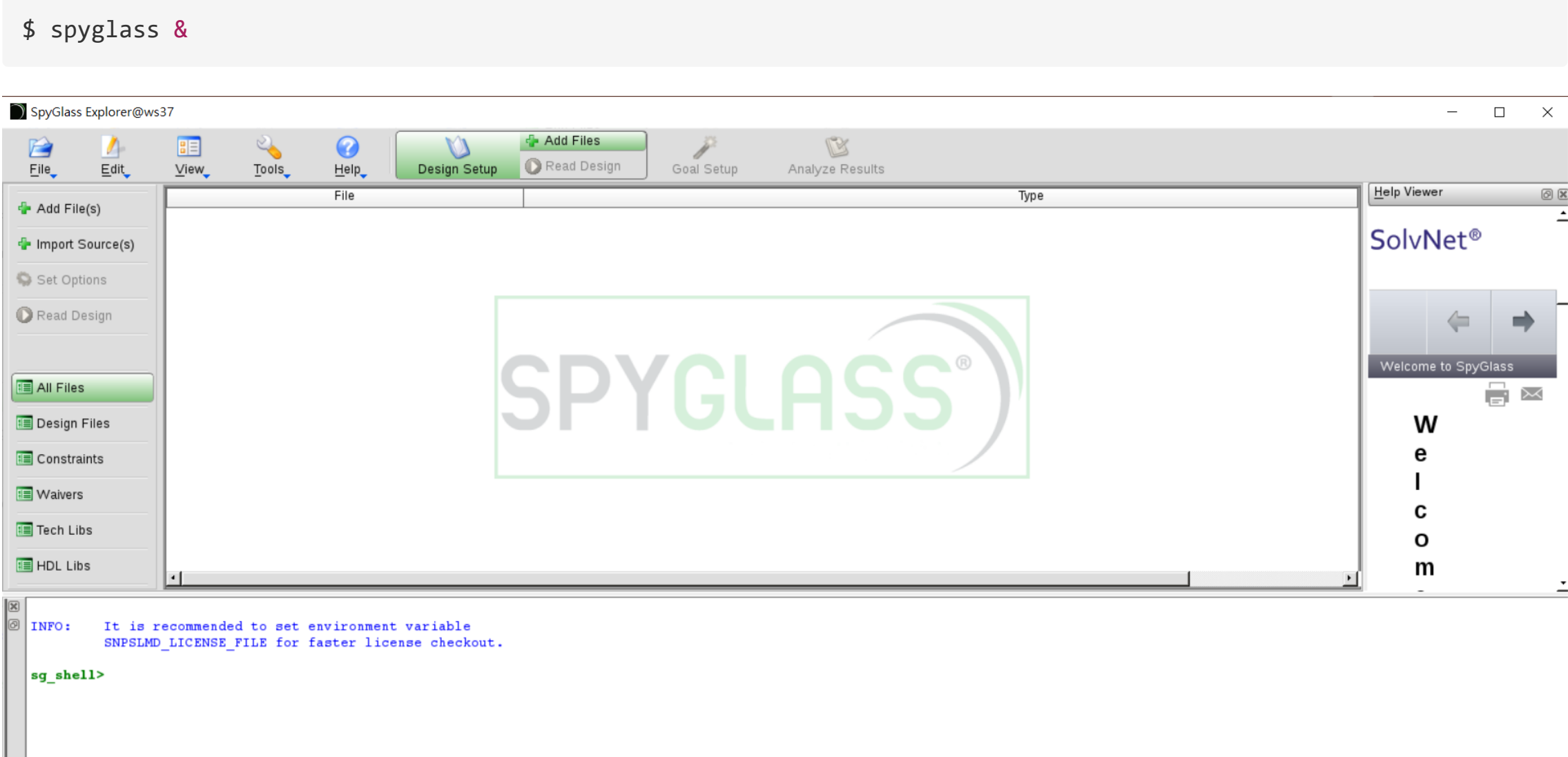


# Spyglass

## Graphical User Interface (GUI)

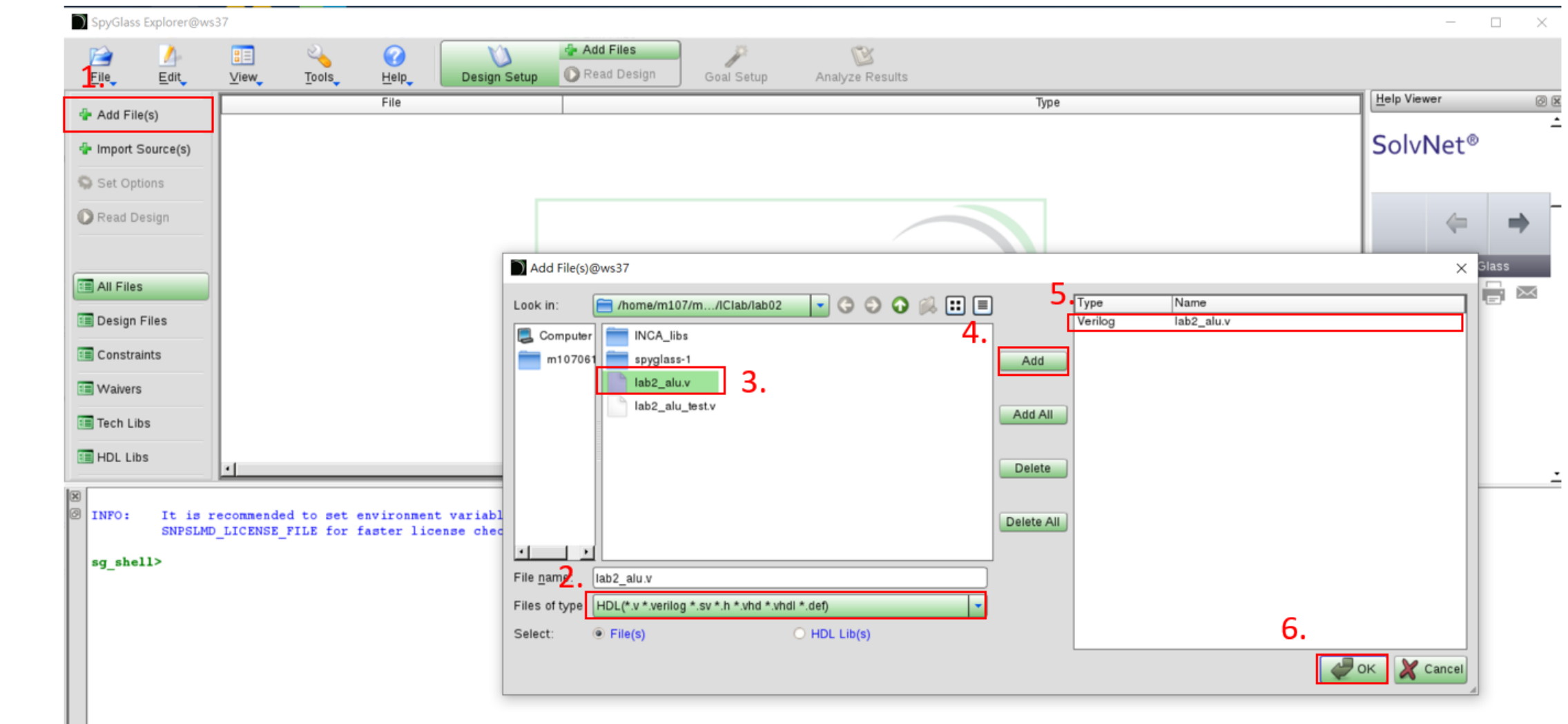
### Start Spyglass GUI

execute the following command



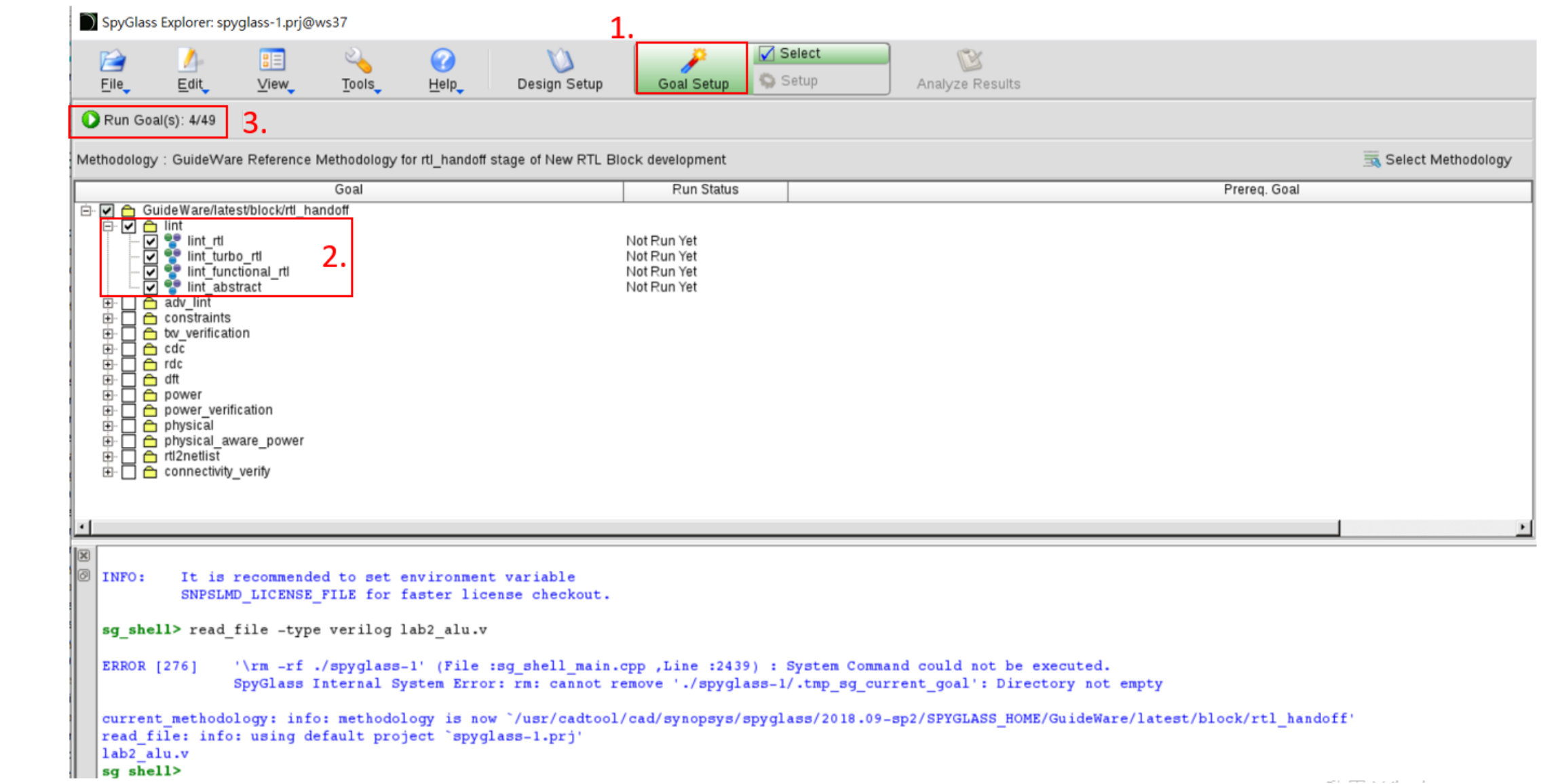
### Import Design

- Click Add Files
- Change Files of type to HDL(\*.v).
- Select your own verilog code .  
(Note that we don't select testbench since we won't synthesize it in our design.)
- Click Add
- You will see your design on right side.
- Click OK



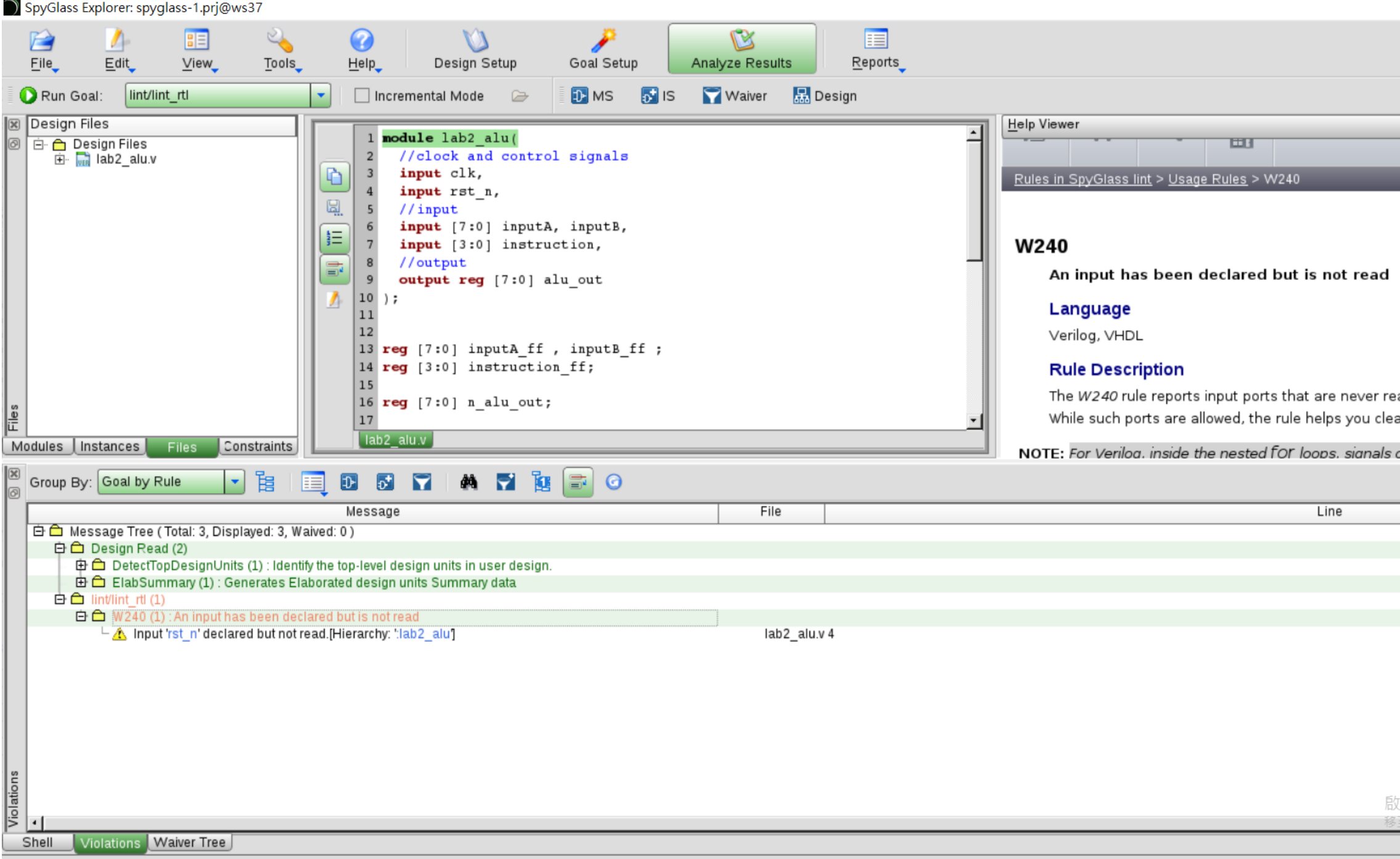
### Goal Setup

- Click Goal Setup
  - Set the options as below.
  - Click Run Goal(s)
- (Note that this step will take you a few minutes to run all the selected goals.)



### Analyze Results

After running the lint check , you can see the analyze results of your design. Check what they means and try to solve all errors and warnings listed as possible.



You can check the detail report in project directory . For example , if you want to see report of lint\_rtl :

```
$ cd spyglass-1/lint/lint_rtl/spyglass_reports/  
$ gedit moresimple.rpt &
```

For lint\_turbo\_rtl , you need to change you path to the following path :

```
$ cd spyglass-1/lint/lint_turbo_rtl/spyglass_reports/  
$ gedit moresimple.rpt &
```

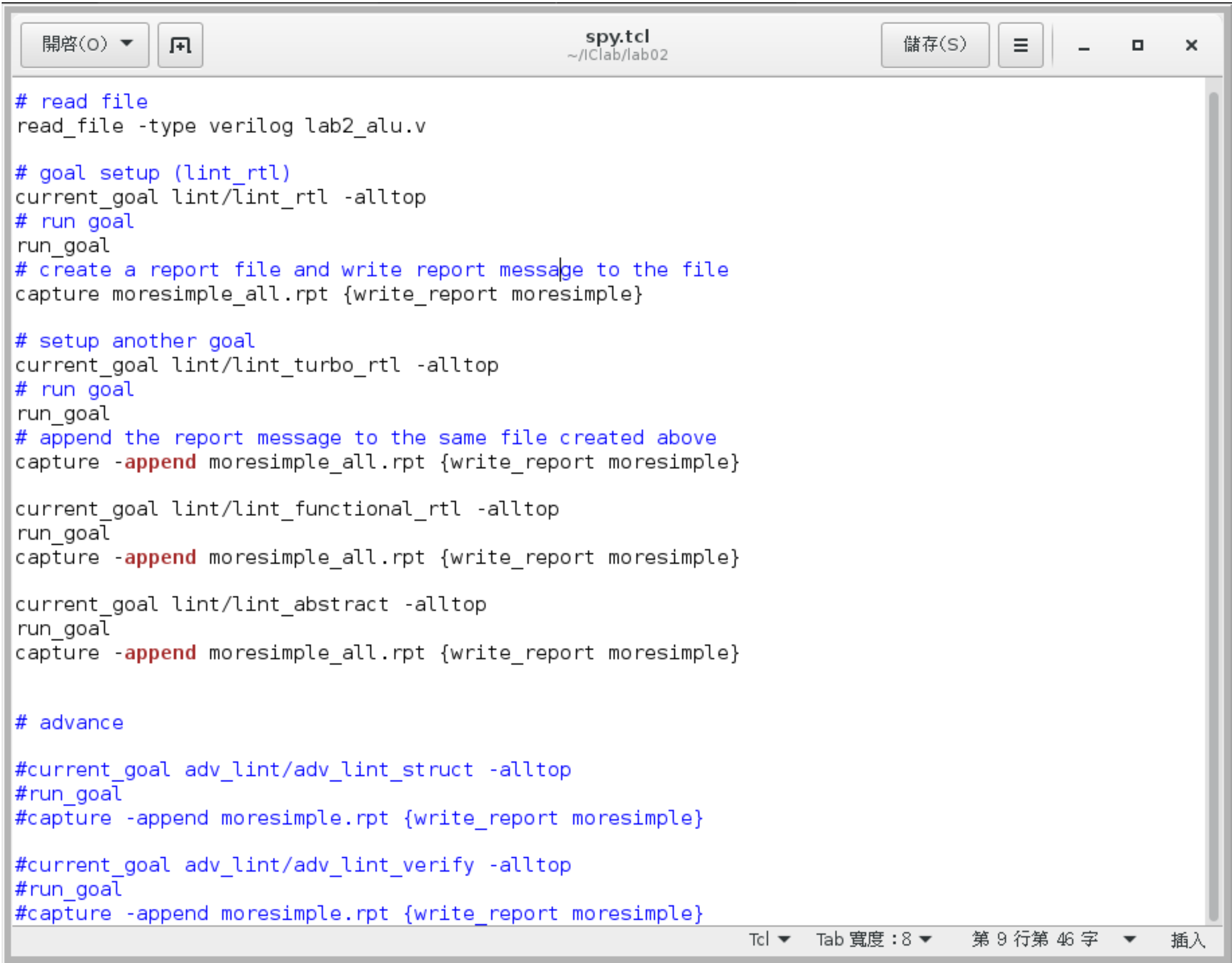
## Tcl Shell Interface

In general , we will use script written in tool command language (tcl) to run the CAD tool for convenience .  
In this part we will introduce the basic script for Spyglass to achieve the same goal.

### Writing your own script

```
$ gedit spy.tcl &
```

Write the command listed below , make sure that you know what they actually mean before using them .



Execute the following command

```
$ sg_shell < spy.tcl
```

### Check report massage

Since we have generated all report massage in one file , you can just check them in the following file :

```
$ gedit moresimple_all.rpt &
```

You can also see detail report in the path we mentioned above .