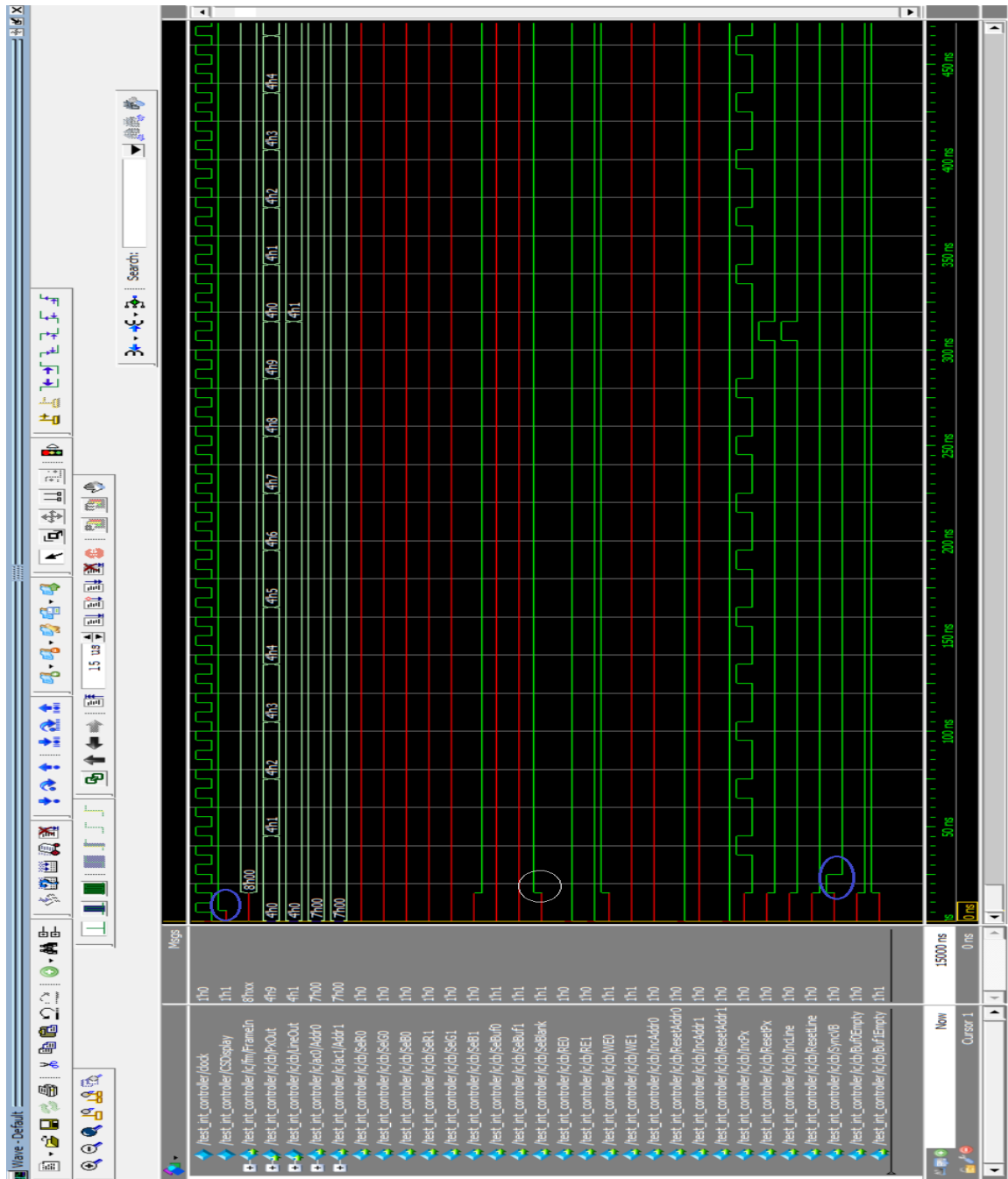


**Timing Diagrams:**

The timing diagrams are shown for a 10x10 image with 2 vertical blanking lines. The active image consists of 10 pixels and 10 lines.

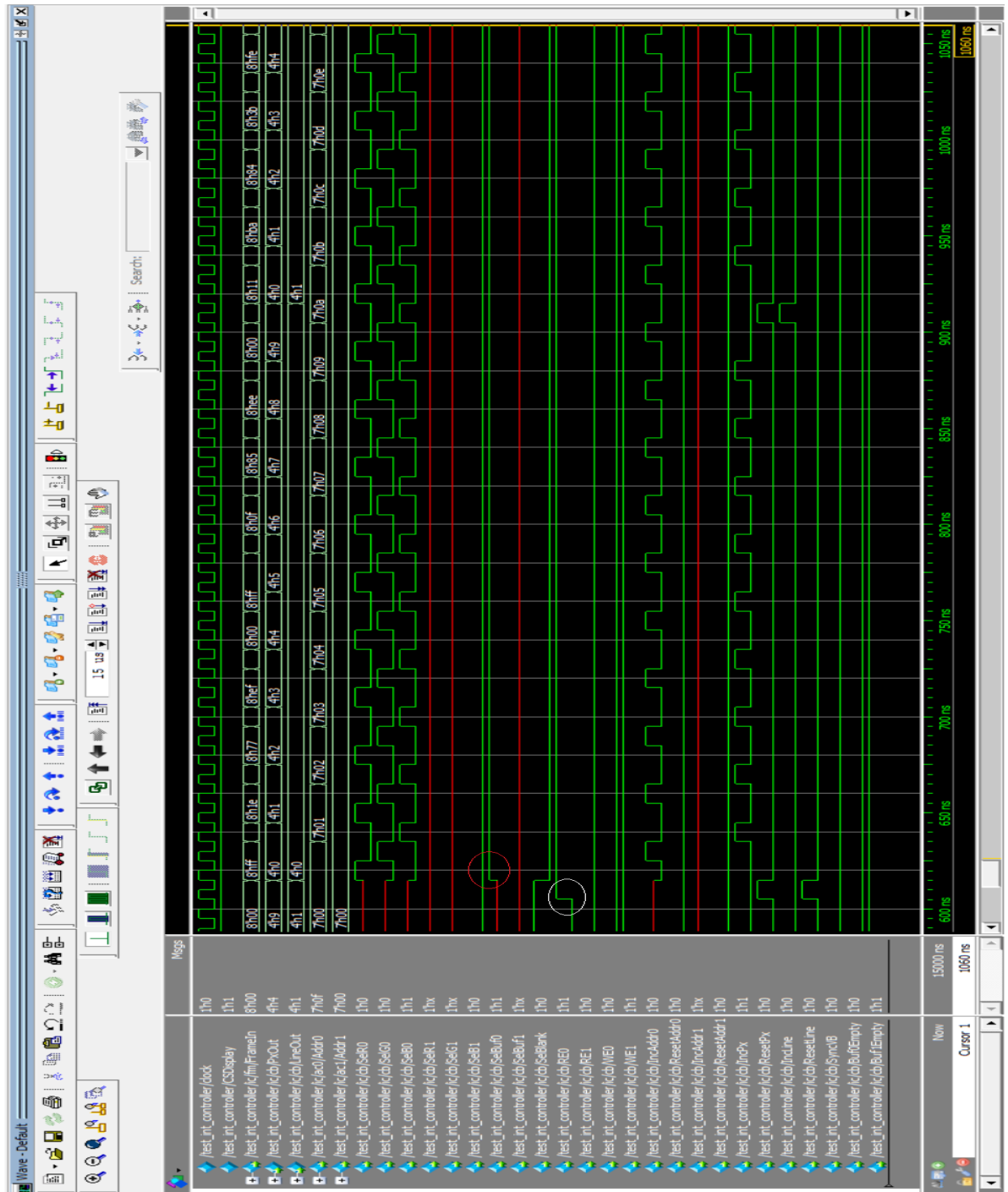
### 1. Vertical Blank: (Line 1 of Buf0)

In the below timing diagram, the display unit is activated by CSDisplay =1. Once active, the first component of the blanking pixel arrives and SyncVB is also high indicating the start of a vertical blank.



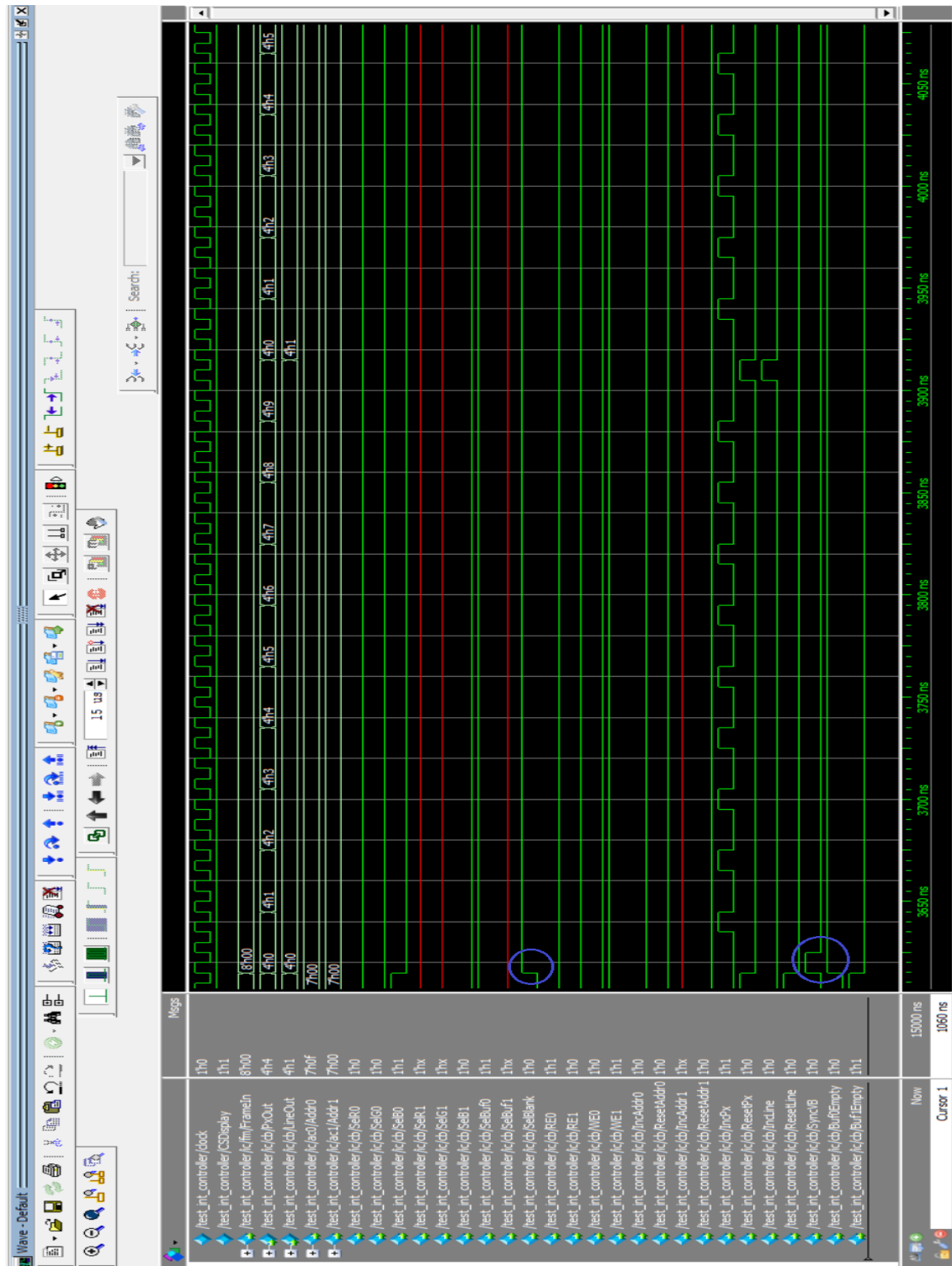
## 2. Active Image: (Line 1 of Buf0)

While the vertical blanking completes, in the last cycle the REO goes high indicating to start reading from Buf0. Then, SelBlank goes down and SelBuf0 goes high to select Buf0. As the SelR0, SelG0 and SelB0 signals go high the respective components are selected from the Buf0. Addr0 indicates the address of the row from which the data is being read.



### 3. Vertical Blank: (Line 1 of Buf1)

As the CSDisplay is still high after reading data from Buf0, SyncVB again goes high indicating the start of vertical blanking for a new frame. First component of the blanking pixel also arrives in the same cycle. The signal SelBlank goes high indicating the start of the vertical blank.



#### 4. Active Image: (Line 1 of Buf1)

While the vertical blanking completes, in the last cycle the RE1 goes high indicating to start reading from Buf1. Then, SelBlank goes down and SelBuf1 goes high to select Buf1. As the SelR1, SelG1 and SelB1 signals go high the respective components are selected from the Buf1. Addr1 indicates the address of the row from which the data is being read.

