#### AV312 - Lecture 9

Vineeth B. S.

Department of Avionics, Indian Institute of Space Science and Technology.

Figures from "Communication Systems" by Haykin and "An Intro. to Analog and Digital Commn." by Haykin and Moher

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#### Review of last class

- ► Frequency modulation
- ▶ Signal spectrum and Bandwidth
- Direct and indirect FM generation
- ► FM demodulation by envelope detection

# Today's class

- Phase locked loop (PLL)
  - Carrier recovery using PLL
  - ► FM demodulation using PLL
  - ▶ DSB demodulation using PLL Costas receiver
- Analysis of PLL
- ► Today's scribes are Gemi Rachel George and Harshitha Gollamudi

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#### Motivation



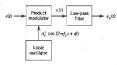
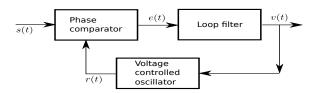


FIGURE 2.7 Coherent detector for demodulating DSB-SC modulated wave.

- ► A local oscillator needs to produce a replica of the carrier at the receiver
- ▶ Replica ⇒ match in both frequency and phase
- ▶ Difference in frequencies ⇒ time varying (linear) difference in phase

$$cos(2\pi f_1 t)$$
 and  $cos(2\pi f_2 t)$ 

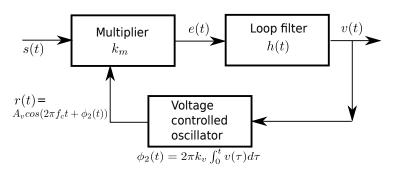
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- ightharpoonup r(t) is the local oscillator's output
- ▶ We want r(t) to "match" with s(t) in phase
- We want e(t) to measure the instantaneous phase difference between s(t) and r(t)
- ightharpoonup Filtered output v(t) controls the VCO output r(t) to match s(t)

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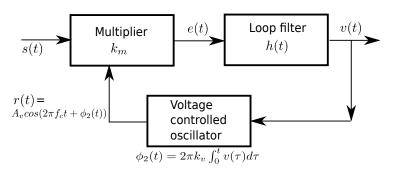
### PLL - An implementation



- $\blacktriangleright$  h(t) is a low pass response
- $\triangleright$   $k_m$  and  $k_v$  are the sensitivities of the multiplier and the VCO respectively

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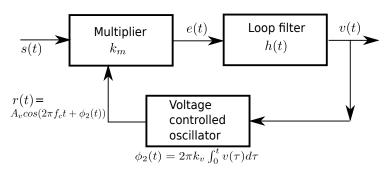
## PLL - Applications



- Assume that phase error  $\phi_1(t) \phi_2(t) \approx 0$
- How does carrier recovery work?

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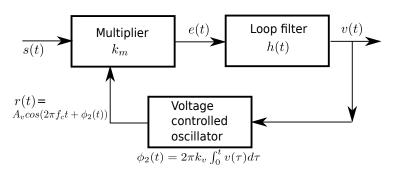
## PLL - Applications



- Assume that phase error  $\phi_1(t) \phi_2(t) \approx 0$
- How does carrier recovery work?
- How does FM demodulation work?

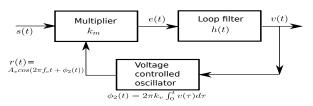
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## PLL - Applications



- Assume that phase error  $\phi_1(t) \phi_2(t) \approx 0$
- How does carrier recovery work?
- How does FM demodulation work?
- Read Costa's receiver from the textbook.

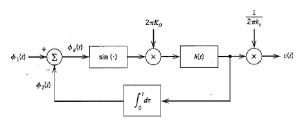
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- $Let s(t) = A_c sin(2\pi f_c t + \phi_1(t))$
- $e(t) = k_m A_c A_v \left[ sin(4\pi f_c t + \phi_1(t) + \phi_2(t)) + \frac{sin(\phi_1(t) \phi_2(t))}{sin(\phi_1(t) \phi_2(t))} \right]$
- ► Since h(t) is a low pass response;  $v(t) = \int_{-\infty}^{\infty} k_m A_c A_v \sin(\phi_1(\tau) - \phi_2(\tau)) h(t - \tau) d\tau$

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#### PLL - Model

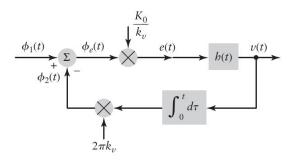


- $\phi_e(t) = \phi_1(t) \phi_2(t)$
- ▶ Loop gain parameter  $K_o = k_v k_m A_c A_v$
- PII model

$$\frac{d\phi_{\rm e}(t)}{dt} = \frac{d\phi_{\rm 1}(t)}{dt} - 2\pi K_{\rm o} \int_{-\infty}^{\infty} \sin(\phi_{\rm e}(\tau)) h(t-\tau) d\tau$$

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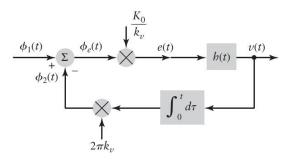
### PLL - Linearized Model



- Assume that  $sin(\phi_e(t)) \approx \phi_e(t)$
- ▶ We use a Laplace transform domain approach
- $\phi_1(t) \leftrightarrow \phi_1(s), \ \phi_2(t) \leftrightarrow \phi_2(s), \ v(t) \leftrightarrow V(s), \ h(t) \leftrightarrow H(s)$

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### PLL - Linearized Model



- $But \Phi_2(s) = 2\pi k_v \frac{V(s)}{s}$
- $\Phi_1(s) \frac{K_o}{k_o} H(s) = V(s) \left[ 1 + \frac{2\pi K_o}{s} H(s) \right]$
- $V(s) = \frac{s(K_o/k_v)H(s)}{s+2\pi K_oH(s)}$  and  $\frac{\Phi_e(s)}{\Phi_1(s)} = \frac{s}{s+2\pi K_oH(s)}$

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# PLL - Behaviour of v(t)

- ▶ What if there is a step change in the phase  $\phi_1(t)$ ?
- ▶ What if there is a change in the frequency of the input?

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