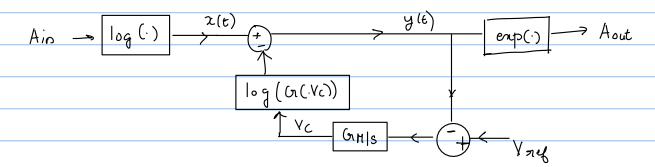
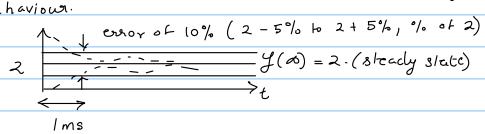
AU314 - Assignment 8

- Draw the block diagram of an AGIC loop which is used to automatically control the average power of a signal, so that the signal put out from the AGI loops has a constant power.
- 2) The AGC loop that was discussed in class was:



Assuming that G(Vc) = earc, design a, GH, and Vref so that y(t) has
the following behaviour.



The y(t) response should converge to the interval as shown above in Ims.