

Vineetha Yogesh

Contact Information

Address	Indian Institute of Science, SP1.19, ECE Dept Bangalore, Karnataka, India - 560012	Email: vineethay@iisc.ac.in Phone: +91 9448319418
Webpage	https://vineethay.github.io/	

Research Interests

Wireless communication, Next-generation wireless technologies, Diversity analysis and detection algorithms, Orthogonal time-frequency space modulation, Channel estimation

Education

Ph.D. - Indian Institute of Science (IISc), Bengaluru Dept of Electrical Communication Engineering <ul style="list-style-type: none">◇ Advisor : Prof. A. Chockalingam◇ CGPA : 8.9	Dec 2020 - Present
M.Tech. - National Institute of Technology (NIT), Tiruchirappalli Dept of Electronics and Communication Engineering <ul style="list-style-type: none">◇ Specialization : Communication Systems◇ CGPA : 9.53	Aug 2016 - June 2018
B.E. - Visvesvaraya Technological University, Belgaum Dept of Electronics and Communication Engineering <ul style="list-style-type: none">◇ CGPA : 85.08 %	Aug 2010 - May 2014
P.U.C. - Karnataka State Board <ul style="list-style-type: none">◇ Specialization : Science (PCMB)◇ Class XII Score : 94.17%	Jun 2008 - May 2010
Central Board of Secondary Education <ul style="list-style-type: none">◇ Class X Score : 86.8%	Jun 1996 - Mar 2008

Course Work

Mathematics	: Linear Algebra, Probability and Random Process, Time frequency Analysis
Communication	: Digital Communication, Multi-user Detection, Wireless Communications, Detection and Estimation theory

Industrial Experience

Qualcomm India Private Limited, Hyderabad <ul style="list-style-type: none">◇ Role : RF Engineer◇ Team : Integrated platform solution◇ Design verification testing of modem test platform (MTP) for various technologies in mobile communication including critical parameters for performance analysis under CDMA, TDS-CDMA technology.◇ Involved in end-to-end design until tape-out of an MTP, which included block diagram development and analysis of RFFE, carrier aggregation (CA) combo analysis for supporting desired regions, schematics, PCB stack design, component placement, PCB RF layout design and performance analysis.	July 2018 - Jul 2020
--	----------------------

Academic Experience

Institute of Smart Structures and Systems (ISSS)

August 2022 - October 2022

- ◇ Course instructor.
- ◇ Courses: Linear Algebra

Institute of Smart Structures and Systems (ISSS)

March 2023 - Present

- ◇ Course instructor.
- ◇ Courses: Wireless Communication

Honors & Awards

- ◇ Recipient of Prime Minister's Research Fellowship (PMRF), 2022.
- ◇ Recipient of Qualstar award in Qualcomm as recognition for outstanding performance in the team.
- ◇ Second topper in M.Tech at NIT Trichy.

Publications

- ◇ Conferences:
 - Vineetha Yogesh, Vighnesh S Bhat, Sandesh Rao Mattu, and A. Chockalingam, "On the bit error performance of OTFS modulation using discrete Zak transform," *IEEE ICC 2023*.
- ◇ Journals:
 - Vineetha Yogesh, Sandesh Rao Mattu, and A. Chockalingam, "Low-complexity delay-Doppler channel estimation in discrete Zak transform based OTFS," under review for *IEEE Comm. Lett.*