

CS4532 Concurrent Programming

Take Home Lab 3 and 4

Due – June 25 before 11:55 PM

Learning Outcomes

In this lab we will learn how to use “parallel for” and optimize a matrix-matrix multiplication to gain better performance. At the end of the lab you will be able to:

- use a “parallel for” statement to iterate through a loop body in parallel
- relate the performance of a given program to the architecture of the CPU
- improve the performance of the given program using a suitable optimization technique

Tasks

1. Write a sequential program to perform matrix-matrix multiplication
 - Dimensions of each matrix is $n \times n$ and elements/values of the matrix are of type Double.
 - Populate each matrix with randomly generated Double values. Make sure to set a different seed for each experiment.
 - Add necessary code to measure the time taken by the matrix-matrix multiplication. Do not measure the time to create matrixes and populate them with values.
2. Identify a suitable library for your chosen language that supports “parallel for” loops.
3. Write another program to perform the matrix-matrix multiplication using a “parallel for” loop.
4. Run both sequential and parallel-for versions of your program while changing the matrix size n from 200 to 2000 in steps of 200. [10 marks]
 - Record the time to perform matrix-matrix multiplication for each execution.
 - Collect sufficient number of samples for each configuration of n , while making sure your performance results are within an accuracy of $\pm 5\%$ and 95% confidence level.
 - Plot the matrix-matrix multiplication time against increasing n .
 - Plot speed up against increasing n .
5. Find out about the architecture of the CPU that you used for the evaluation. [5 marks]
 - Find out CPU model, no of cores, no of hardware-level threads, cache hierarchy, hardware optimizations, etc.
 - Can you justify the gained speed up knowing the architecture of the CPU?
 - Discuss your observations in detail while relating the observed behavior in graphs to the architecture of the CPU.
6. Find out at least two ways to optimize matrix-matrix multiplication. [5 marks]
 - Briefly describe a possible technique(s) while giving references.
7. Write another program to perform the matrix-matrix multiplication using a “parallel for” loop and a suitable optimization technique(s).
8. Run the new program and plot the results as described in Step 4. Describe your observations in relation to the optimization technique(s) you selected and CPU architecture as in Step 5. [10 marks]

Notes

- Each team can have 2 students. You are free to select your lab buddy; however, it should not be the same buddy that you had for any of the previous labs.
- 25% of the marks (out of 40) for this lab is proportional to the extra speed up that you gain after optimizing the program in Step 7. Therefore, better the speed up more the marks you gain. Following formula will be used to calculate your 25%:

$$\text{Min} (\text{Speedup_after_optimization} - \text{Speedup_before_optimization}, 20) \times 1.25$$

- Instructor will validate the speed up by running all 3 versions of your code. So make sure you give all the necessary instructions to complete and execute your programs.
- Make sure to comment your code.
- It is recommended that you clarify any concerns with the instructor before submitting the lab.
- I would be easier to do the project in C/C++ due to the availability of many parallel computing libraries for implementing “parallel for” loop. However, you are not allowed to use matrix multiplication libraries like BLAS and ATLAS.

What to Submit

- Submit the following files as a single gzip file:
 - All source files
 - make file, if relevant
 - README.txt explaining how to compile and run your program. Also indicate any required libraries and how to install and link them to your program
 - A PDF file with graphs, images, and descriptions
- Name the file as **lab3_<index no 1>_<index no 2>gzip**. Replace <index no x> with your index number.