

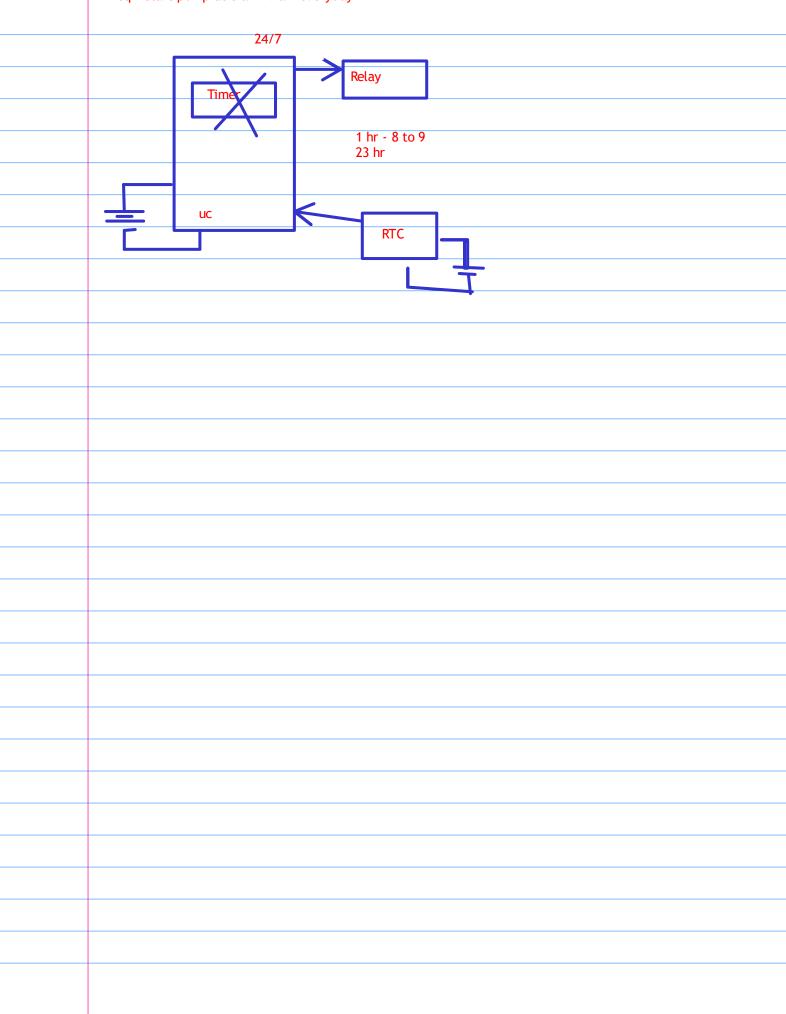
1	Automatic gate opening system	- Hospital entrance gate	
	1.Sensors		
	2. Motor - Relay - gate	ES ? yES	
	3.Comparator 4.timer		
	5.mc		
	To whom ? to everyone		
:	Automatic gate opening system	n - Jewellery Manf company	
	1.Sensors		
	2.Motor -Relay -gate	ES ? yES	
	3.Comparator 4.timer	L3 : yL3	
	5.mc		
	To whom		
	-employee		
	9 to 6 - -Biometric		
	-id		
	-Face recog - databa	ase - image processing	
	Security _{- 24/7}		
	2 shifts		
	Owner - any time	SOC -system on chip -uc or up -Heart memory - brain	

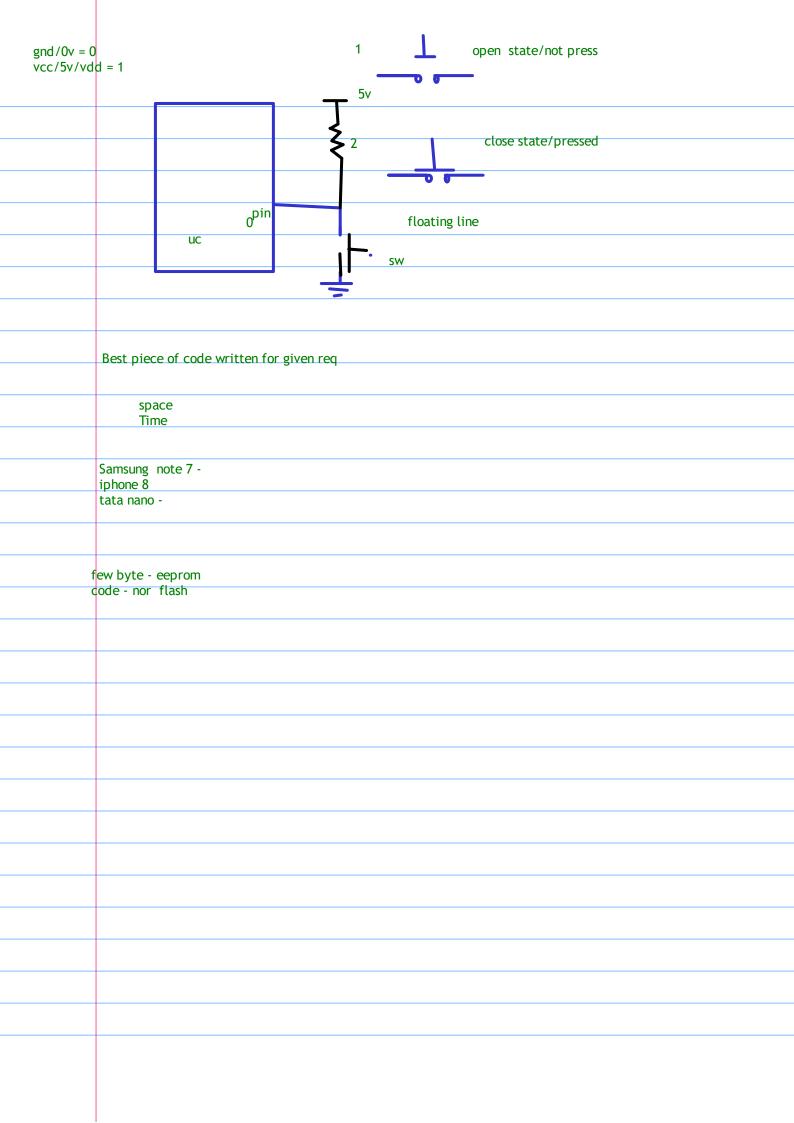
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Types of Memory
  1.ROM - Read only memory - read - non - volatile
  2.RAM -Random access Memory - read and write - volatile
  3. Hybrid memory
 ROM
   PROM / - ProgrammableRead only memory
         - programmable - only one
         -cant reprogram
         -OTP -One time programmable
  Ex: Kids toys industry
 EPROM - Erasable Programmable Read only memory -
            -Erasable
            -uv rays
             -UVROM
  mobile - selfie - EPROM- delete -
    Ex: Labs ,R&D
 Mask ROM
     -Hide / Protect
     -manuf date, slot id
Ex - Bootloader
RAM - Random access Memory
-read and write operation
 -SRAM
 -DRAM
SRAM - static Ram
    -power is lost ,data is lost
    -less dense
    -low power
ex . cache memory
DRAM - Dynamic Random Access Memory
  Ex.Ghajini - short term memory loss
 -short term memory loss
 -capacitor - no charge - data is lost
 -charge refreshment is needed
 -more power
 -more dense
  Ex:Main memory
```

```
Hybrid memory
   -properties of ROM and RAM
      mobile - selfie - PROM - delete? no
                    -SRAM - baterry low - sw off - restart - photo?no
EEPROM - Electrically erasable programmable read only memory
    - read and write
    - byte access
    -read fast
                         1 byte read and write
    -write slow
    -few bytes
Flash Memory
                                               1 byte
   -read and write
                                                     1 block = 8 byte
   -block access - 4 byte , 8,64,256
                                                                 5 th byte rewrite
   - large amt of data
                                 NAND FLASH
  NOR Flash
                                 -more dense
   -less dense
                                 -less reliable
   -more reliable
-code
                                  -data
                                                     code
    -XIP support
      execute in place
                                                       data
                             1 cm2
                              less dense - MBs
                              more dense - GBs
  main.c -> HDD
  gcc main.c -> a.out
  ./a.out - memory segments - RAM
   4 - 8 kbs -
   12 kb
```

Automatic Water pump system

Req - start pump at 8 am -9 am everyday

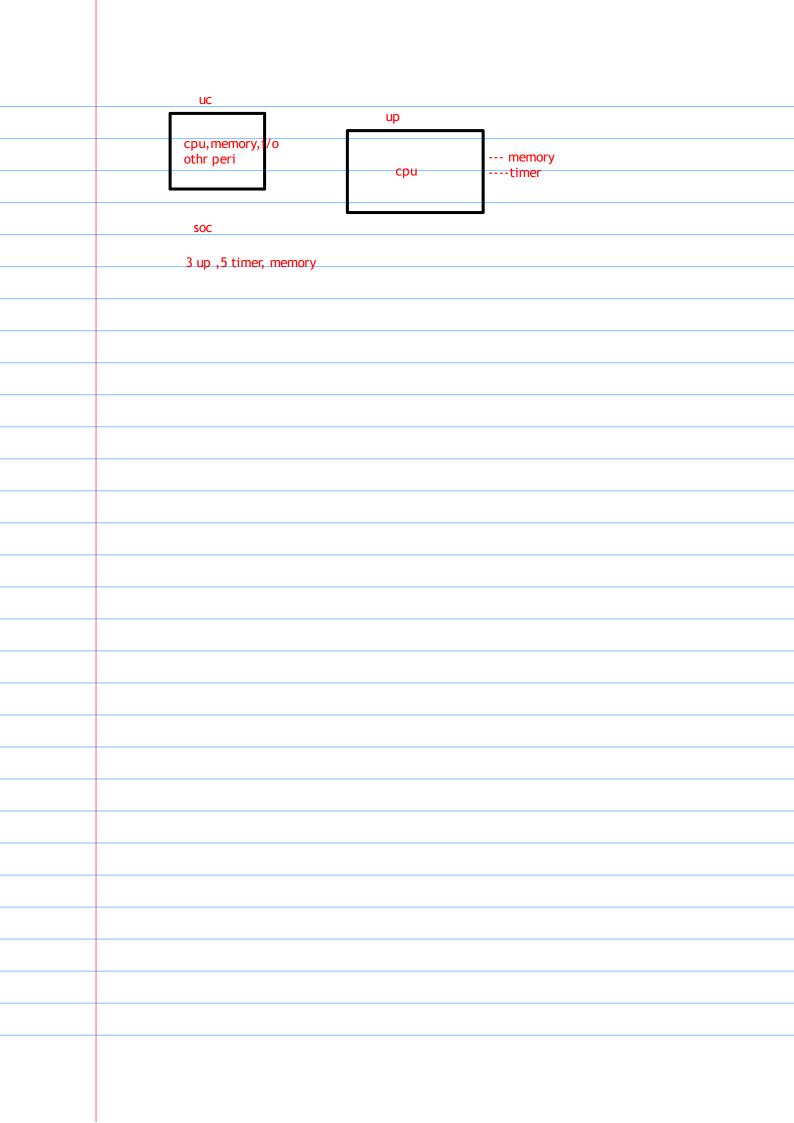




3 yrs
Basic prototype- 2 yrs
Non Recurring enginerring cost
Xyz
x+y+z - xyz - xy
ES-HW+SW+SPECIIFC task
GPS - General purpose system -system designed for general purpose
EX-Computer
mobile phone -
A system using which if yoiu are able to design an ES ,its not ES ,its GPS
ex.can i desigin an ES using automatic gate opening system? no - ES
can i desigin an ES using google car? no -ES can i desigin an ES using MOBILE? YES -ES not GPS
Screen
keyboard mouse

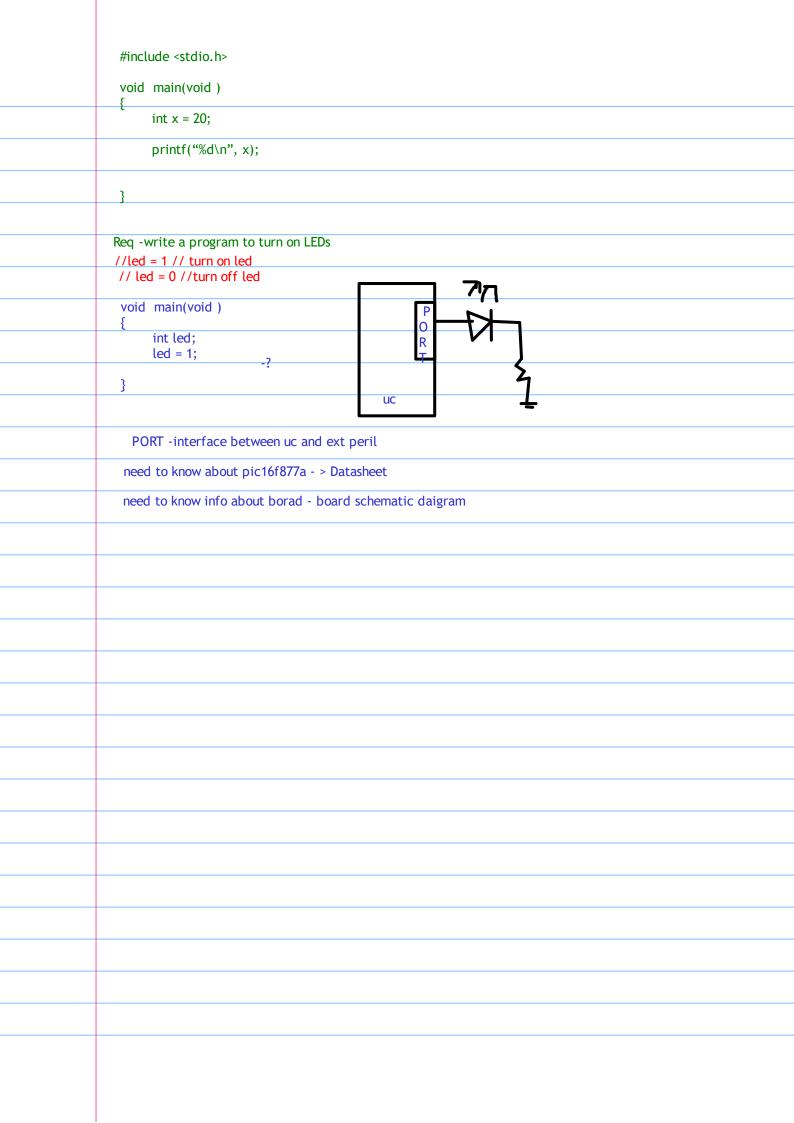
Real time ES Asystem capable of taking action in given instance within specified amt of time
Hard real time -time strictly followed
Ex. Air bag system, pace maker
Soft real time 40 min - 45 mins - few delay is acceptable
autumatic water pump sys - 8.05 - >
Firm real time
-time strictly followed - loss in property
Rover - soft landing crash landing - loss
Rover+human - hard
Fire alarm - office -human - > hard real time -> , stock room - > hard real time
y scock room - Hard real time

main.c -hdd
gcc main.c - a.out - hdd ./a.out - memory segments
stack heap - RAM
neap - RAM code data
same RISC CISC Ex> Von-Neuman arch
uC - ?

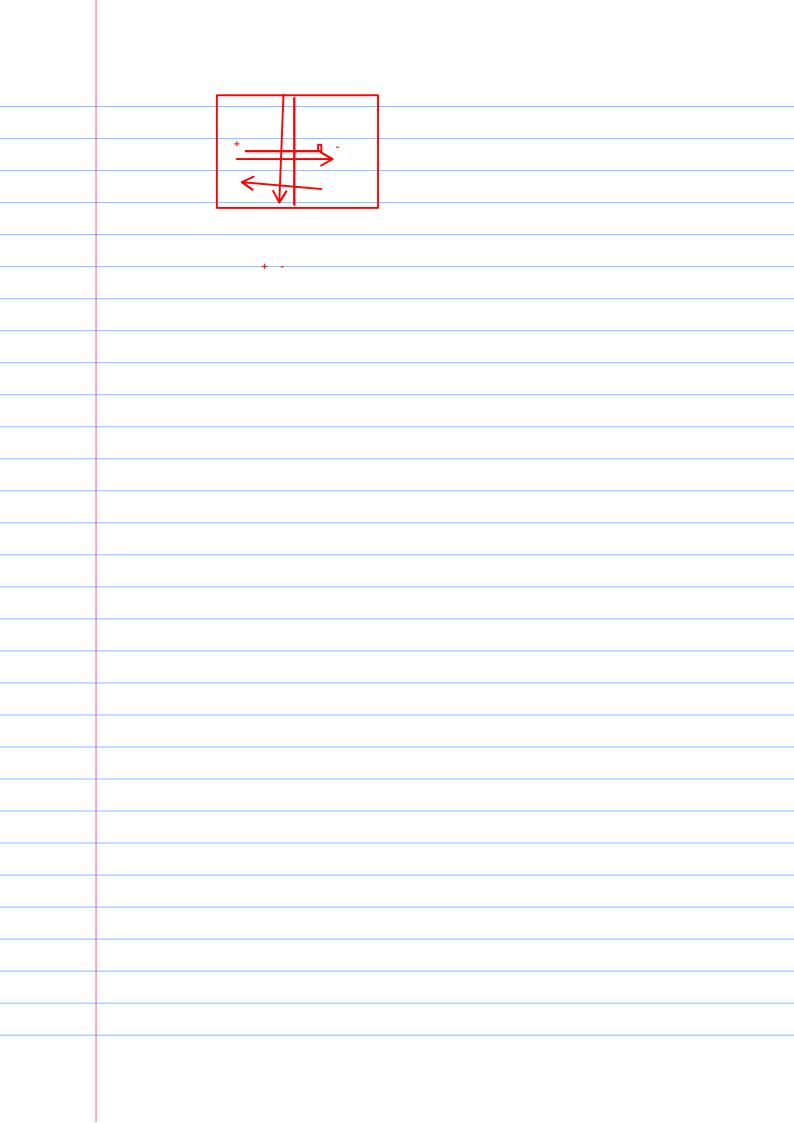


3 years 1 st 2 year 60%
3-
EOL - END OF LIFE - DATE

Host - Sys which is used to develop a specific app
ex:computer
Target
sys developed for a specific app
ex. online ,simulation tool - PICsimlab board -PIC genious - PIC16F877A
Cross compiler
compiler - app - converts source code to machine code
where is code - >computer where is it compiled -> computer
running ? target
cross compiler
ex, xc8,AVR gcc

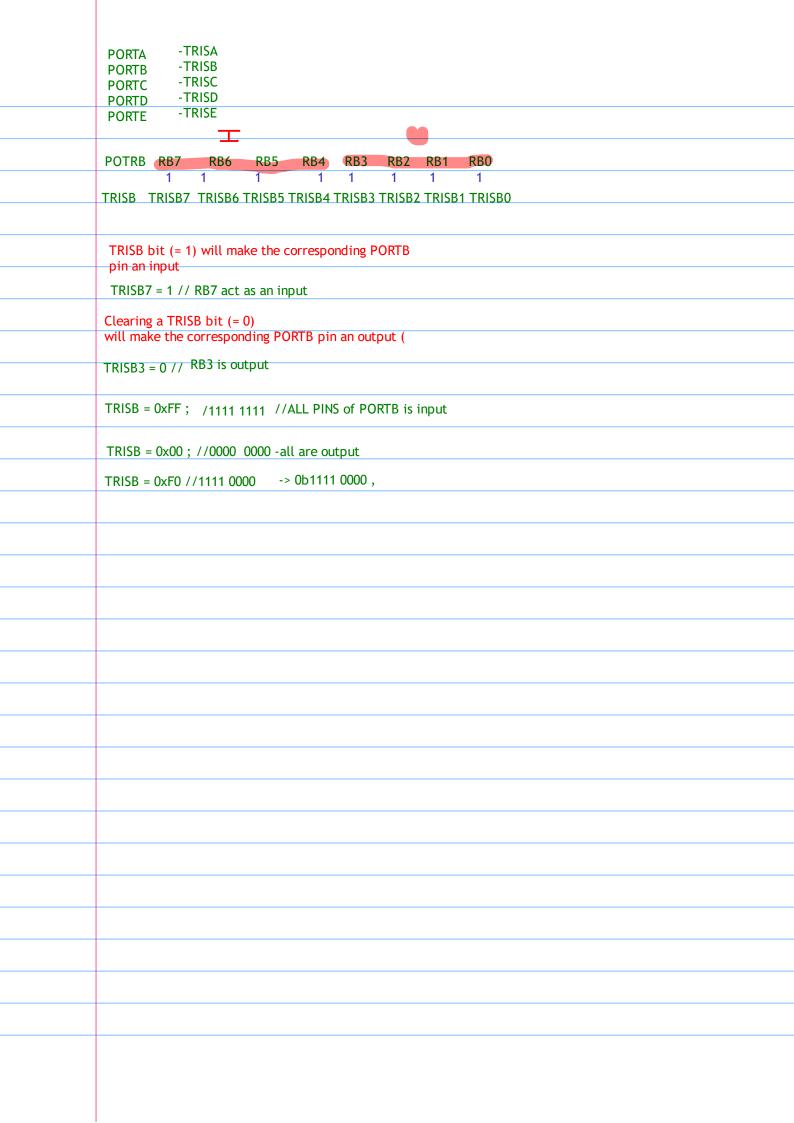


```
PIC16F877A
  Ports - 5 PORTS
   PORTA - 6
   PORTB - 8
  PORTC -8
PORTD -8
PORTE -3
        BiDirectional
 size of uc = size of data bus = 8 bit
 //led = 1 // turn on led
// led = 0 //turn off led
// to turn on leds on PORTB
 // direction is output for led
                                                   PORTB
                                                   -8 BIT
   void main(void)
                                                   -bidirectional
       int TRISB, PORTB;
       TRISB = 0x00;
       PORTB = 0xFF;
   }
```



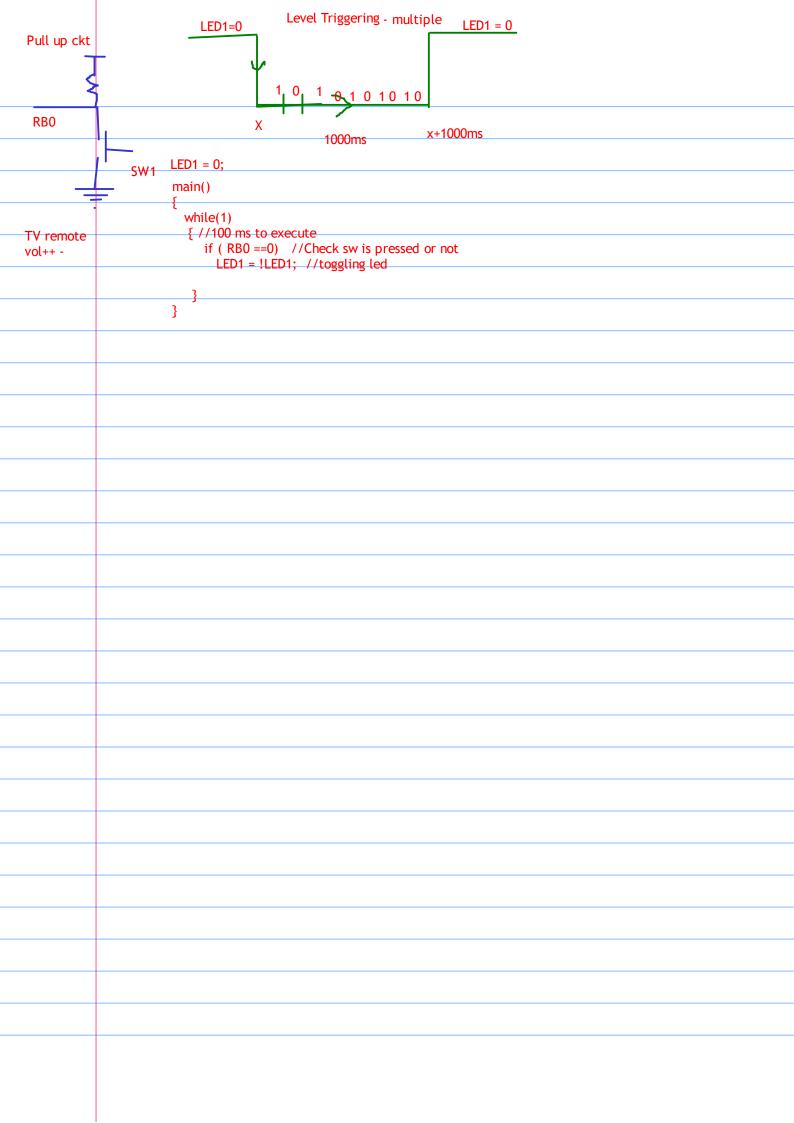
```
TRISB
void main(void)
                                                                0x00
    unsigned char *trisb = (unsigned char*) 0x86;
                                                       0x86
    *trisb = 0x00;
                                                                    PORTB
   unsigned char *portb = (unisgned char *) 0x06;
                                                                   0xff
    *portb = 0xFF;
                                                       0x06
  main.c
#include <xc.h>
init_config()
  //make all portb pins as output
  TRISB = 0 \times 00;
  //turn off all leds
 PORTB = 0x00;
                                                     .hex
Void main()
  init_config();
                                            target - exe - compiler - write
  while(1)
                                            mplab xIDE
   PORTB = 0xFF;
                                            xc8
                                            picsimlab
}
```

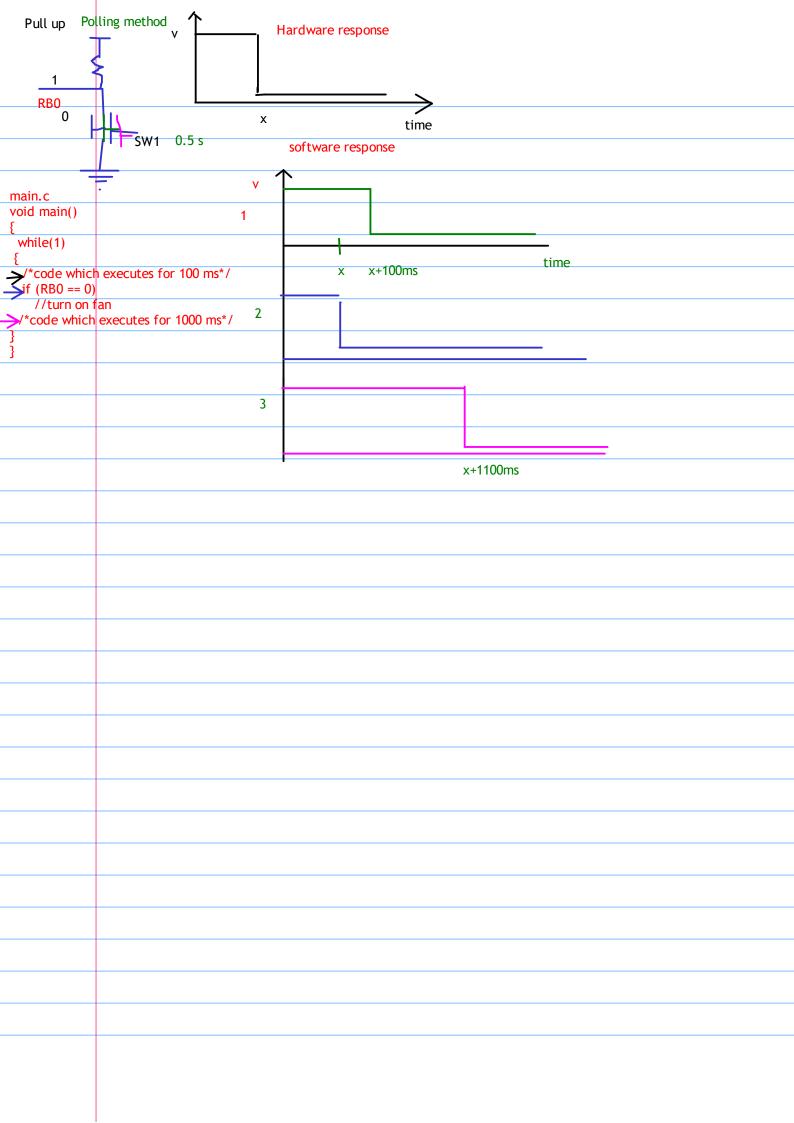
current sourcing ckt to turn on led ,uc = 1 to turn off led, uc = 0
current sinking ckt
to turn on led ,uc = 0 to turn off led, uc = 1
to-turn-off-led, uc = 1



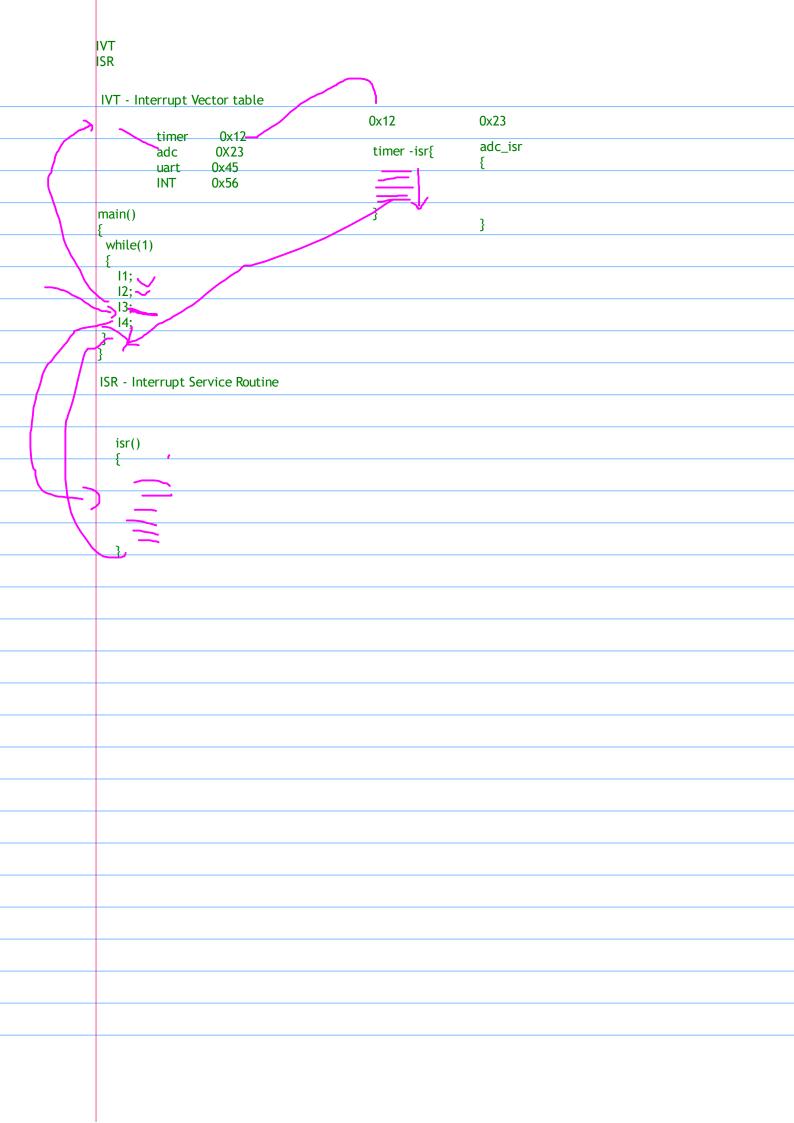
RD7 RD6 RD5 RD4 RD3 RD2 RD1 RD0	RD0
0 0 0 0 0 0 0 0 DELAY	1111 1111 << 1 1111 1110 << 1
0 0 0 0 0 0 1	1111 1100
DELAT 0 0 0 0 0 0 1 1	
DELAY 0 0 0 0 1 1 1	
1 1 1 1 1 1 1	
0000 0000	
0000 0000 << 1 1 0000 0001 << 1 1	
0000 0011<<1 0000 1100 1	

Pull up ckt -low level or falling edge
when sw is open - uc = 1 when sw is closed -uc = 0
Which shi is closed ac o
Pull down ckt -High level or rising edge
when sw is open - uc = 0 when sw is closed - uc = 1
In a circuit , if the connection is not complete or if its not connected to ant
potential then its called Floating line .
i need to take action based on low level or falling edge

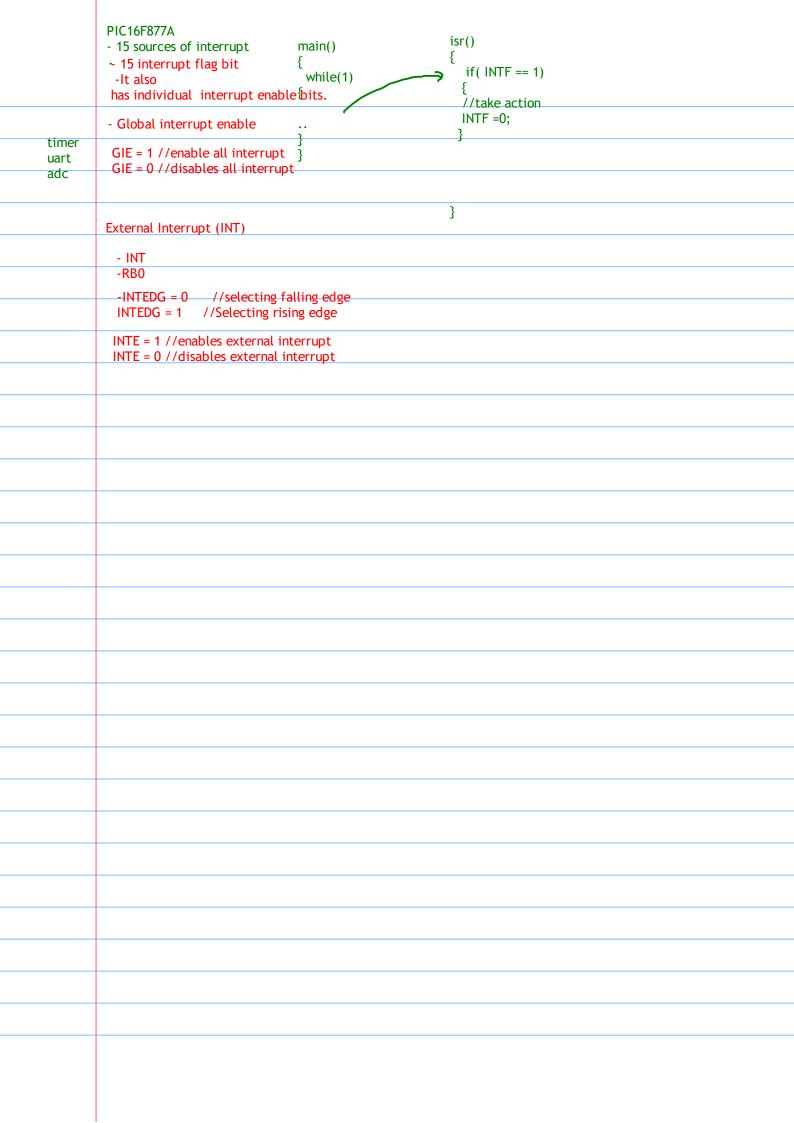




Disadvantage of polling method
- Bad response - Event loss
- poor power managemnet
Interrupt
Interrupt is a signal ,it wil make the processor to stop its current exexution proceed to execute the interrupt handler set for interrupting source
proceed to execute the interrupt handler set for interrupting source
Exceptional error
INT ,TRAP
LI system calls -software interrupt



isr
- simple and short
-no-blocking stst
loop
Latencey is the time taken from the point when interrupt occurs to the execution of 1st instruction in thr interrupt handle
Air bag system - crash /acc - interrupt - opens air bag(1st inst)



Toggle LED whenever ext interrupt occures	
-LED - External interrupt	
Togglr LED using polling and interrupt	
LED1 - interrupt method xc : LED2 - polling method	EP(3) sleep
·	
-LED -External interrupt -DKP	
off on-off-on-off-on off-on-off	
OIT-OIT-OIT-OIT	

