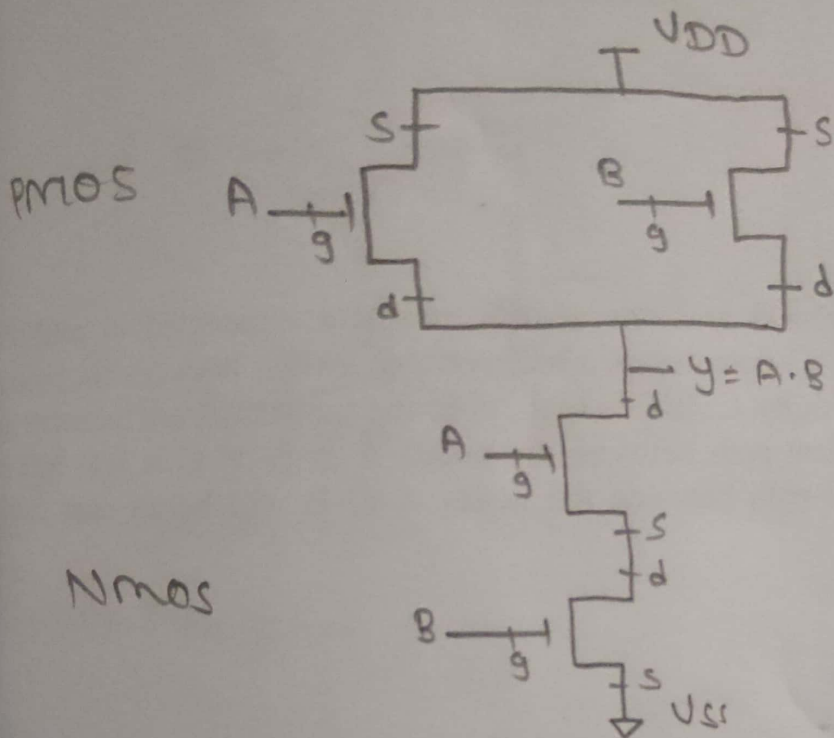
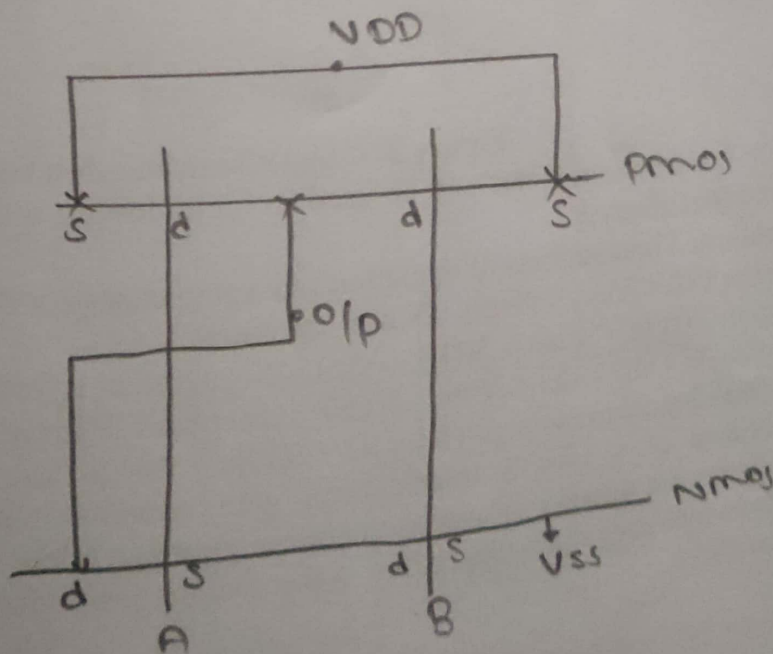


CMOS NAND

* circuit diagram

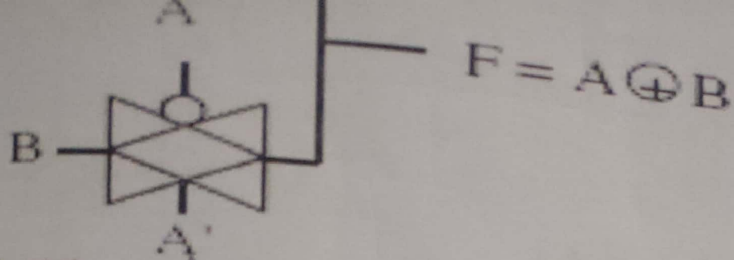


Stick Diagram



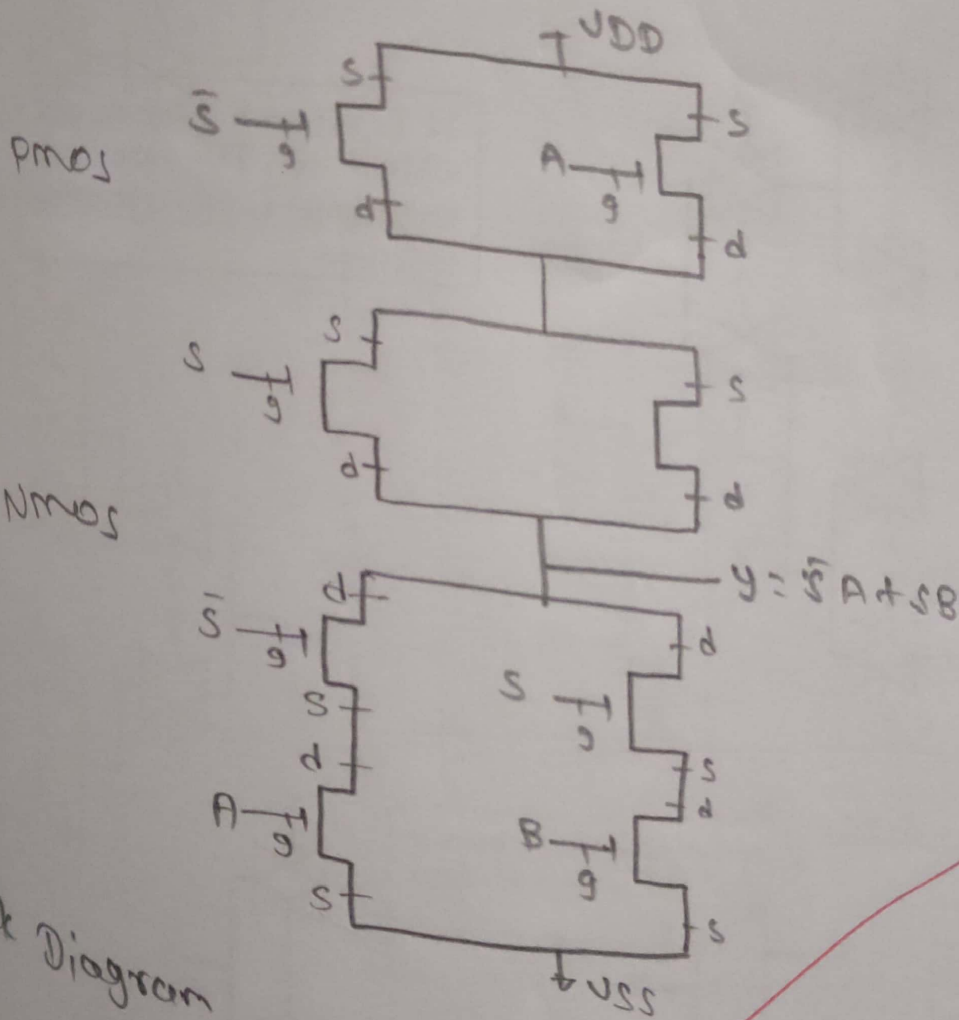
CMOS TRANSMISSION GATES (PASS GATES)

The NMOS switch passes a good zero but a poor 1. The PMOS switch passes a good one but a poor 0.

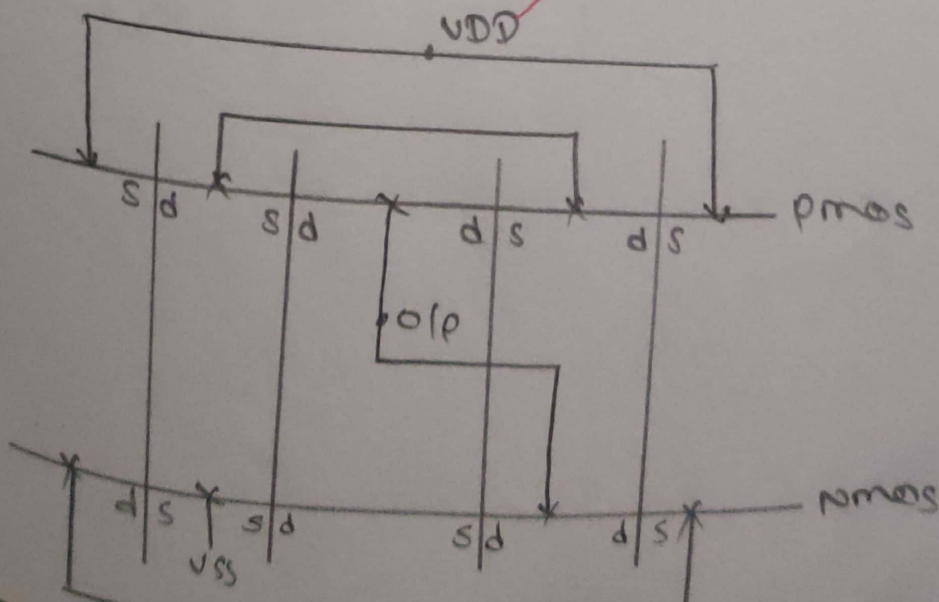


CMOS. 2-1 mux using cmos

Circuit Diagram



Stick Diagram



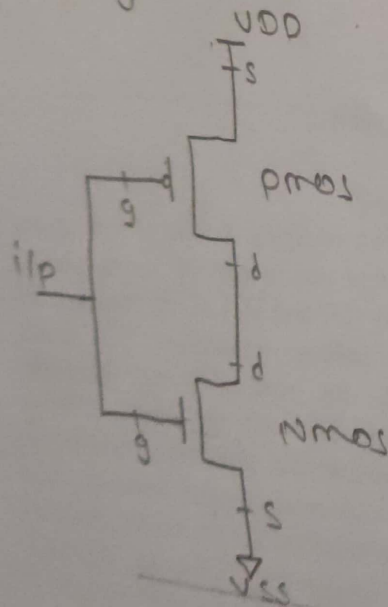
Conclusion:

We have installed microwind software, run the designing CMOS, Inverter, NAND, NOR, Half Adder & also understood the design rule. And simulate the above & verified the timing.

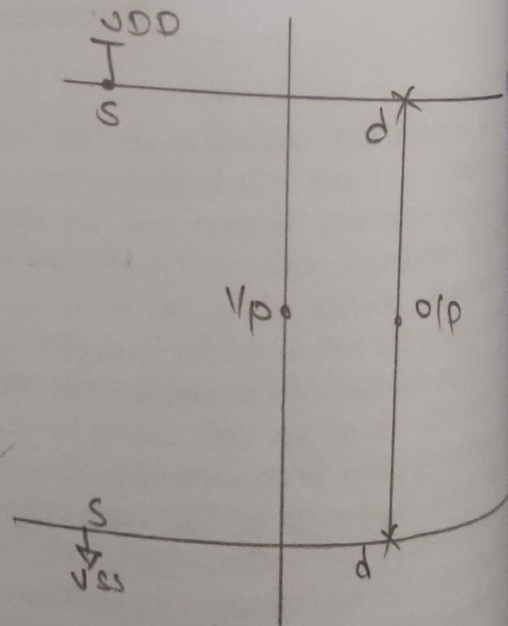
- Question 1. Draw small signal model for MOSFET?
2. What is body effect?

CMOS Inverter

*Circuit Diagram

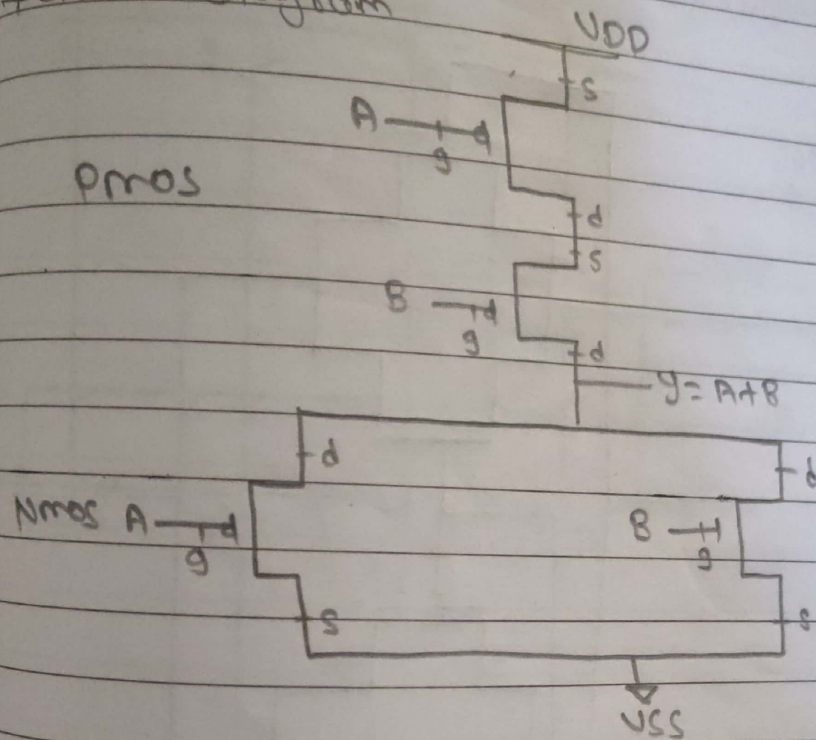


• Stick Diagram

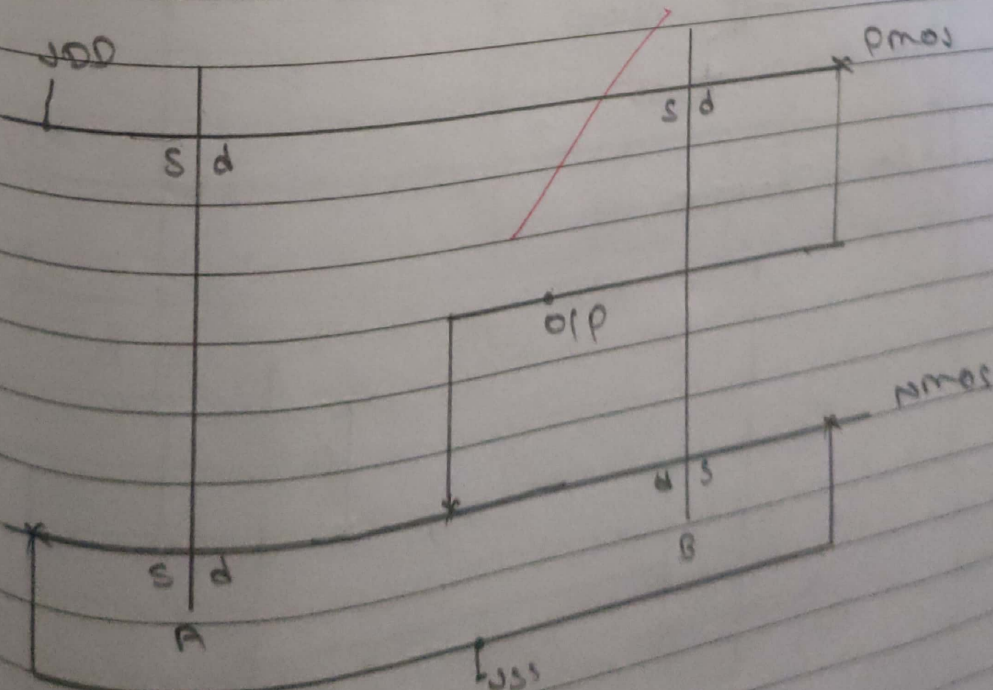


CMOS NOR

*Circuit Diagram

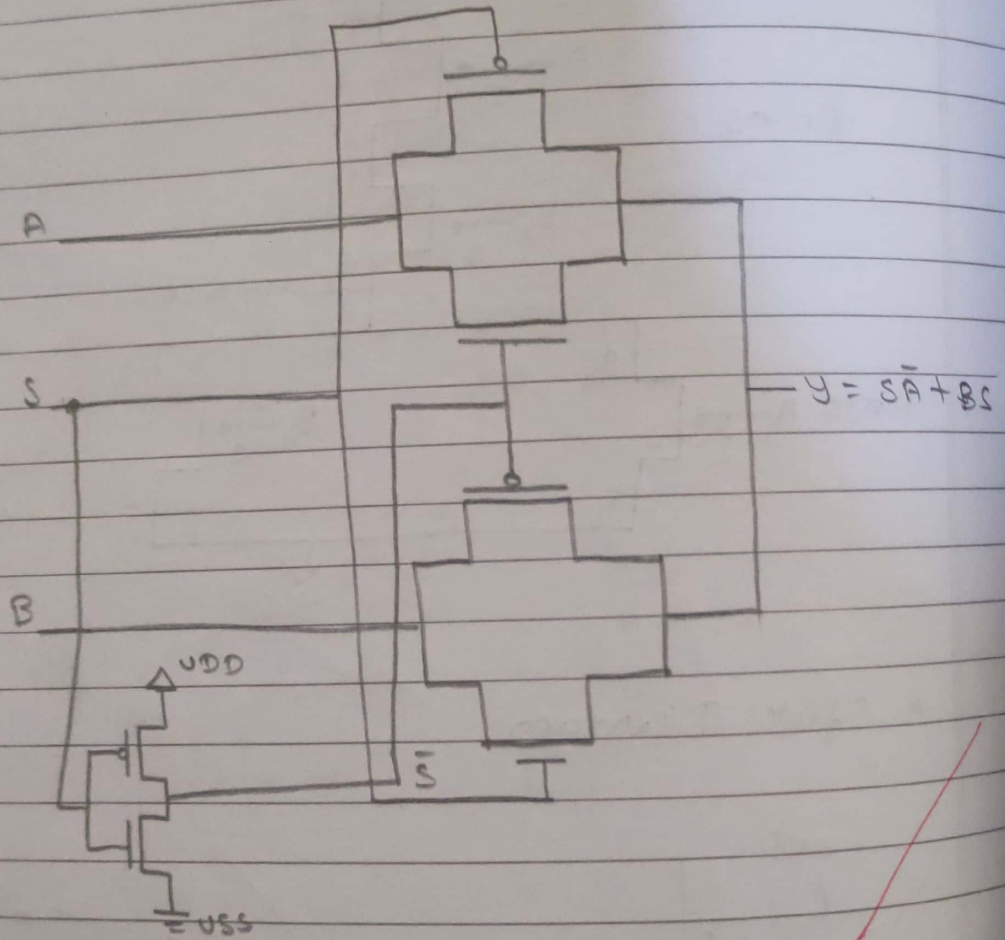


• Stick Diagram

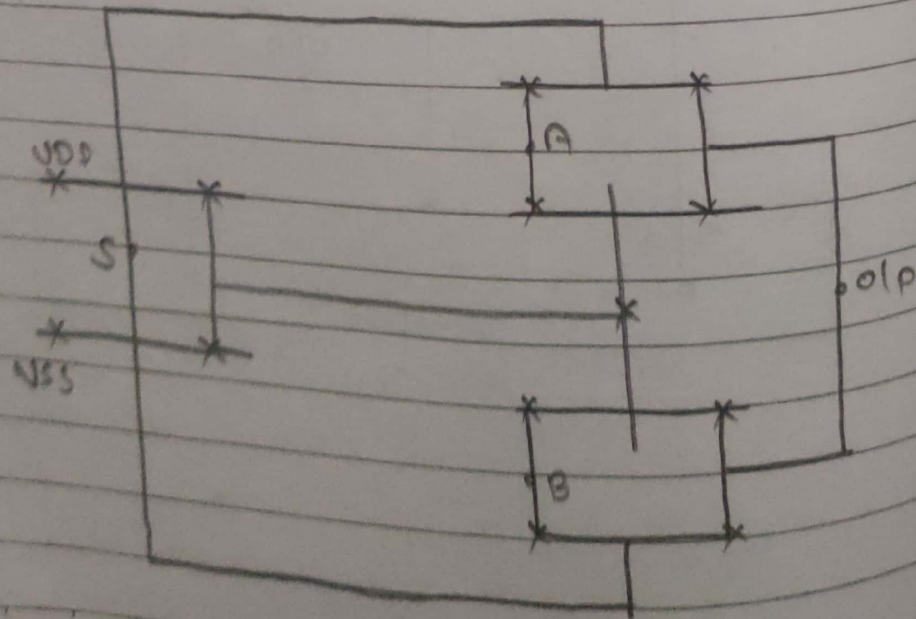


2:1 mux using TGL

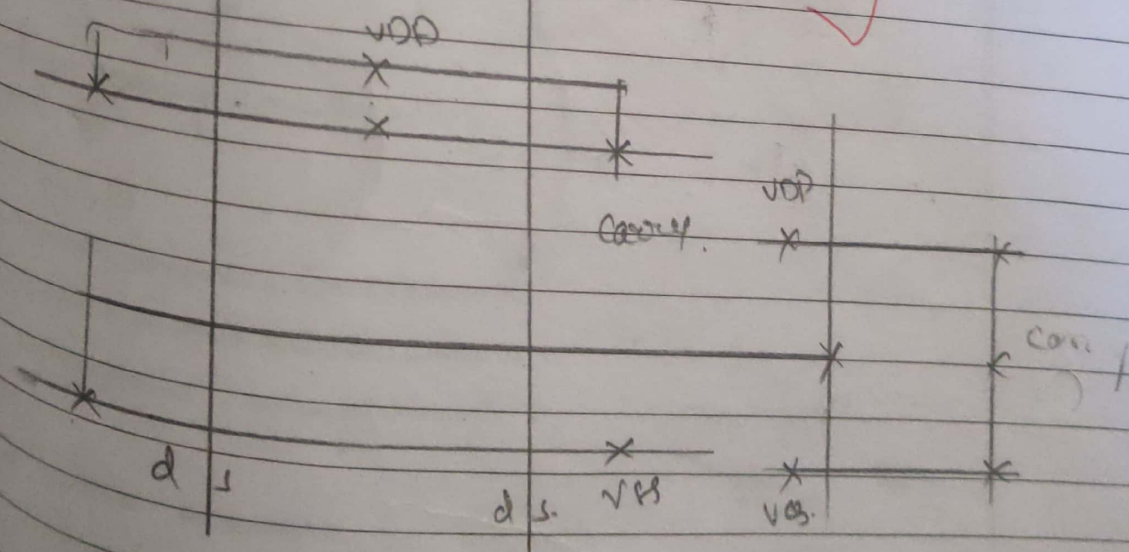
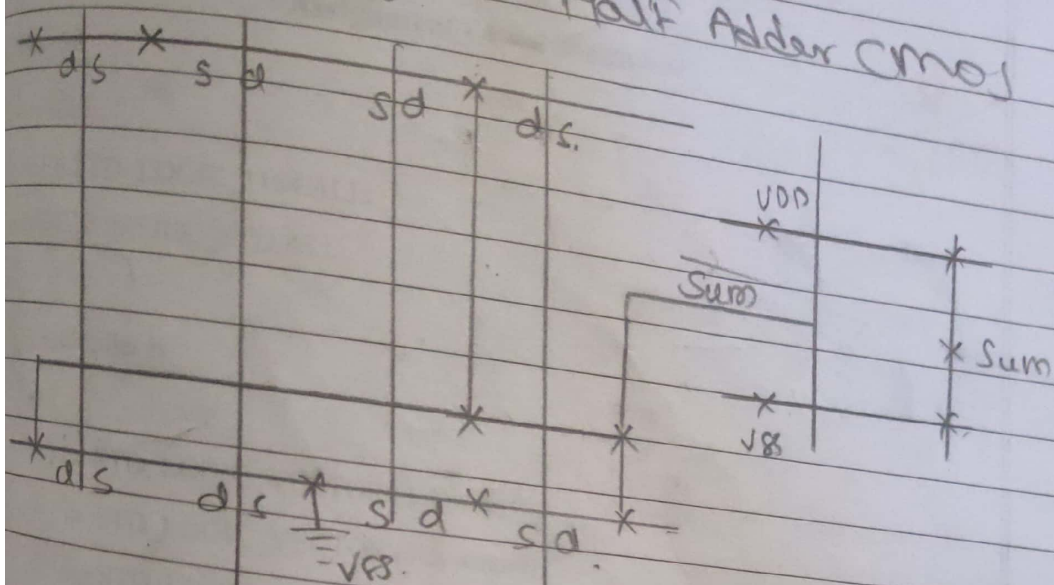
• Circuit Diagram



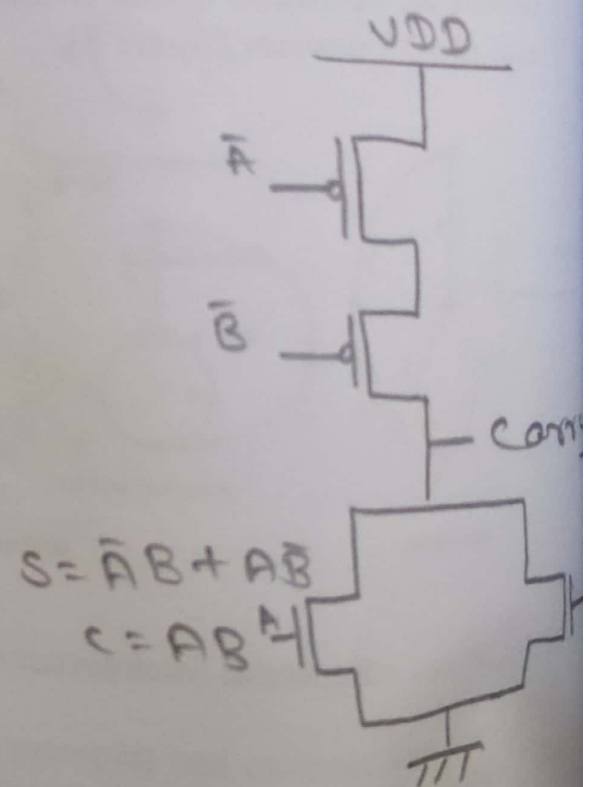
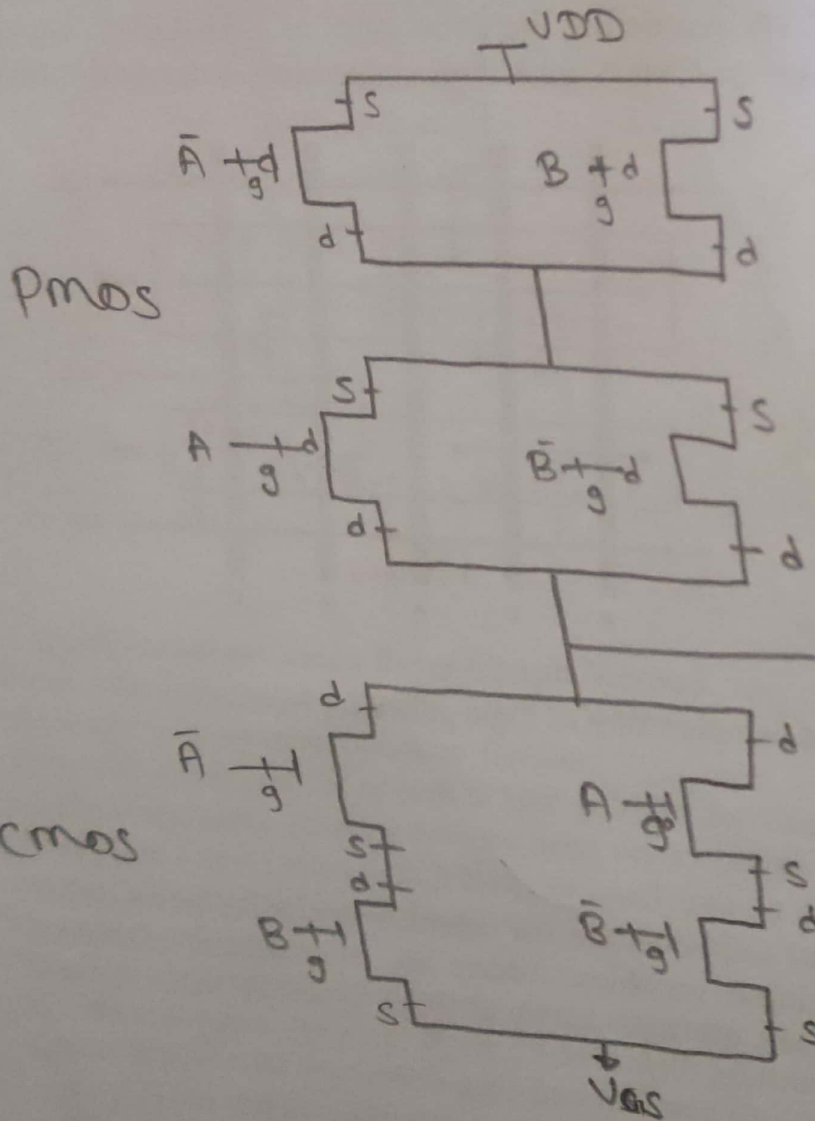
• Stick Diagram



* Stick Diagram Half Adder CMOS



Half adder CMOS and Stick Diagram



* Stick Diagram

