



Virtual Labs (Electronics), IIT Bombay

Initiative of Ministry of Human Resource Development (MHRD)
under the National Mission on Education through ICT



Form for Expression of Interest

Name of the Institute..... JABALPUR ENGINEERING COLLEGE, JABALPUR
Address..... GOKALPUR, JABALPUR (M.P.)
PIN - 482011 E-mail: <http://www.jec-jabalpur.org>
Affiliated to..... Rajiv Gandhi Proudyogiti Vishwavidyalaya, Bhopal *
AICTE/UGC approval..... F.No. 760-S1-204 (E/ET/IS)
* Academic Autonomous

User Information

- a) No. of UG students accessing the lab..... 330
b) No. of PG students accessing the lab..... 40
c) Which of the Wadhvani Virtual labs will be used..... ALL

Name of the Proposed Nodal Coordinator..... Dr. BHAVANA JHARIA
Department..... Dept. of Electronics & Communication Engg.
Area of Interest..... Electronics & VLSI Design & Technology
Contact Details..... dr.bhavana.jharia@jec-jabalpur.org, 09425467710

Name of the Second Nodal Coordinator..... Mrs. Kanchan Cecil
Department..... Dept. of Electronics & Communication Engg.
Area of Interest..... Electronics & Microcontrollers
Contact Details..... kanchan.cecil@jec-jabalpur.org; 09977863302
Name of the Director/ Principal..... Dr. P. K. Thinge

Certified that

1. The institute is recognized by AICTE/UGC.
2. We have adequate infrastructure (Dedicated space with min. 10 PCs having 1Mbps broadband connection) for accessing Virtual Labs.
3. Students will not be charged for using the Virtual Labs facility.
4. Standard laid down lab procedures will be strictly followed.
5. The technical evaluation team from IIT Bombay can visit the institute on or after 15th May/11 (date).

Date 09.03.2011


Principal
Jabalpur Engineering College
Jabalpur - 482 011 (M.P.)

1. Please fill up the following table.

| Do you have following courses in your curriculum? | Yes / No | UG / PG | Compulsory / Elective | Do you have a lab course associated? | If yes which processor / development kit / simulation software. | If no, would like to use Vlab set up to complement your theory course |
|---|----------|----------|------------------------|--------------------------------------|---|---|
| DSP | Yes | UG PG | Compulsory | Yes | MAT LABS/W | — |
| HDL/FPGA based | Yes | UG | Compulsory | Yes | SPARTAN-II Tanner, MicroWind s/w | |
| Microcontrollers | Yes | UG | Compulsory | Yes | 8051 Kit | |
| Signals and systems. | Yes | UG | Compulsory | No | — | Yes |
| Image processing | Yes | UG PG | Elective Compulsory | Yes | MATLAB s/w | Yes |
| Fibre optic communication | Yes | UG PG | Compulsory Elective | Yes — | Optical Tx & Rx kit step index Plastic fiber | yes |

2. How do you conduct the following lab courses?

(Whether using trainer kits / preassembled circuits / Bread boards)

- Electronic Devices and circuits. : Using trainer kit & Bread Boards
- Circuits and networks : Using trainer kit
- Digital circuits : Using trainer kit
- Linear Integrated circuits : Using trainer kit & Bread Boards

3. Do the students design the circuits or they are given a ready-made circuit with component values?

Students are given a ready-made designed circuit and they have to find out responses with different values of component also. Some experiments are designed by them selves.

4. Are there any experiments which you are not able to include but are parts of the university curriculum.

Mention the part / parts of lab course with reason.

- (1) DSP
 - (2) PLL
 - (3) Signals & Systems
 - (4) Noise Generation & calculation
 - (5) Analog VLSI Designing
- } subjects / Topics are introduced recently in syllabus.

5. Can the students of your institute work beyond the college hours to make use of Vlabs facility?

Yes.

6. Will you be interested in becoming a nodal center for such an activity?

Yes.