

A Report on
Two-Day Workshop on Virtual DSP Lab and
Reconfigurable Hardware Lab

Jointly organized by
Department of Electronics &
Telecommunication,
R C Patel Institute of Technology, Shirpur
And
Virtual Labs (Electronics), IIT Bombay
on
15th – 16th October 2011.

A REPORT

The Department of Electronics & Telecommunication, R C Patel Institute of Technology, Shirpur and Virtual Labs (Electronics), and IIT Bombay organized two day Workshop on Virtual DSP Lab and Reconfigurable Hardware Lab on 15th – 16th October 2011.

Inauguration :

Workshop was inaugurated at 10.30 a.m. in Library Hall of R C Patel Institute of Technology, Shirpur by Prof. Dr J. B. Patil, Principal R C Patel Institute of Technology, Shirpur and Mrs. Madhumita Date, Project Manager Virtual Lab, IIT Bombay.

Vice Principal and Head of Department of Electronics & Telecommunication Prof Dr P J Deore introduced dignitaries on the Dias to audience and Prof. Dr J. B. Patil, Principal R C Patel Institute of Technology, Shirpur welcome them all with flowers.

In this program, R C Patel Institute of Technology, Shirpur was declared as one of the Nodal Centers of Virtual Labs (Electronics) IIT Bombay.

In the presentation Mrs. Madhumita Date pointed out the need of the Virtual Lab in helping students and teachers in understanding the concepts of various experiments in engineering. She also appealed to all participants to use it and gave critical feedback on it.



Date Madam, Prof Dr J B Patil and Prof Dr P J Deore performing Saraswati Poojan



Inauguration of Two Day workshop on Virtual DSP Lab & Reconfigurable Hardware by Prof Dr J B Patil, Principal RCPIT Shirpur with Madhumita Date Madam, Project Manager, Virtual Lab, IIT Bombay.



Inauguration of Two Day workshop on Virtual DSP Lab & Reconfigurable Hardware by Prof Dr J B Patil, Principal RCPIT Shirpur with Madhumita Date Madam, Project Manager, Virtual Lab, IIT Bombay.



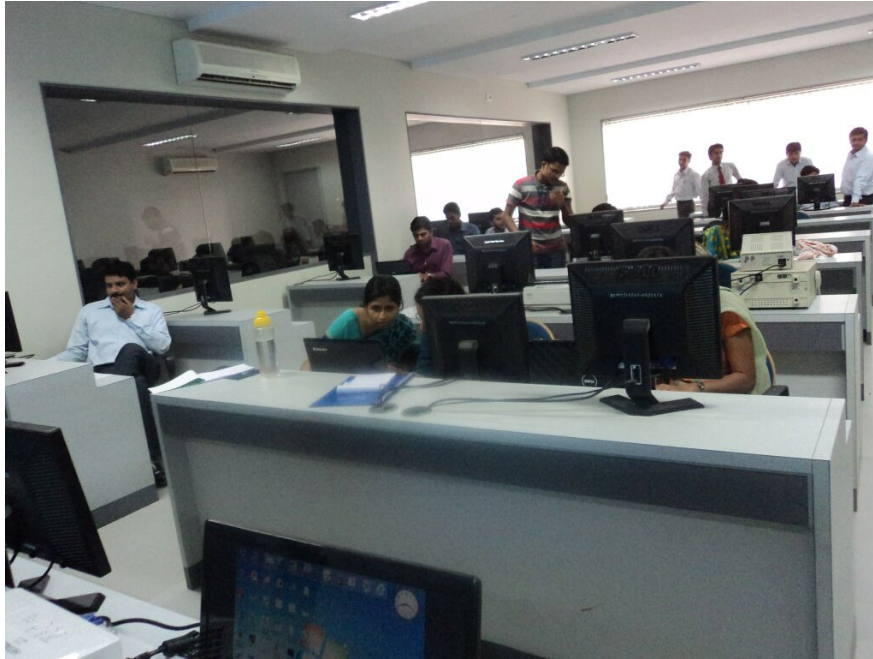




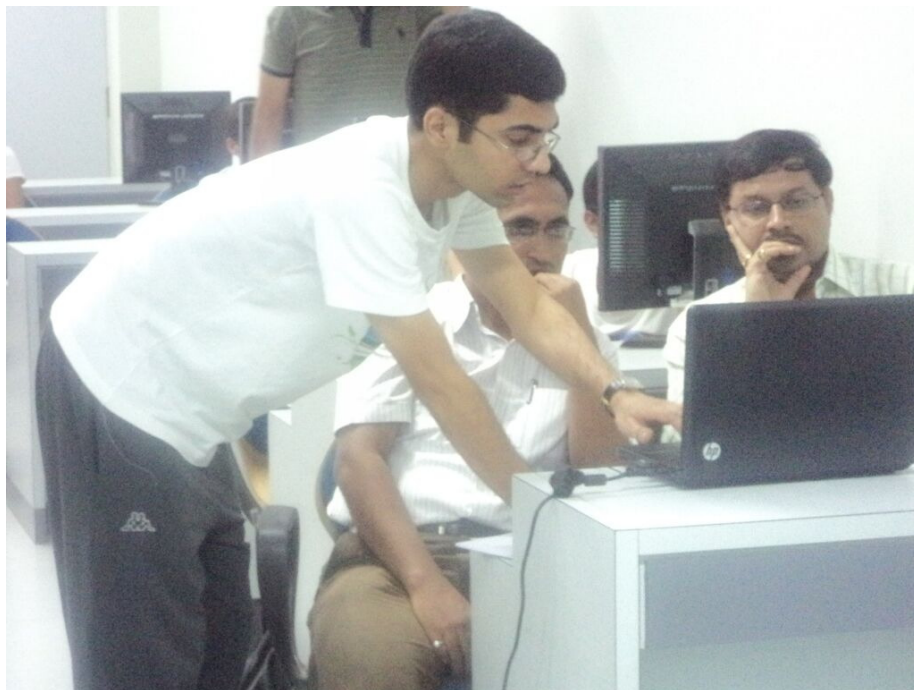
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The participants were divided in to 2 groups for Reconfigurable Hardware Lab and Virtual DSP Lab sessions. Total for 4 sessions conducted in these two days from 8.00 am morning to 6.00 pm evening.,

Total 36 participants from all over Maharashtra and other than Maharashtra State (Gujarat) benefitted. Out of 36 participants, total 26 outside participants and 10 Participants from RCPIT Shirpur participated in this workshop.



Total 19 Participants took part in Virtual DSP Lab workshop conducted by Mrs Madhumita Date, Samir Shelke, Srinivasa Rao and Narendra N, They explained installation and use of scilab, Filter design using Scilab and C, Implementation of designed filter using remote lab facility.



Similarly 17 participants took part in CPLD workshop conducted by Mrs. Madhumita Date and Debapratim Ghosh and Saurabh Agarwal. They explained the CPLD board installation, use of Altera Quartus II Development environment and complete process of programming the reconfigurable devices. CPLD boards were used by each participant and had hands on experience on it.