

To study a simple current mirror circuit

```
.include model_spice\bc547.txt
Q1 3 2 0 QBC547BP
Q2 6 2 0 QBC547BP
R1 1 2 10k
R2 4 5 5
vc1 1 0 dc 5V
vc2 4 0 dc 0v
va 2 3 dc 0v
vb 5 6 dc 0v
.dc vc2 0 5 .001

.control
run
plot i(vb) i(va)
.endc

.end
```

-----Screen Shot-----

