SHADMAN SHAHID

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CAREER SUMMARY

An engineering graduate from Bangladesh University of Engineering and Technology majoring in electronics engineering. 1 year experience of working in a photonics research lab. Interested in the research of **photonic technologies** for **state-of-the-art computational applications**. Adept in **electromagnetic FDTD technique**, **nano-electronic device modelling in MATLAB/Python**, **VLSI design tools**, and **Deep learning algorithms** Enjoys basketball, tutoring and graphic designing in spare time.

PROFESSIONAL EXPERIENCE

Nanophotonics Research Group 🗹

Department of Electrical and Electronic Engineering, Bangladesh University of Engineering and Technology (**BUET**) **RESEARCH ASSISTANT**

Dhaka, Bangladesh March 2021 – Jan 2022

- Primary research focus: Physics driven research of periodic hole array designs for Tamm State based plasmonic lasers
- Authoring funding proposals related to solar photovoltaic (PV) system optimization, floating solar PV systems and flat panel display innovation
- Management of day-to-day activities of the lab and ensuring functionality of devices and hardware.
- Mentoring undergraduate students in their thesis work.

EDUCATION

Bangladesh University of Engineering and Technology

M.Sc in Electrical and Electronic Engineering

Dhaka, Bangladesh

Ongoing

Bangladesh University of Engineering and Technology

B.Sc in Electrical and Electronic Engineering CGPA - 3.86 on a scale of 4 (Top 6%)

Dhaka, Bangladesh Graduated - Feb 2021

Dhaka, Bangladesh

Notre Dame College

HIGHER SECONDARY CERTIFICATE: GPA - 5 ON A SCALE OF 5

Graduated – Aug 2015

Dhaka, Bangladesh

RAJUK Uttara Model College

SECONDARY SCHOOL CERTIFICATE: GPA - 5 ON A SCALE OF 5

Graduated – May 2013

RESEARCH FOCUS

Photonic design optimization, || Photonic integrated circuit, || Machine learning techniques in photonic design, || Photonic inverse design, || Nanophotonics and plasmonics || Computational electromagnetics

RESEARCH PUBLICATIONS

A merged lattice metal nanohole array based dualmode plasmonic laser with an ultra-low threshold 🗷

NANOSCALE ADVANCES, ROYAL SOCIETY OF CHEMISTRY (UK)

Dec 2021

- Authors: Shadman Shahid, Shahed-E- Zumrat and Muhammad Anisuzzaman Talukder
- In this research project, dual-mode Tamm plasmon resonance observed in the interface between a multi-layer dielectric stack and a metal film with a merged lattice nanohole array is exploited for generating dual-wavelength lasing.

Dual-wavelength hybrid Tamm plasmonic laser

OPTICS EXPRESS, OPTICA PUBLISHING GROUP

Jun 2022

- Authors: Shahed-E- Zumrat, Shadman Shahid and Muhammad Anisuzzaman Talukder
- Simultaneous excitation of (hybrid) photonic and Tamm plasmonic modes/states come together in a double DBR metal structure for efficient dual-mode lasing in this research endeavour.

RESEARCH PROJECTS

An Investigation into Dual-mode Lasing Response in planar multi-layer Plasmonic laser Systems 🗹

Undergraduate Thesis Dec 2020

- Two design approaches have been proposed in order to elicit dual mode lasing response in planar multilayer plasmonic systems for nanophotonic applications.
- One of the design approaches has already been published in a research paper. The open access paper can be accessed here. Another research paper, depicting the second design approach is in the final stages of the journal publication review process.
- Supervised by: Professor Dr. Muhammad Anisuzzaman Talukder

Inverse design of thin film stacks through a generative residual global optimization network Res-GLONet 🗗 | Project Report

April 2022

• Faster optimization of a thin film dielectric stack for a given response, with the help of a generative neural network coupled to a Transfer Matrix Method (TMM) solver.

Hamming error correcting code generator and reciever ☑ | Project Report

Dec 2020

• This project shows a prototype of a single forward error correcting system based on least Hamming distance principle. The system was implemented in **verilog** via Cadence Innovus solution.

Implementing Tetris Game using Verilog | Project Report

Feb 2019

 A simplified version of the Tetris game was implemented using an FPGA (Field Programmable Gate Array) board - EPF10K70 device - a FLEX 10K device. The EPF10K70 device was programmed using Verilog HDL.

TECHNICAL SKILLS

Programming Languages Python, Verilog, C, C++, MATLAB

Database Management Software Microsoft SQL server

Tools & Softwares MATLAB, Lumerical Suite, Cadence Suite, COMSOL Multiphysics, Microsoft Excel

Deep Learning Tools Pytorch, Tensorflow, Keras, Scikit-learn, Spacy, etc

Web development frameworks Django, Pelican

Editing and Designing Softwares Origin Pro, Adobe Illustrator, Photoshop

Markup Languages △TFX

LEADERSHIP AND VOLUNTARY EXPERIENCE

IEEE BUET Student Branch 📝

CHAIRPERSON - IEEE IAS BUET SB Chapter

PROGRAM COORDINATOR

VOLUNTEER

July 2019 – Mar 2021 Sep 2017 - Jul 2019

Jan 2017 – Jul 2017

- Organized events, workshops with different student organizations under IEEE Bangladesh Section.
- Was chief instructor of workshop on Lumerical Inc. (FDTD) for undergraduate students of BUET.

Notre Dame Debating Club

Notre Dame College, Dhaka Sep 2013 - Dec 2015

ENGLISH DEBATER

- Participated and won in numerous debate competitions in school and college level.
- Served as Asst. Gen Secretary of Notre Dame Debating Club for the year 2015.
- Was the lead designer of publications, posters, logos and merchandise for the club during my tenure

AWARDS AND ACHIEVEMENTS

• Nominated in **University Dean's list** for three out of four levels of undergraduate study.

BUET, Dhaka

• University Merit Scholarship for seven out of eight terms of undergraduate study.

BUET, Dhaka

• 1st prize at Inter University Poster Presentation Contest, at Essonance 2017

IUT, Gazipur, Bangladesh.

REFERENCES

Dr. Muhammad Anisuzzaman Talukder 🔀

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