



1

2

3

4

A

A

B

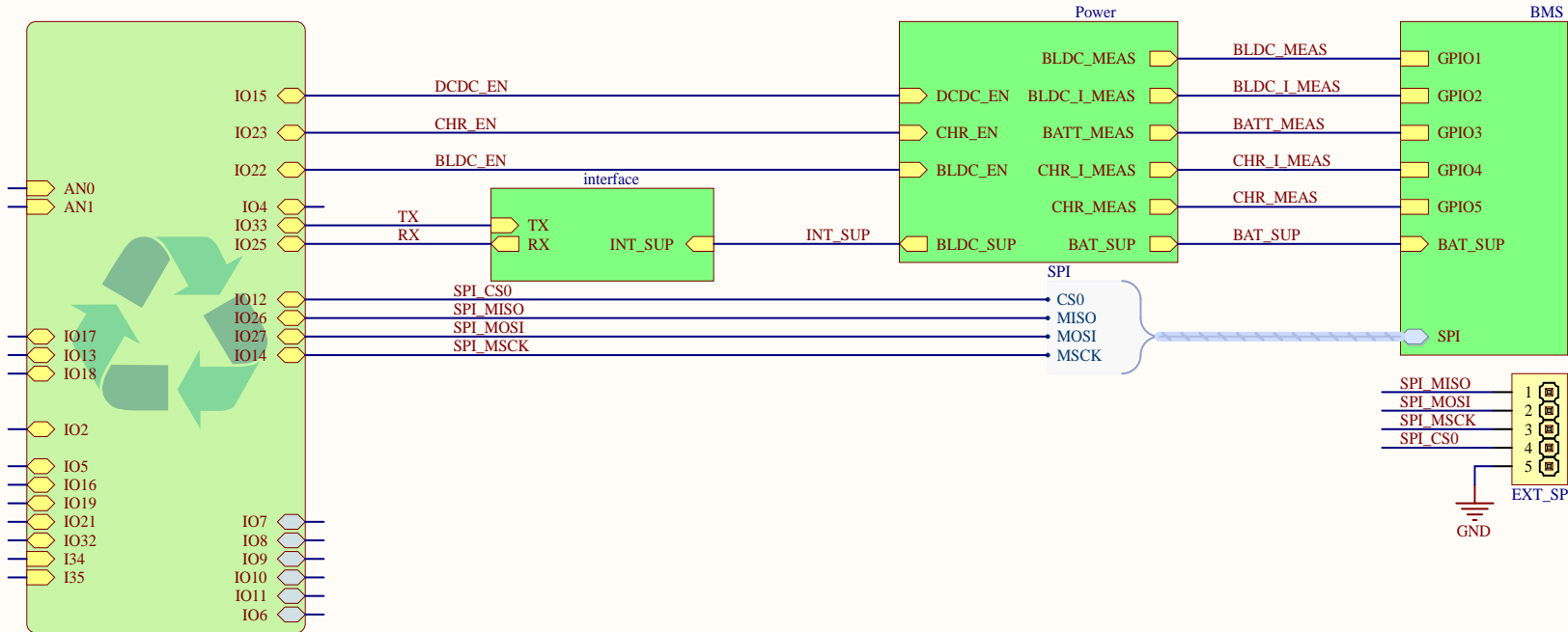
B

C

C

D

D



V1.1

LTC6804-2 pin 1 -> BATT\_POS  
R9 100 -> 10  
R23 33k -> 3k9  
remove Q3  
add D2 D3  
C8 75p -> 100n  
remove MH4

Title		
Top sheet		
Size	Number	Revision
A4		
Date:	12.28.2022	Sheet of
File:	C:\Users\...\top_sheet.SchDoc	Drawn By:

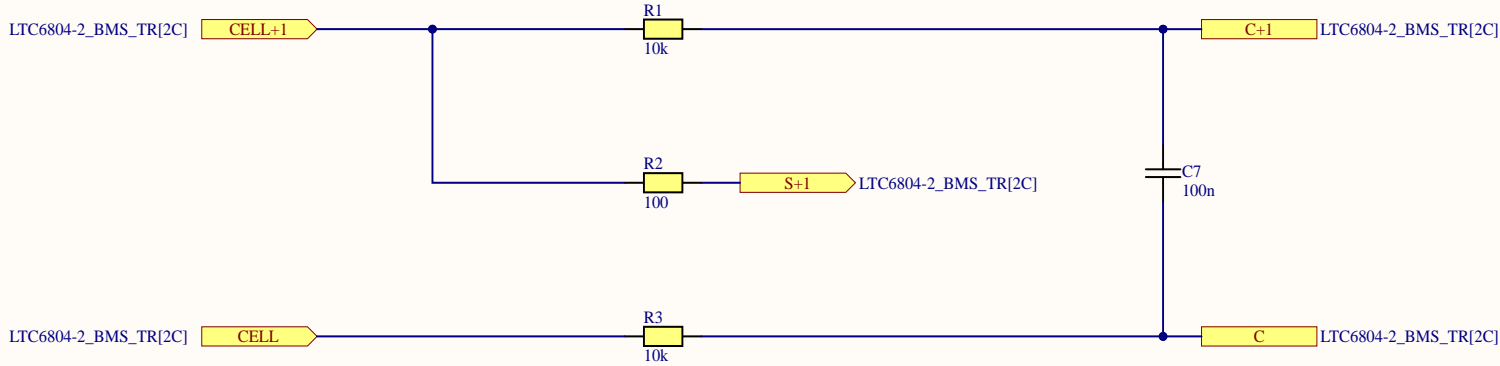
1

2

3

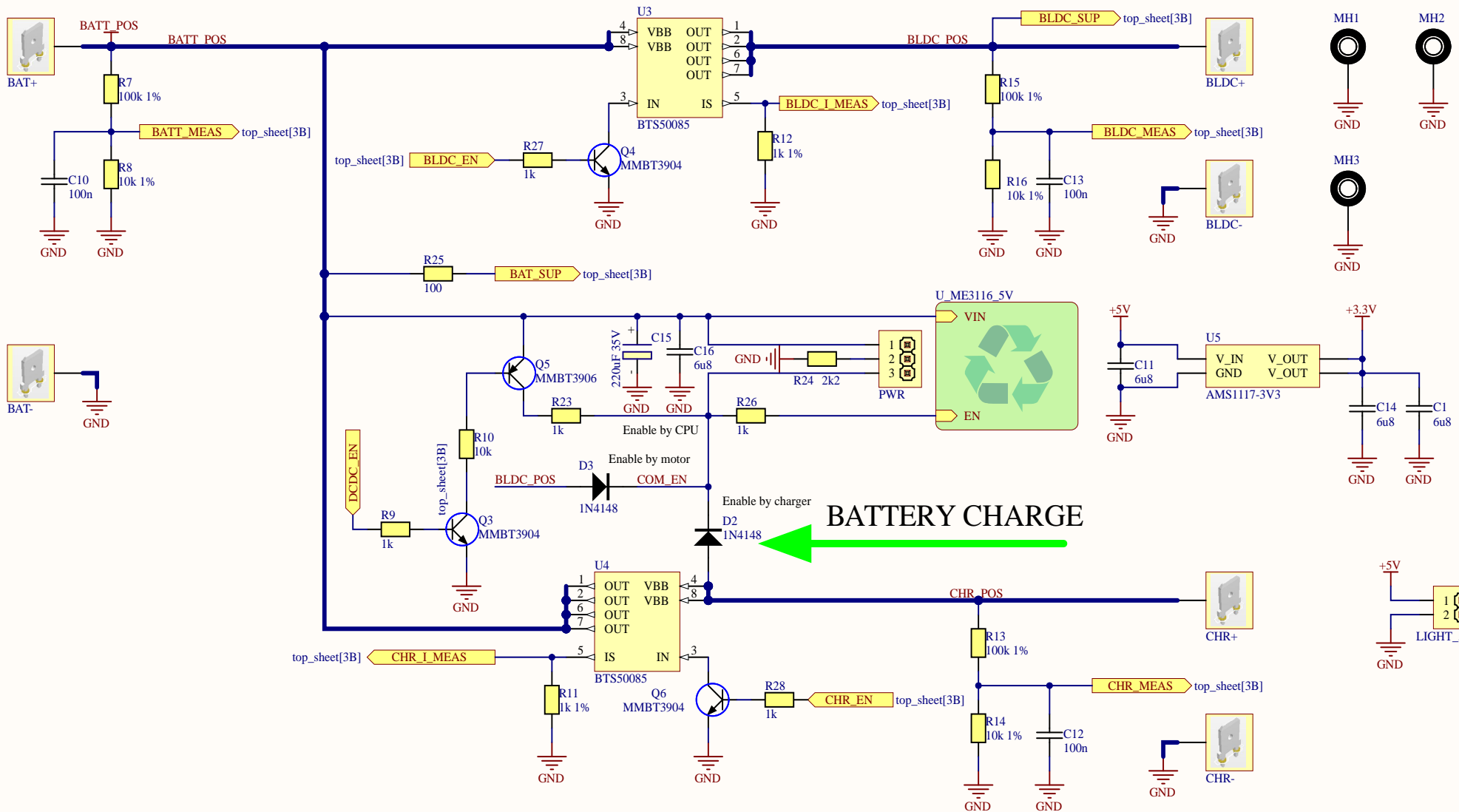
4





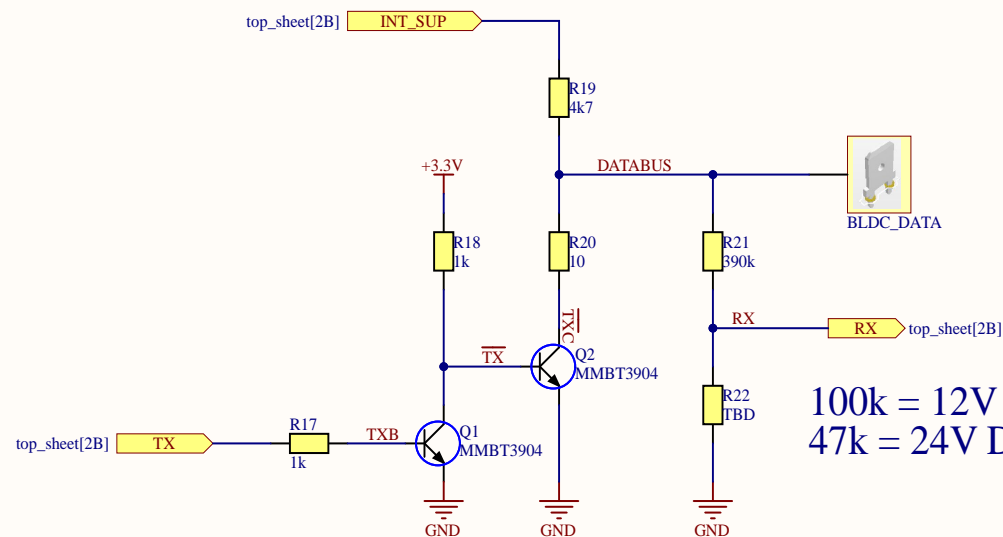
Title		
Individual cell ballancer		
Size	Number	Revision
A4		
Date:	12.28.2022	Sheet of
File:	C:\Users\...\cell_ballance_r.SchDoc	Drawn By:

# MOTOR POWER



# BATTERY CHARGE

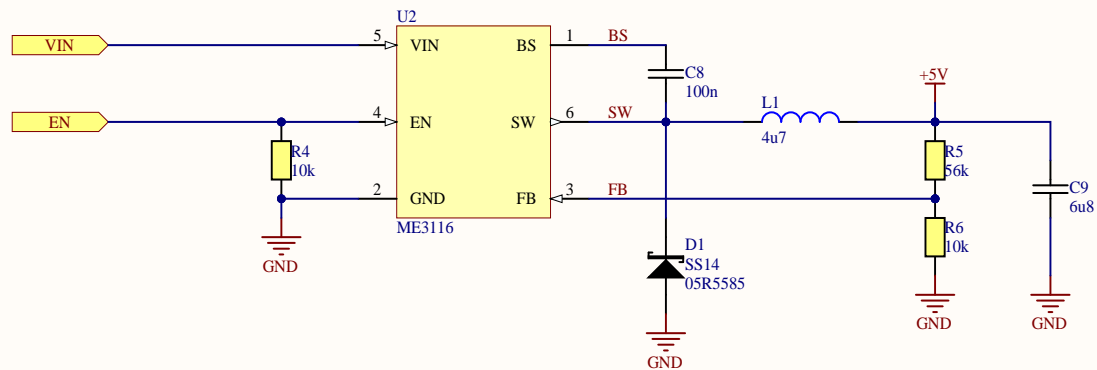
Title		
Power distribution		
Size	Number	Revision
A4		1.1
Date:	12.28.2022	Sheet of
File:	C:\Users\...\power.SchDoc	Drawn By:



100k = 12V DATABUS  
47k = 24V DATABUS

This schematic converts 3.3V full-duplex UART interface to the single K-Line operating at 9600 Hz

Title		
Interface converter		
Size A4	Number	Revision 1.0
Date:	12.28.2022	Sheet of
File:	C:\Users\...\data_interface.SchDoc	Drawn By:



Title			DC/DC Voltage converter 1A rated	
Size	Number		Revision	
A4			1.0	
Date:	12.28.2022		Sheet of	
File:	C:\altium\DeviceSheets\ME3116_5V.SchDoc		Drawn By:	

