

Assignment 1 (CS 201 P)

Objective: Design a digital circuit which can perform addition/subtraction of two 3-bit numbers. The circuits should be able to detect the overflow also.

Available IC's are : 7400 NAND (2 bit Input), 7402 NOR (2 bit Input), 7404 NOT (2 bit Input), 7408 AND (2 bit Input), 7432 OR (2 bit Input), 7486 EX-OR (2 bit Input)

Note: Come to the lab with the circuit diagram.