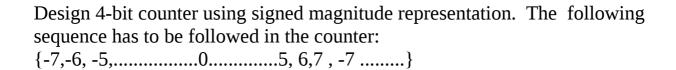
Assignment3



Plot the following waveforms: a) clock and b0 bit b) clock and b1 bit.

Assume that D-flip flop and basic gates are available.