

Course Code	Course Title	L	T	P	C
BECE204L	Microprocessors and Microcontrollers	3	0	0	3
Pre-requisite	BECE102L	Syllabus version			
		1.0			
Course Objectives:					
<div><div></div><div><div></div><div></div><div></div></div><div>1. To acquaint students with architectures of Intel microprocessors, microcontroller and ARM processors.</div><div>2. To familiarize the students with assembly language programming in 8051 microcontroller and ARM processor.</div><div>3. To interface peripherals and I/O devices with the 8051 microcontroller.</div></div>					
Course Outcome:					
<div>At the end of the course, the student should be able to</div> <div><div></div><div></div><div></div><div></div><div></div><div></div></div> <div><div>1. Comprehend the various microprocessors including Intel Pentium Processors</div><div>2. Infer the architecture and Programming of Intel 8086 Microprocessor.</div><div>3. Comprehend the architectures and programming of 8051 microcontroller.</div><div>4. Deploy the implementation of various peripherals such as general purpose input/output, timers, serial communication, LCD, keypad and ADC with 8051 microcontroller</div><div>5. Infer the architecture of ARM Processor</div><div>6. Develop the simple application using ARM processor.</div></div>					
Module:1	Overview of Microprocessors	3 hours			
Introduction to Microprocessors, 8-bit/16-bit Microprocessor, Overview of Intel Pentium, I (i3, i5, i7) Series Processor.					
Module:2	Microprocessor Architecture and Interfacing: Intel x86	8 hours			
16-bit Microprocessor: 8086 - Architecture and Addressing modes, Memory Segmentation, Instruction Set, Assembly Language Processing, Programming with DOS and BIOS function calls, minimum and maximum mode configuration, Programmable Peripheral Interface (8255), Programmable Timer Controller (8254), Memory Interface to 8086.					
Module:3	Microcontroller Architecture: Intel 8051	7 hours			
Microcontroller 8051 - Organization and Architecture, RAM-ROM Organization, Machine Cycle, Instruction set: Addressing modes, Data Processing - Stack, Arithmetic, Logical; Branching – Unconditional and Conditional, Assembly programming.					
Module:4	Microcontroller 8051 Peripherals	5 hours			
I/O Ports, Timers-Counters, Serial Communication and Interrupts.					
Module:5	I/O interfacing with Microcontroller 8051	7 hours			
LCD, LED, Keypad, Analog-to-Digital Convertors, Digital-to-Analog Convertors, Sensor with Signal Conditioning Interface.					
Module:6	ARM Processor Architecture	5 hours			
ARM Design Philosophy; Overview of ARM architecture; States [ARM, Thumb, Jazelle]; Registers, Modes; Conditional Execution; Pipelining; Vector Tables; Exception handling.					
Module:7	ARM Instruction Set	8 hours			
ARM Instruction- data processing instructions, branch instructions, load store instructions, SWI Instruction, Loading instructions, conditional Execution, Assembly Programming.					
Module:8	Contemporary issues	2 hours			

		<b>Total Lecture hours:</b>		<b>45 hours</b>	
<b>Text Book(s)</b>					
1.	A.K. Ray, K.M. Bhurchandi, Advanced Microprocessor and Peripherals, 2012, 2 <sup>nd</sup> Edition, Tata McGraw-Hill, India.				
2.	Mohammad Ali Mazidi, Janice G. Mazidi, Rolin D. McKinlay, The 8051 Microcontroller and Embedded Systems, 2014, 2 <sup>nd</sup> Edition, Pearson, India.				
<b>Reference Books</b>					
1.	Muhammad Ali Mazidi, ARM Assembly Language Programming & Architecture: 1, 2016, 2nd Edition, Microdigitaled.com				
2.	A. Nagoor Kani, 8086 Microprocessors and its Applications, 2017, Second Edition, Tata McGraw-Hill Education Pvt. Ltd., New Delhi, India.				
3.	Joseph Yiu, The Definitive Guide to ARM® Cortex®-M0 and Cortex-M0+ Processors, 2015, 2 <sup>nd</sup> Edition, Elsevier Science & Technology, UK				
Mode of Evaluation: Continuous Assessment Test, Digital Assignment, Quiz and Final Assessment Test					
Recommended by Board of Studies			14-05-2022		
Approved by Academic Council			No. 66	Date	16-06-2022