

ARMSim# - The ARM Simulator Dept. of Computer Science

FileViewCacheDebugWatchHelp

RegistersView

General PurposeFloating Point

HexadecimalUnsigned DecimalSigned Decimal

R0: 00000000R1: 00000002R2: 00000010R3: 00000002R4: 00000000R5: 00000000R6: 00000000R7: 00000000R8: 00000000R9: 00000000R10 (s1): 00000000R11 (fp): 00000000R12 (ip): 00000000R13 (sp): 00011400R14 (lr): 00000000R15 (pc): 00011400

CPSR RegisterNegative (N): 0Zero (Z): 0Carry (C): 0Overflow (V): 0IRQ Disable: 1FIQ Disable: 1Thumb (T): 0CPU Mode: System

0x000000df

CodeView

p1.o

00001000: E3A0000E mov r0, #1400001004: E3A01002 mov r1, #200001006: E0802001 add r2, r0, r10000100C: E0423000 sub r3, r2, r0

MemoryView0

OutputView

Console stdin/stdout/stderr