Microprocessor and Computer Architecture UE20CS252

4th Semester, Academic Year 2021-22

Date: 15/03/2022

Name:	SRN:	Section:
Vishwa Mehul Mehta	PES2UG20CS389	F

Title of the Program

Program Number: ____1__

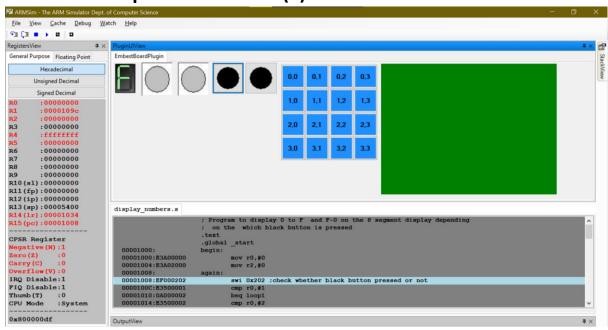
- 1. Display hexadecimal digits [0-9,A-F] on the 8 segment display.
 - 1. ARM Assembly Code(1)

Week#____7___

```
🙏 vishwa@Acer-Vishwa: /mnt/c/U 🛛 💢
26
        mov r5, #16
29
30 <u>b</u>ack2:
31
        swi 0x200 ; Set 8 segment display to light up
33
34
        cmp r5,#0
36
37
40
41 loop3:
42
        sub r4, r4, #1
        cmp r4,#0
43
44
45
        mov pc, lr
46
47
   .data
        zero: .byte 0b11101101
48
49
```

```
🙏 vishwa@Acer-Vishwa: /mnt/c/U 🛛 🗡
               bne back2
38
40
                        r4, #64000
41
42
               sub r4, r4, #1
43
                       r4,#0
44
45
               mov pc, lr
46
47
      .data
               zero: .byte 0b11101101
one: .byte 0b01100000
two: .byte 0b11001110
48
49
50
               three: .byte 0b11111010
four: .byte 0b01110011
five: .byte 0b10101011
six: .byte 0b10101111
51
52
53
54
              seven: .byte 0b111000000
eight: .byte 0b11111111
nine: .byte 0b11110111
A: .byte 0b11110111
B: .byte 0b00111111
55
56
57
58
59
60
61
               D:
62
               F:
                      .byte 0b10010111
63
```

2. Output Screen Shot (1)



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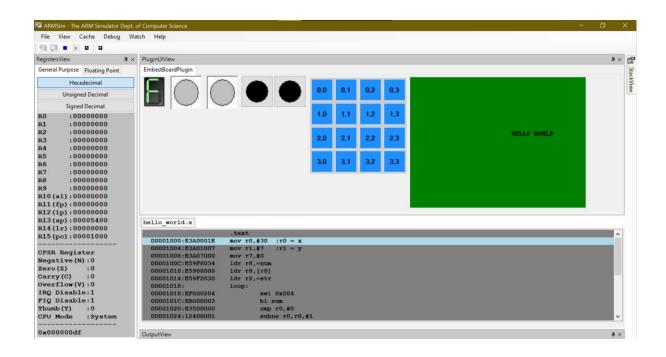
Date: 15/03/2022

Name:	SRN:	Section:
Vishwa Mehul Mehta	PES2UG20CS389	F
Week# 7	Drogram Number: 2	
vveer#/	Program Number:2_	
	Title of the Program	

- 2. Move a string from RIGHT to LEFT on the LCD display panel.
 - 1. ARM Assembly Code(1)

```
in the sum is the
```

2. Output Screen Shot (1)



Disclaimer:

- The programs and output submitted is duly written, verified and executed by me.
- I have not copied from any of my peers nor from the external resource such as internet.
- If found plagiarized, I will abide with the disciplinary action of the University.

Signature: Vishwa Mehul Mehta

Name: Vishwa Mehul Mehta

SRN: PES2UG20CS389

Section: F

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