**Microprocessor and Computer Architecture**

**UE20CS252**

**4th Semester, Academic Year 2021-22**

Date: 22/02/2022

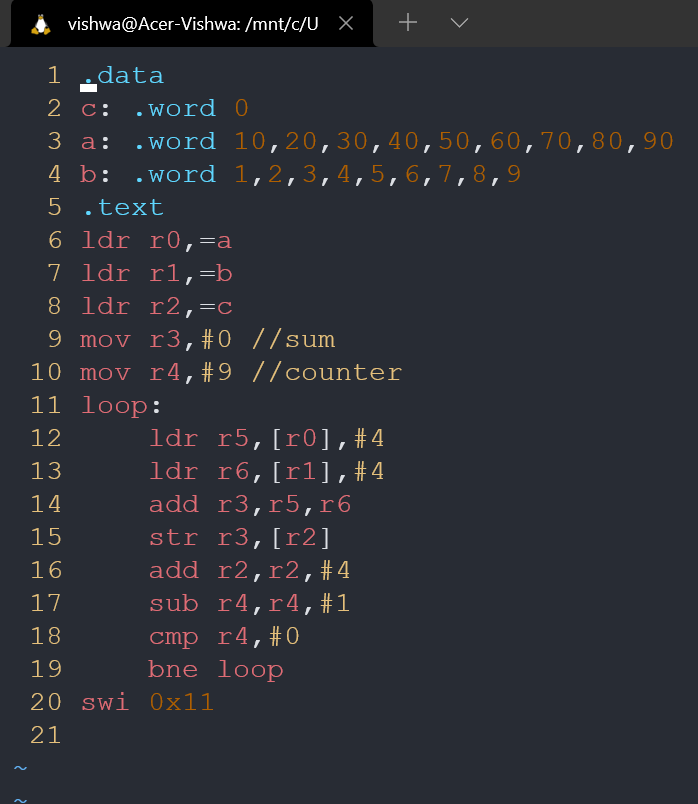
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| Name:  Vishwa Mehul Mehta | SRN:  PES2UG20CS389 | Section:  F |

Week#\_\_\_\_5\_\_\_\_\_\_\_ Program Number: \_\_\_\_1\_\_\_

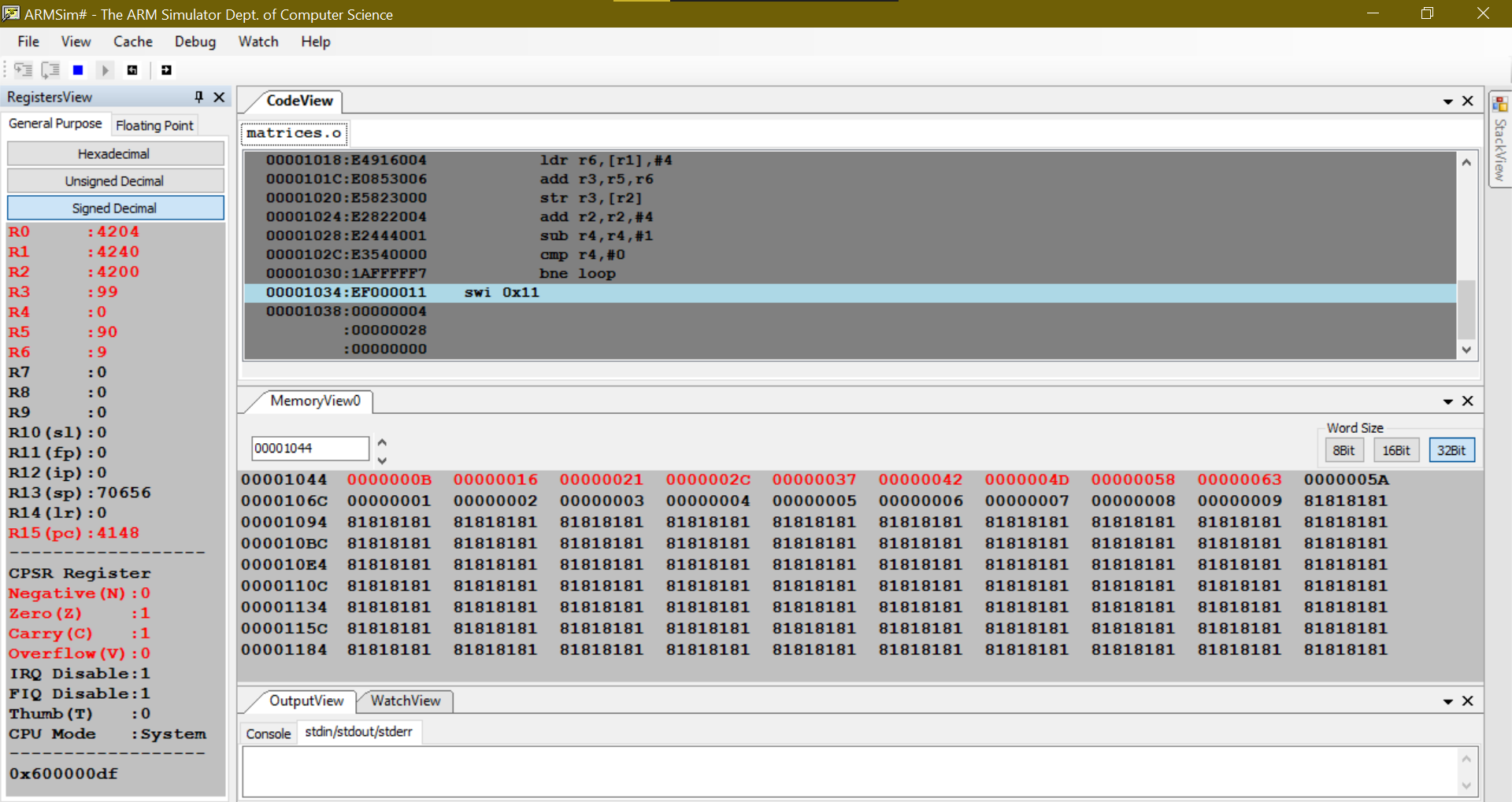
Title of the Program

**1. Write a program in ARM7TDMI-ISA to add 2 matrices of order3. i.e., Implement c[i][j] = a[i][j] + b[i][j].**

1. **ARM Assembly Code(1)**

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1. **Output Screen Shot (1)**

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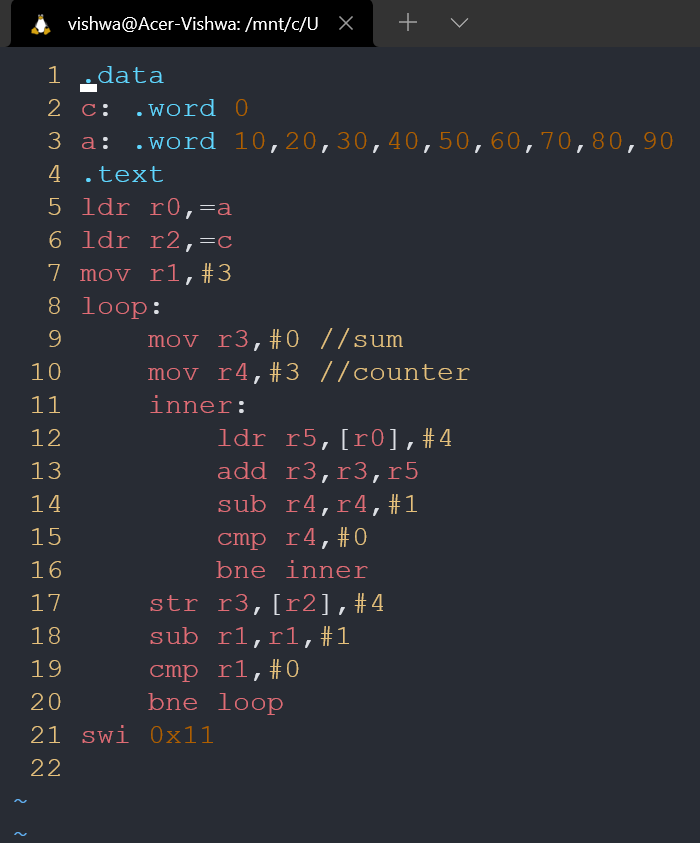
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| Name:  Vishwa Mehul Mehta | SRN:  PES2UG20CS389 | Section:  F |

Week#\_\_\_\_4\_\_\_\_\_\_\_ Program Number: \_\_\_\_2\_\_\_

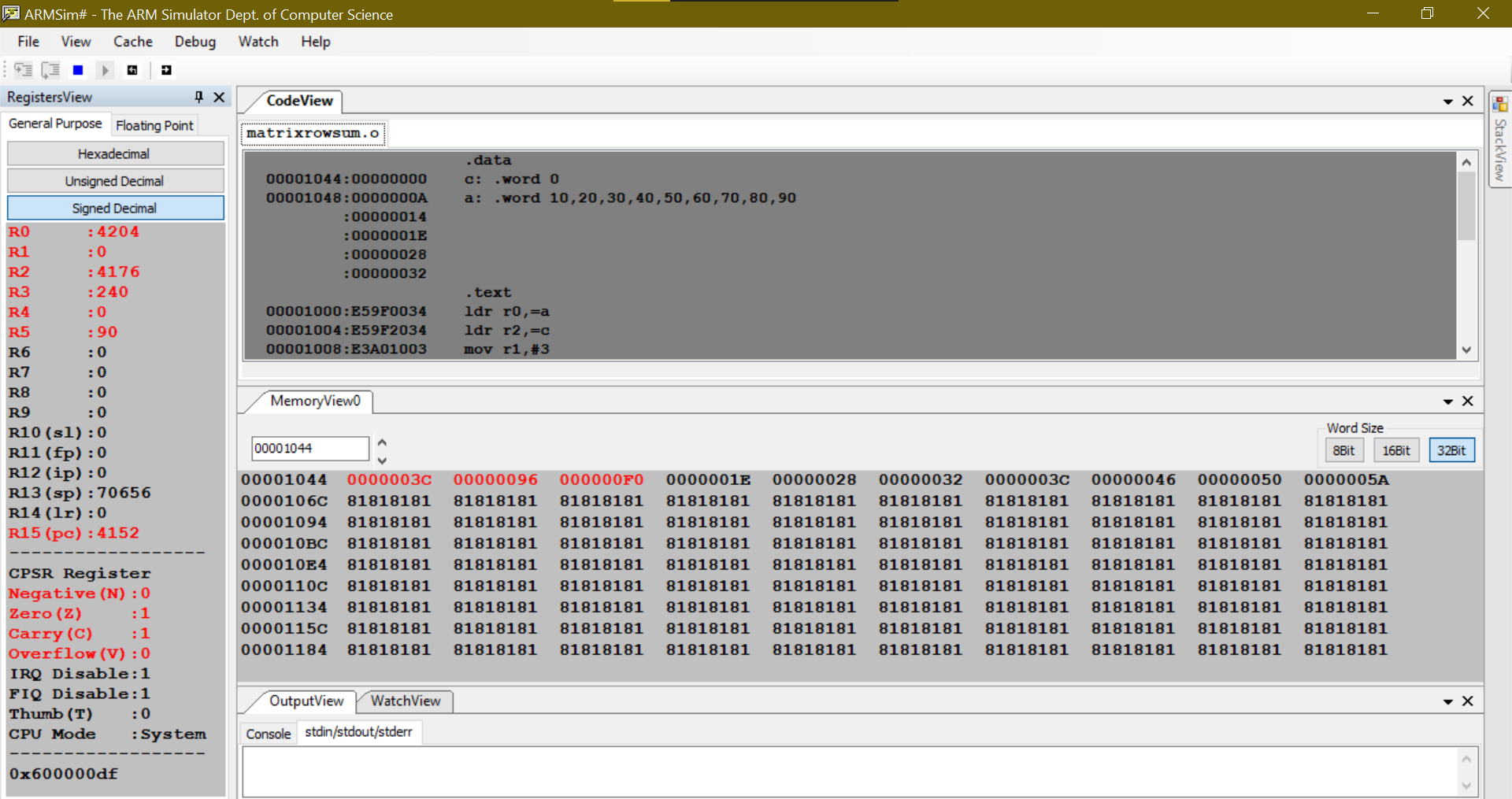
Title of the Program

**2.** **Write a program in ARM7TDMI-ISA to find the ROWSUM of a matrix.**

1. **ARM Assembly Code(1)**

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1. **Output Screen Shot (1)**

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**Disclaimer:**

* The programs and output submitted is duly written, verified and executed by me.
* I have not copied from any of my peers nor from the external resource such as internet.
* If found plagiarized, I will abide with the disciplinary action of the University.

Signature: Vishwa Mehul Mehta

Name: Vishwa Mehul Mehta

SRN: PES2UG20CS389

Section: F

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