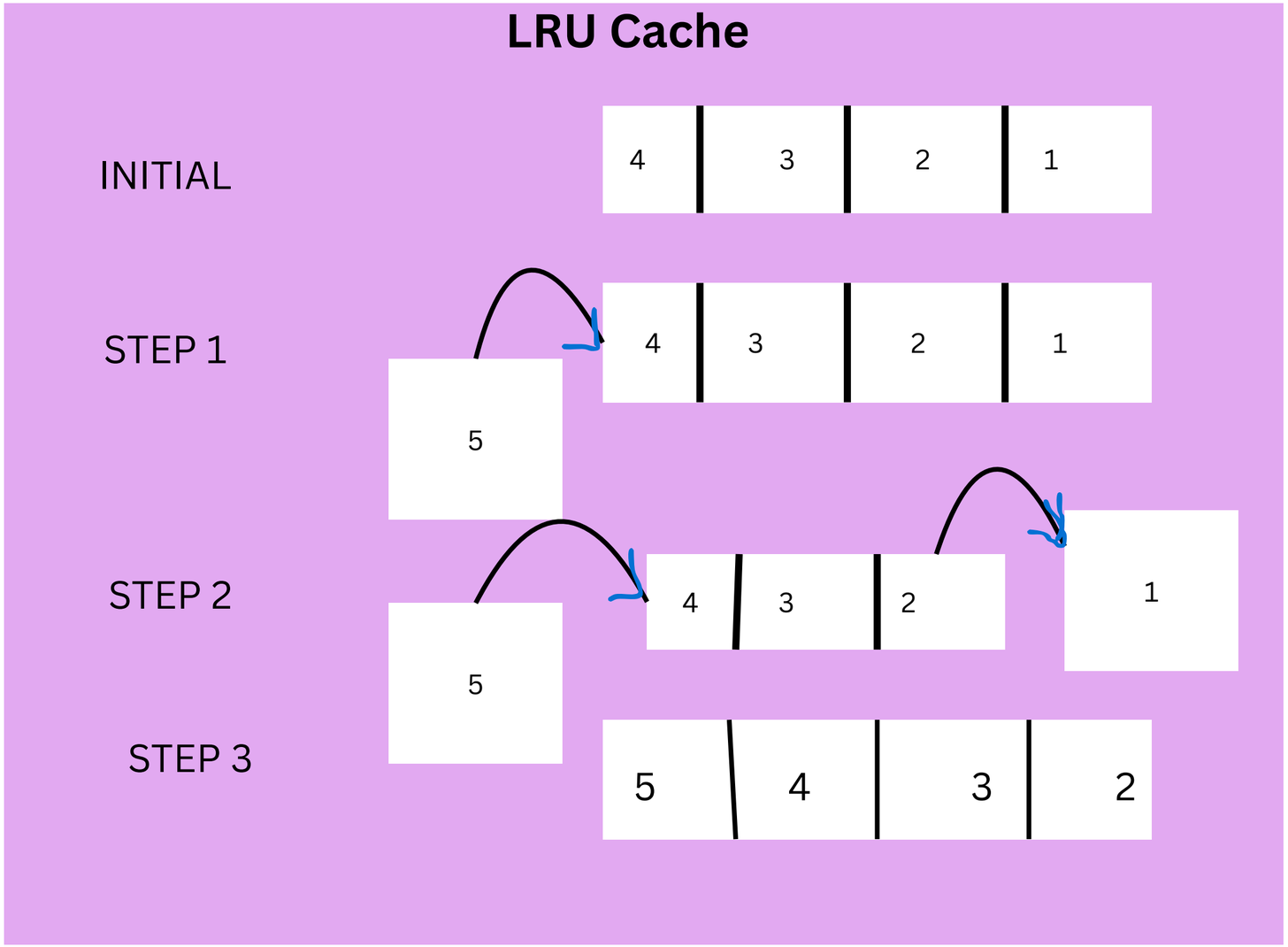
LRU Cache
INITIAL
4
3
2
1
STEP 1
4
3
2
1
5
STEP 2
4
3
2
1
5
STEP 3
5
4
3
2