

Warnings & Errors Cancelation

- **Warning (18236):** "Number of processors has not been specified which may cause overloading on shared machines. Set the global assignment NUM_PARALLEL_PROCESSORS in your QSF to an appropriate value for best performance."

Answer:

Write " set_global_assignment -name NUM_PARALLEL_PROCESSORS 4" in QSF file.

- **Warning (12125):** Using design file example.vhd, which is not specified as a design file for the current project but contains definitions for x design units and y entities in project.

Answer:

Press on Project → Add/Remove Files in Project → Add Source File → Apply → OK.

- **Critical Warning (169085):** No exact pin location assignment(s) for x pins of y total pins. For the list of pins please refer to the I/O Assignment Warnings table in the fitter report.

Answer:

We should locate all the x pins of y total pins in the fit location at "Pin Planner". There is "Fitter Location" in the Pin Planner that can help to locate at the fitter location.

- **Warning (169177):** x pins must meet Intel FPGA requirements for 3.3-, 3.0-, and 2.5-V interfaces. For more information, refer to AN 447: Interfacing Cyclone IV E Devices with 3.3/3.0/2.5-V LVTTTL/LVCMOS I/O Systems.

Answer:

There are pins that work on 2.5V (Default) like "resetrn" and "sysclk". change them to 2.5V (Default)

- **Error (12006):** Node instance "dut" instantiates undefined entity "Ram_X". Ensure that required library paths are specified correctly, define the specified entity, or change the instantiation. If this entity represents Intel FPGA or third-party IP, generate the synthesis files for the IP.

Answer:

Press on Project → Add/Remove Files in Project → Add QIP File → Apply → OK.