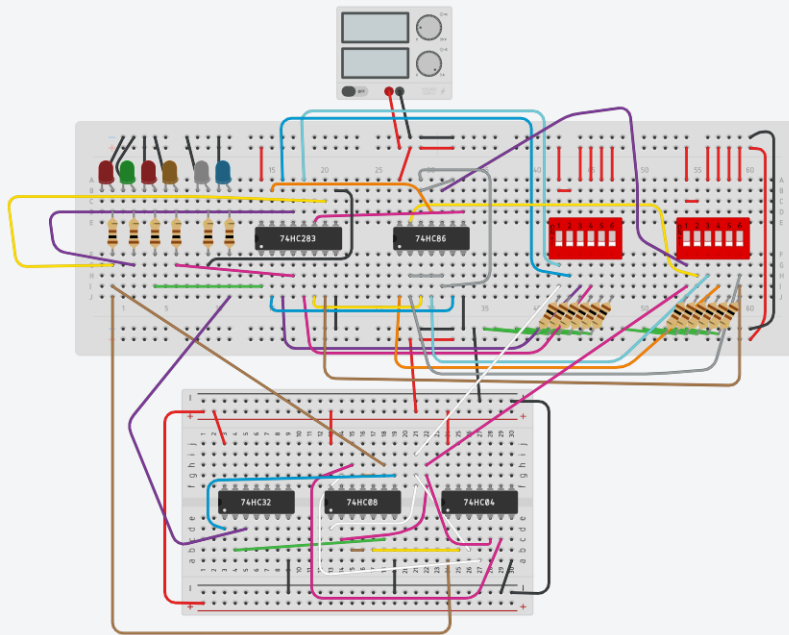
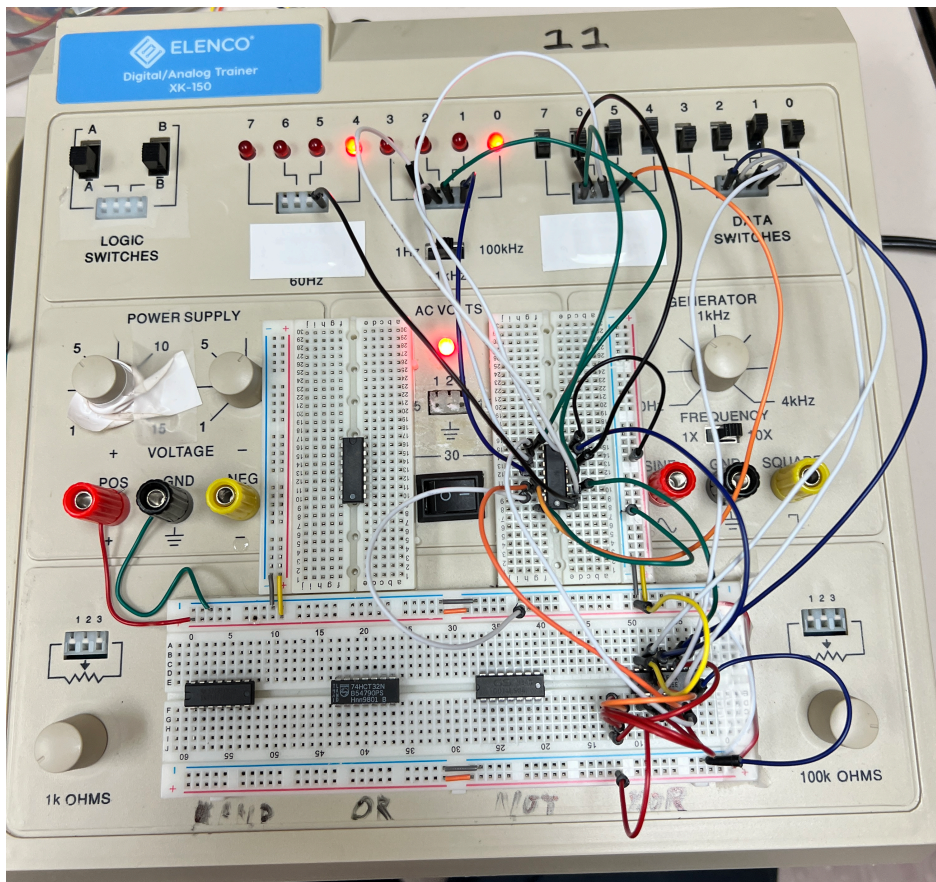
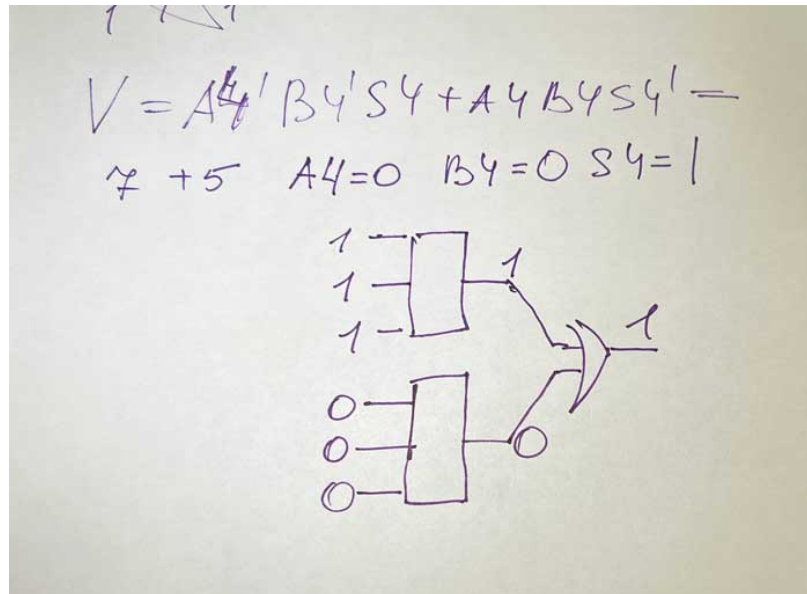


[illegible]

Control	Decimal Number	Binary Inputs	Binary Output	Decimal Output	Overflow	Why V=1?
M	A	B	A4 A3 A2 A1 B4 B3 B2 B1 C4 S4 S3 S2 S1	S	V	
0	3	2	0011 0010	00101	0	C3=0, C4=0
0	4	-7	0100 1001	01101	0	C3=0, C4=0
0	-1	-5	1111 1011	11010	0	C3=1, C4=1
0	7	5	0111 0101	01100	1	C3=1, C4=0
1	3	2	0011 0010	10001	0	C3=1, C4=1
1	-8	-6	1000 1010	01110	0	C3=0, C4=0
1	-1	5	1111 0101	11010	0	C3=1, C4=0

before current table and after current is table



It is a random picture of the board, not from calculation of -8-6