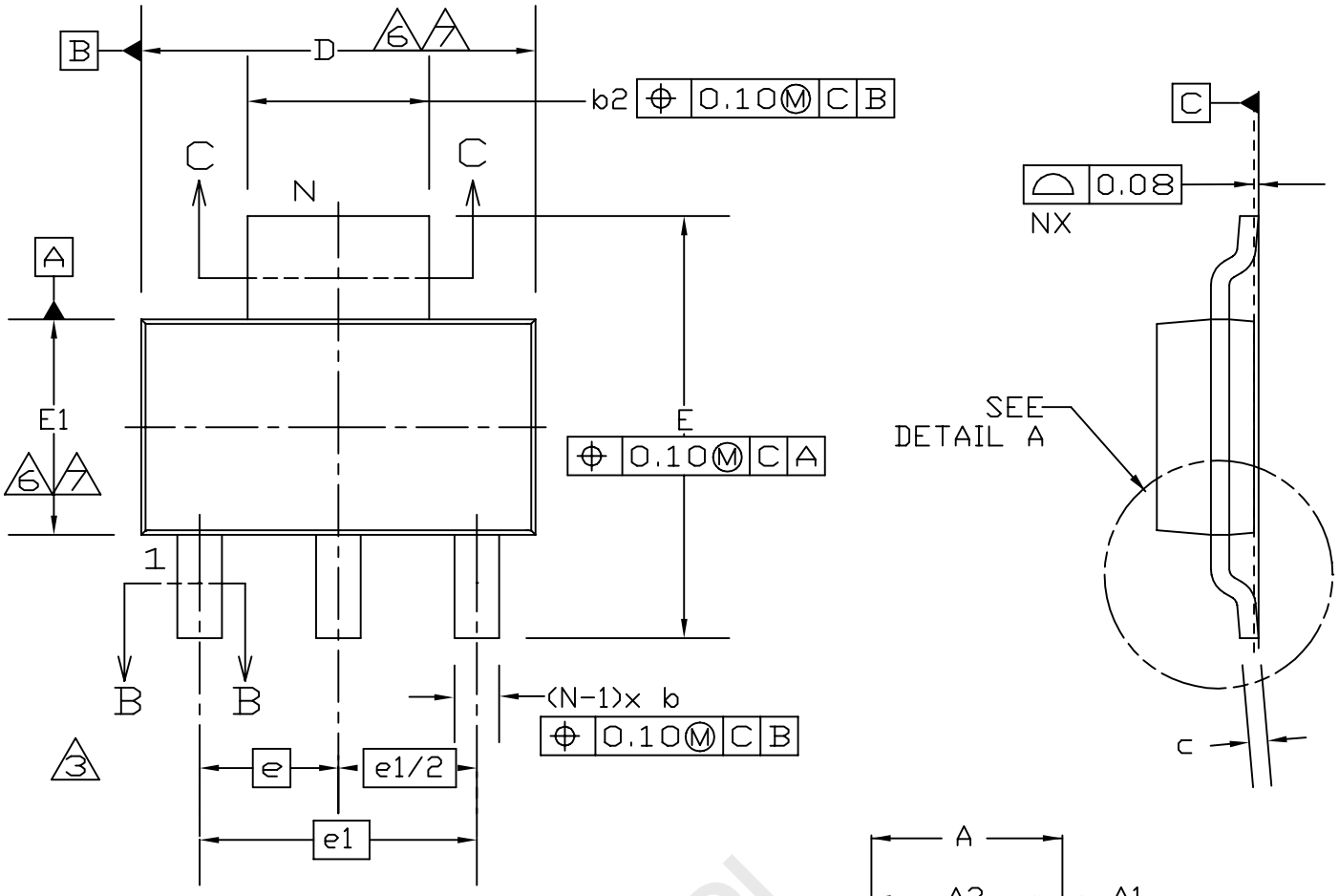
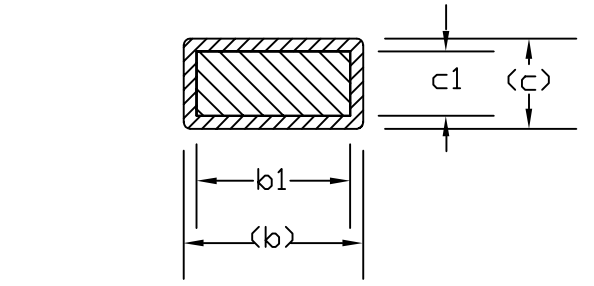
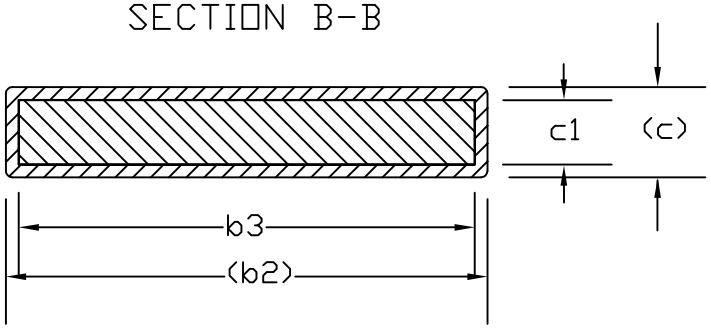
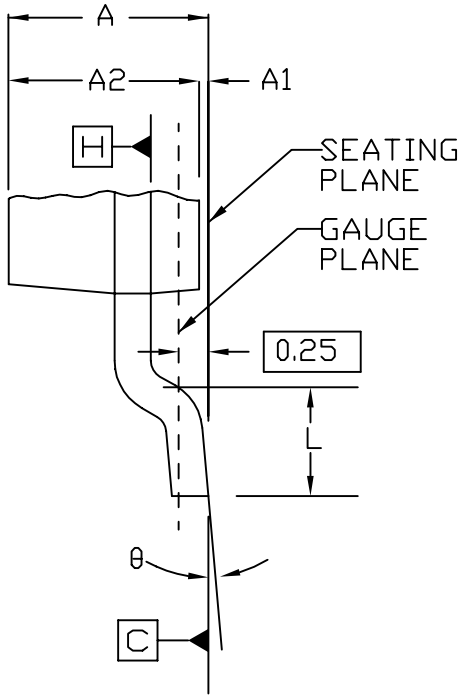




SECTION B-B



SECTION C-C



DETAIL A

JEDEC SOLID STATE PRODUCT OUTLINE	*THIS REGISTERED OUTLINE HAS BEEN PREPARED BY THE JEDEC JC-II COMMITEE AND REFLECTS A PRODUCT WITH ANTICIPATED USAGE IN THE ELECTRONICS INDUSTRY; CHANGES ARE LIKELY TO OCCUR*				
PLASTIC SMALL OUTLINE PACKAGE SOP/SOT	DESIGNATOR R-PDSO-G	ISSUE C	DATE 05/02	TO-261	SHEET 1 OF 3

VARIATIONS

SYMBOL	DIMENSIONS IN MILLIMETERS							
	AA				AB			
	MIN	*NOM*	*MAX*	NOTE	*MIN*	*NOM*	*MAX*	NOTE
A	–	–	1.80	–	–	–	1.80	–
A1	0.02	–	0.10	–	0.02	–	0.10	–
A2	1.50	1.60	1.70	–	1.50	1.60	1.70	–
b	0.66	0.76	0.84	4	0.66	0.76	0.84	4
b1	0.60	0.71	0.79	4	0.60	0.71	0.79	4
b2	2.90	3.00	3.10	4	2.90	3.00	3.10	4
b3	2.84	2.95	3.05	4	2.84	2.95	3.05	4
c	0.23	0.30	0.35	4	0.23	0.30	0.35	4
c1	0.23	0.28	0.33	4	0.23	0.28	0.33	4
D	6.30	6.50	6.70	6,7	6.30	6.50	6.70	6,7
E	6.70	7.00	7.30	–	6.70	7.00	7.30	–
E1	3.30	3.50	3.70	6,7	3.30	3.50	3.70	6,7
e	2.30 BASIC			3	1.50 BASIC			3
e1	4.60 BASIC			3	4.50 BASIC			3
L	0.75	–	–	–	0.75	–	–	–
N		4		5		5		5
θ	0°	–	10°	–	0°	–	10°	–
NOTES	1,2				1,2			
REF	10-416				10-416			
ISSUE	C				C			

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.

2. DIMENSIONS ARE IN MILLIMETERS.

3. ONLY THE FOUR LEAD VARIATION SHOWN FOR REFERENCE..

4. DIMENSIONS b1, b3, AND c1 APPLY TO BASE METAL ONLY.
   DIMENSIONS b, b2, AND c APPLY TO PLATED LEAD.
   THESE DIMENSIONS APPLY TO THE FLAT SECTION OF THE LEAD
   BETWEEN 0.08 mm AND 0.15 mm FROM THE LEAD TIP.

5. N IS THE MAXIMUM LEAD COUNT WHICH INCLUDES THE WIDE LEAD.

6. DIMENSIONS D AND E1 ARE DETERMINED AT THE OUTERMOST EXTREMES
   OF THE PLASTIC BODY EXCLUSIVE OF MOLD FLASH, TIE BAR BURRS,
   GATE BURRS, AND INTERLEAD FLASH, BUT INCLUDING ANY MISMATCH
   BETWEEN THE TOP AND BOTTOM OF THE PLASTIC BODY.

7. DATUMS A AND B ARE TO BE DETERMINED AT DATUM H.

JEDEC SOLID STATE PRODUCT OUTLINE	PLASTIC SMALL OUTLINE PACKAGE SOP/SOT	ISSUE C	DATE 05/02	TO-261	SHEET 3 OF 3