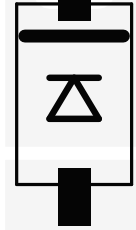


Title CTRL.SchDoc			
Product: DEV.Tnn.BRD			
Project: CTRL.PrjPcb			
Variant: [No Variations]			
Size: A4	Authors: Vitech	Revision: v1	
Date: 14.03.2025	Time: 23:19:14	Sheet 1 of 1	
Commit:			

C1
0.1uF

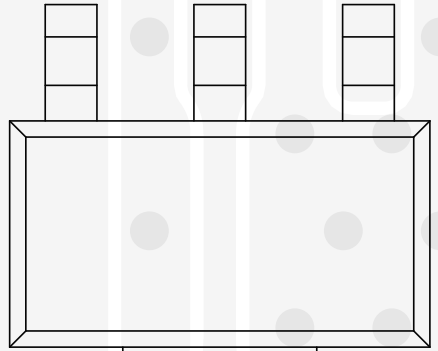
FB1
DEM10K0331SH1D



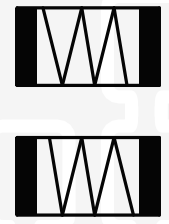
VD2
1N5819HW



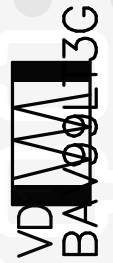
DA1
BD1117STR



R2
240

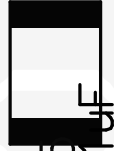


R9
1k



C3
10uF

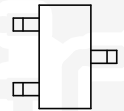
R7
27



C5
0.1uF



C4
10uF



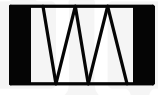
VD1
43ZE11G



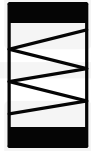
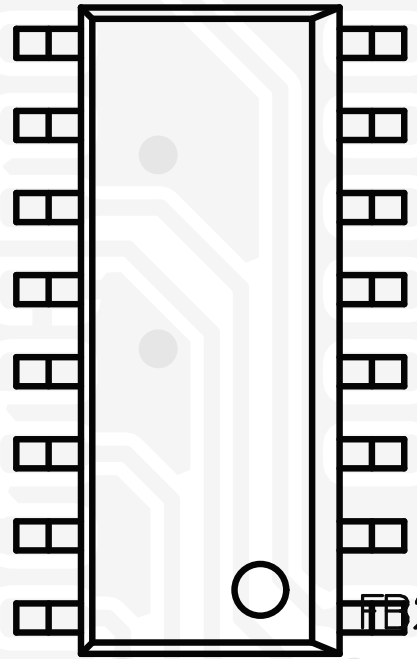
R3
1k



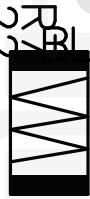
R2
240



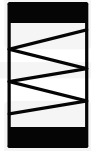
DA2
UC3525



R4
1k



R2
240



R1
1k



C6
0.1uF



R5
120

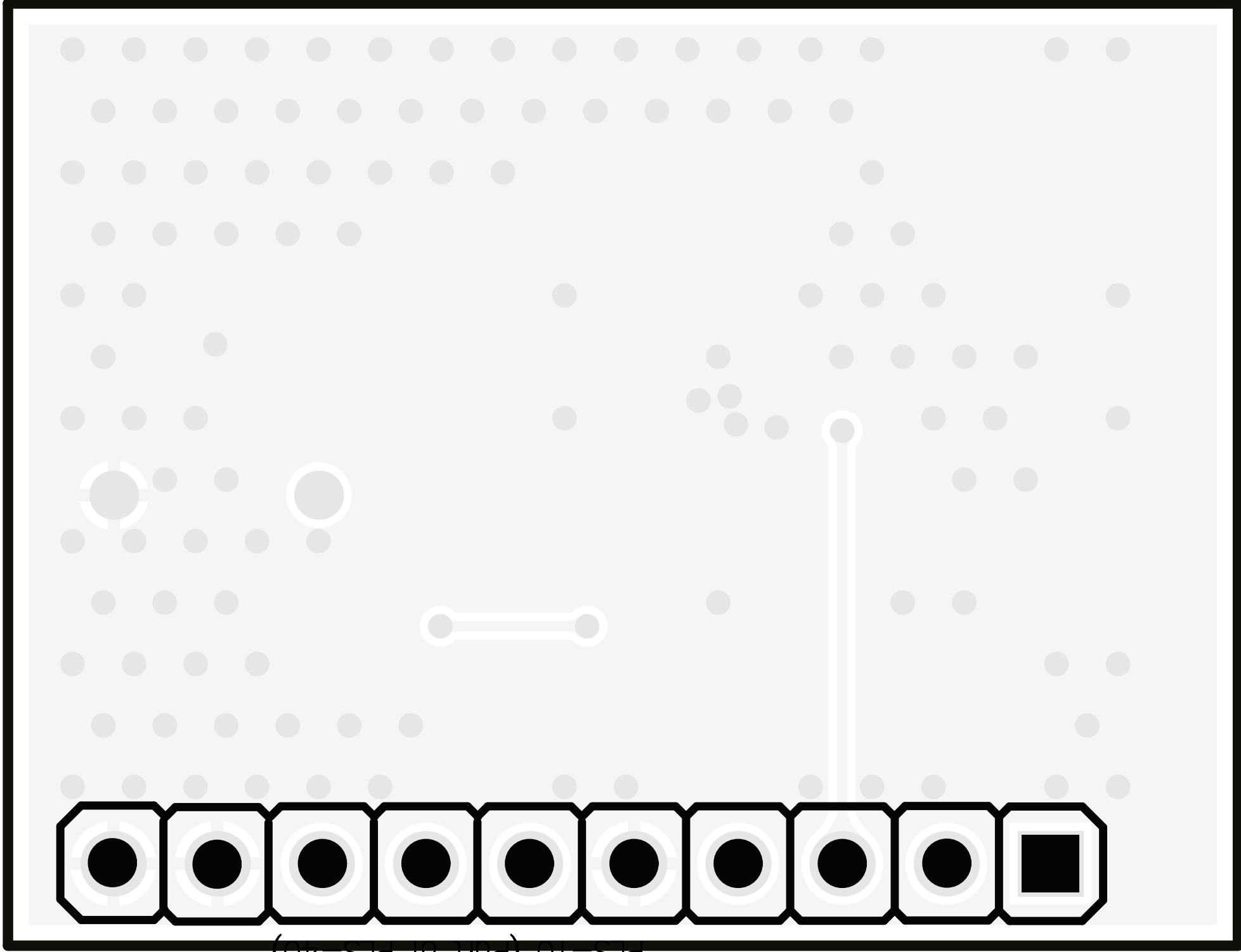


R6
15k



R5
120

R6
15k



X1
PLS 10 (Part of PLS 10)

SG3525_CTRL.V1.0
Vitech-ua

03.2025

