

IGBT

SGH80N60UFD

Ultrafast IGBT

General Description

Fairchild's UFD series of Insulated Gate Bipolar Transistors (IGBTs) provides low conduction and switching losses. The UFD series is designed for applications such as motor control and general inverters where high speed switching is a required feature.

Features

- High speed switching
- Low saturation voltage : $V_{CE(sat)} = 2.1 \text{ V } @ I_C = 40 \text{A}$
- · High input impedance
- CO-PAK, IGBT with FRD : t_{rr} = 50ns (typ.)

Applications

AC & DC motor controls, general purpose inverters, robotics, and servo controls.





Absolute Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Description		SGH80N60UFD	Units	
V _{CES}	Collector-Emitter Voltage		600	V	
V _{GES}	Gate-Emitter Voltage		± 20	V	
	Collector Current	@ T _C = 25°C	80	Α	
I _C	Collector Current	@ T _C = 100°C	40	Α	
I _{CM (1)}	Pulsed Collector Current		220	Α	
I _F	Diode Continuous Forward Current	@ T _C = 100°C	25	Α	
I _{FM}	Diode Maximum Forward Current		280	Α	
P_{D}	Maximum Power Dissipation	@ T _C = 25°C	195	W	
	Maximum Power Dissipation	@ T _C = 100°C	78	W	
T _J	Operating Junction Temperature		-55 to +150	°C	
T _{stg}	Storage Temperature Range		-55 to +150	°C	
T _L	Maximum Lead Temp. for Soldering Purposes,/8" from Case for 5 Seconds		300	°C	

Notes:(1) Repetitive rating: Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Units
$R_{\theta JC}(IGBT)$	Thermal Resistance, Junction-to-Case		0.64	°C/W
R _{0,JC} (DIODE) Thermal Resistance, Junction-to-Case			0.83	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient		40	°C/W

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Cha	racteristics					
BV _{CES}	Collector-Emitter Breakdown Voltage	V _{GE} = 0V, I _C = 250uA	600			V
ΔB _{VCES} / ΔT _J	Temperature Coefficient of Breakdown Voltage	V _{GE} = 0V, I _C = 1mA		0.6		V/°C
I _{CES}	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0V$			250	uA
I_{GES}	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$			± 100	nA
On Chai	racteristics					
V _{GE(th)}	G-E Threshold Voltage	$I_C = 40 \text{mA}, V_{CE} = V_{GE}$	3.5	4.5	6.5	V
	Collector to Emitter	$I_C = 40A$, $V_{GE} = 15V$		2.1	2.6	V
$V_{CE(sat)}$	Saturation Voltage	$I_C = 80A$, $V_{GE} = 15V$		2.6		V
•	C Characteristics Input Capacitance		T	2790		nE
C _{ies}	• • • • • • • • • • • • • • • • • • • •	$V_{CE} = 30V_{V_{GE}} = 0V_{V_{CE}}$				pF
C _{oes}	Output Capacitance Reverse Transfer Capacitance	f = 1MHz		350 100		pF pF
	ng Characteristics	T		22		
t _{d(on)}	Turn-On Delay Time	-		23		ns
t _r	Rise Time	.,		50	120	ns
t _{d(off)}	Turn-Off Delay Time Fall Time	$V_{CC} = 300 \text{ V}, I_{C} = 40\text{A},$		90 50	130 150	ns
t _f	Turn-On Switching Loss	$R_G = 5\Omega$, $V_{GE} = 15V$, Inductive Load, $T_C = 25^{\circ}C$		570		ns uJ
E _{on}	Turn-Off Switching Loss			590		uJ
E _{ts}	Total Switching Loss	-		1160	1500	uJ
t _{d(on)}	Turn-On Delay Time			30		ns
t _r	Rise Time			55		ns
t _{d(off)}	Turn-Off Delay Time	$V_{CC} = 300 \text{ V}, I_{C} = 40\text{A},$		150	200	ns
t _f	Fall Time	$R_G = 5\Omega$, $V_{GE} = 15V$,		160	250	ns
E _{on}	Turn-On Switching Loss	Inductive Load, T _C = 125°C		630		uJ
E _{off}	Turn-Off Switching Loss			940		uJ
E _{ts}	Total Switching Loss			1580	2000	uJ
Q_g	Total Gate Charge	$V_{CE} = 300 \text{ V}, I_{C} = 40 \text{A},$		175	250	nC
Q _{ge}	Gate-Emitter Charge	$V_{CE} = 300 \text{ V}, I_{C} = 40\text{A},$ - $V_{GF} = 15\text{V}$		25	40	nC
Q _{gc}	Gate-Collector Charge	·GE 101		60	90	nC
∽gc	Internal Emitter Inductance	Measured 5mm from PKG		14		nH

Electrical Characteristics of DIODE $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions		Min.	Тур.	Max.	Units	
V	Diode Forward Voltage	I _F = 25A	$T_C = 25^{\circ}C$		1.4	1.7	V	
V_{FM}	Diode Forward Voltage	IF = 25A	T _C = 100°C		1.3] v	
	Diode Reverse Recovery Time		$T_C = 25^{\circ}C$		50	95	no	
t _{rr}			T _C = 100°C		105		ns	
	Diode Peak Reverse Recovery	I _F = 25A,	$T_C = 25^{\circ}C$		4.5	10	Α	
^I rr	Current	di/dt = 200A/us	T _C = 100°C		8.5		A	
	Q _{rr} Diode Reverse Recovery Charge	Diada Dawara Daaswar Charra		$T_C = 25^{\circ}C$		112	375	
Q _{rr}			T _C = 100°C		420		nC	

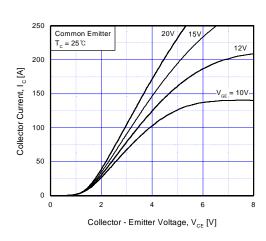


Fig 1. Typical Output Characteristics

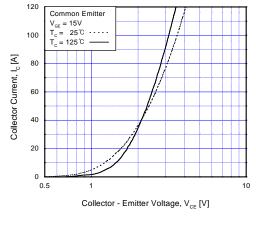


Fig 2. Typical Saturation Voltage Characteristics

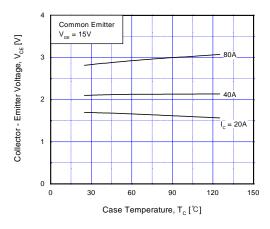


Fig 3. Saturation Voltage vs. Case
Temperature at Variant Current Level

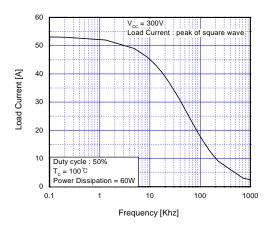


Fig 4. Load Current vs. Frequency

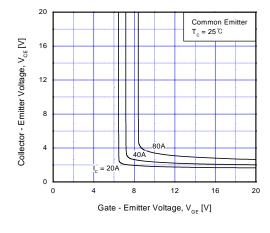


Fig 5. Saturation Voltage vs. V_{GE}

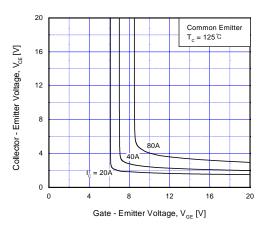
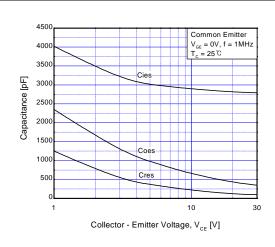


Fig 6. Saturation Voltage vs. $V_{\rm GE}$

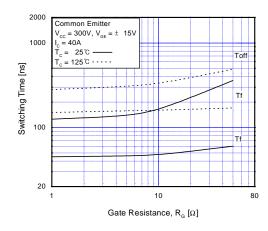
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Common Emitter $V_{cc} = 300V, V_{cg} = \pm 15V$ $V_{cc} = 40A$ $V_{cc} = 25\,^\circ\text{C}$ $V_{cc} = 125\,^\circ\text{C}$ $V_{$

Fig 7. Capacitance Characteristics

Fig 8. Turn-On Characteristics vs.
Gate Resistance



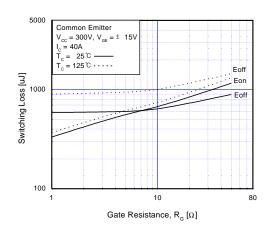
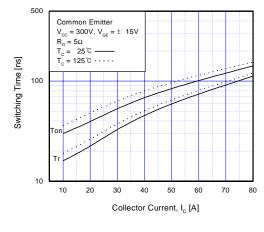


Fig 9. Turn-Off Characteristics vs.
Gate Resistance

Fig 10. Switching Loss vs. Gate Resistance



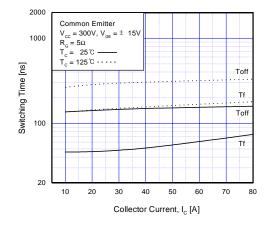
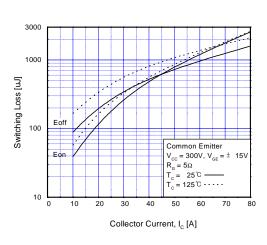


Fig 11. Turn-On Characteristics vs. Collector Current

Fig 12. Turn-Off Characteristics vs.
Collector Current



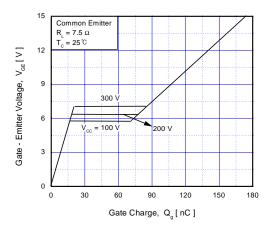
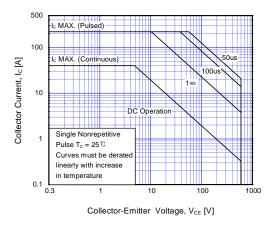


Fig 13. Switching Loss vs. Collector Current

Fig 14. Gate Charge Characteristics



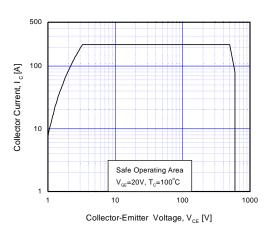


Fig 15. SOA Characteristics

Fig 16. Turn-Off SOA Characteristics

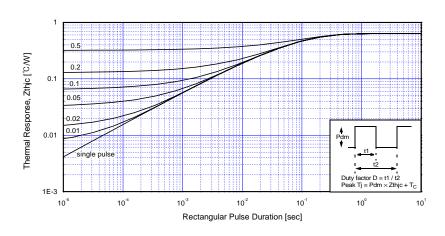
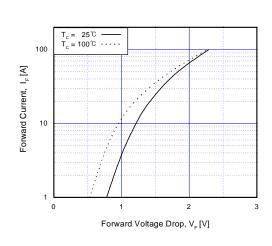


Fig 17. Transient Thermal Impedance of IGBT



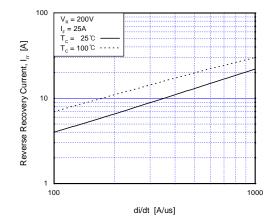
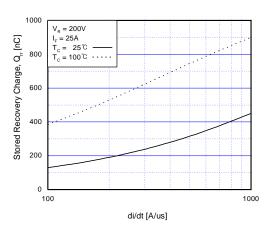


Fig 18. Forward Characteristics

Fig 19. Reverse Recovery Current



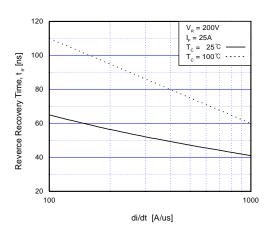
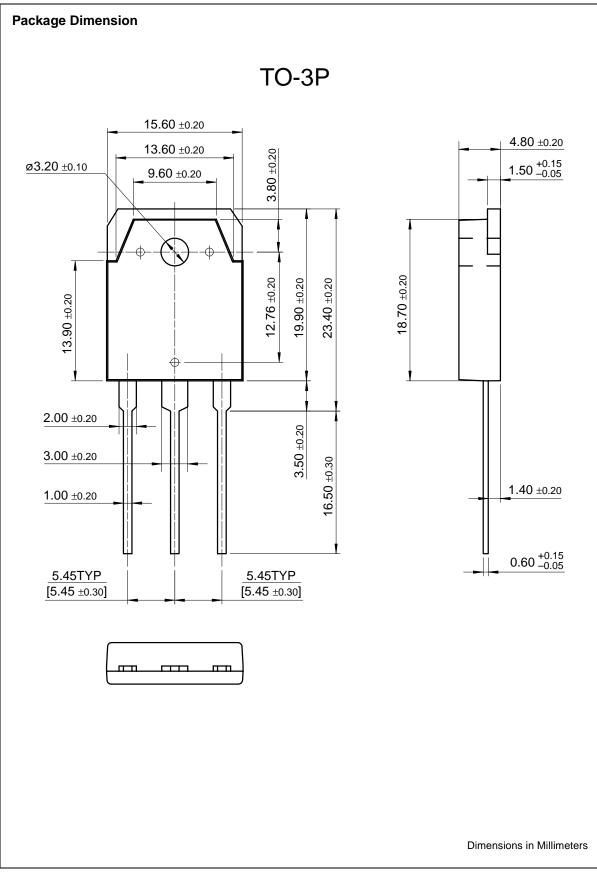


Fig 20. Stored Charge

Fig 21. Reverse Recovery Time



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