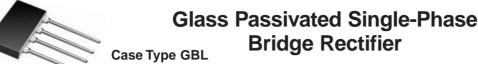
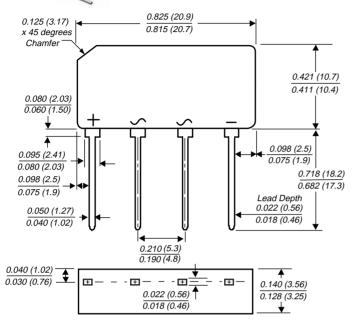


Vishay Semiconductors formerly General Semiconductor



Reverse Voltage 50 and 1000 V Forward Current 4.0 A



Polarity shown on front side of case, positive lead beveled corner.

Dimensions in inches and (millimeters)

Features

- Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- This series is UL listed under the Recognized Component Index, file number E54214
- · Glass passivated chip junction
- · High case dielectric strength
- Typical I_R less than 0.1μA
- · High surge current capability
- Ideal for printed circuit boards

Mechanical Data

Case: Molded plastic body over passivated junctions
Terminals: Plated leads solderable per MIL-STD-750,
Method 2026

High temperature soldering guaranteed: 260°C/10 seconds, 0.375" (9.5mm) lead length,

5lbs. (2.3kg) tension

Mounting Position: Any
Weight: 0.071 oz., 2.0 g

Packaging codes/options:

1/400 EA. per Bulk Tray Stack

Maximum Ratings & Thermal Characteristics Ratings at 25°C ambient temperature unless otherwise specified.

Parameter	Symbol	GBL 005	GBL 01	GBL 02	GBL 04	GBL 06	GBL 08	GBL 10	Unit
Maximum repetitive peak reverse voltage	VRRM	50	100	200	400	600	800	1000	V
Maximum RMS voltage	VRMS	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	VDC	50	100	200	400	600	800	1000	V
Maximum average forward Tc=50°C rectified output current at T _A =40°C	lF(AV)	4.0 ⁽¹⁾ 3.0 ⁽²⁾					Α		
Peak forward surge current single sine-wave superimposed on rated load (JEDEC Method) TJ=150°C	IFSM	150					А		
Rating for fusing (t<8.3ms)	l²t	93					A ² sec		
Typical thermal resistance per leg	R⊕JA R⊕JL	22 ⁽¹⁾ 3.5 ⁽²⁾					°C/W		
Operating junction storage and temperature range	T _J , T _{STG}	-55 to +150					°C		

Electrical Characteristics Ratings at 25°C ambient temperature unless otherwise specified.

Maximum instantaneous forward drop per leg at 4.0 Amperes	VF	1.00	V	
Maximum DC reverse current at rated T_{A} = 25°C DC blocking voltage per leg T_{A} =125°C	I _R	5.0 500	μА	
Typical junction capacitance per leg at 4.0V, 1MHz	СЈ	95	40	pF

Notes: (1) Unit mounted on 3.0 x 3.0 x 0.11" thick (7.5 x 7.5 x 0.3cm) Al. plate

(2) Unit mounted on P.C.B. at 0.375" (9.5mm) lead length and 0.5 x 0.5" (12 x12mm) copper pads

Document Number 88609 19-Feb-02

GBL005 thru GBL10

Vishay Semiconductors

formerly General Semiconductor

Ratings and Characteristic Curves (TA = 25°C unless otherwise noted)

Fig. 1 - Derating Curves Output

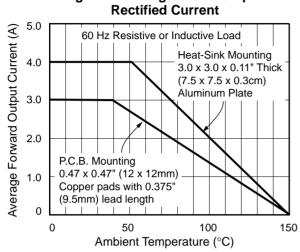


Fig. 3 - Typical Forward Voltage Characteristics Per Leg

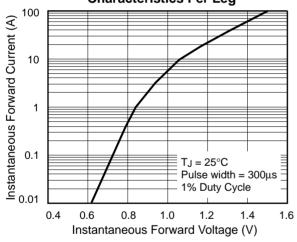


Fig. 5 - Typical Junction Capacitance Per Lea

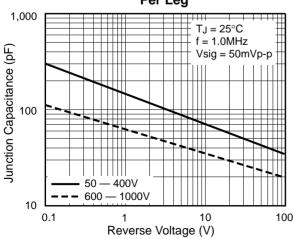


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current Per Leg

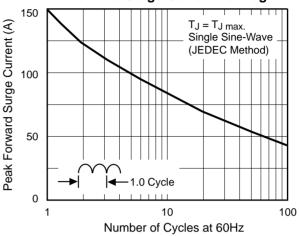


Fig. 4 - Typical Reverse Leakage Characteristics Per Leg

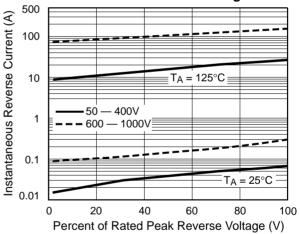
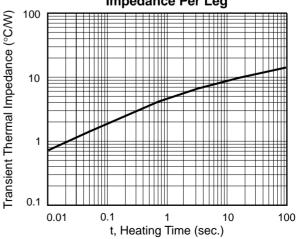


Fig. 6 - Typical Transient Thermal Impedance Per Leg



Document Number 88609 www.vishay.com 19-Feb-02



This datasheet has been download from:

www.datasheetcatalog.com

Datasheets for electronics components.