

SANYO

No. 4750

**VCR Audio Signal Recording
and Playback Processor****Functions**

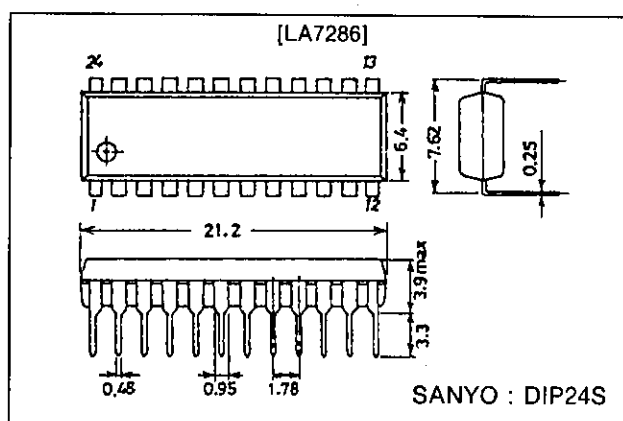
- Equalizer amplifier
- Line amplifier
- Recording amplifier
- Recording bias current automatic adjustment circuit
- Ripple filter
- Mute
- ALC
- Recording/playback switch
- SP, LP, EP switch
- Tape head switch

Features

- No adjustment of recording bias current required (due to adoption of automatic adjustment circuit).
- Recording bias oscillation circuit power supply switch on chip.
- Eliminates need for choke coil for recording equalizer.
- Playback amplifier equivalent input noise voltage: 1.0 μVrms .
- Reduced capacitance (3.3 μF) of ALC detection capacitor.
- High withstand voltage head switch on chip.
- Supply voltage: 9 V and 12 V operation.

Package Dimensions

unit : mm

3067-DIP24S**Specifications****Maximum Ratings at $T_a = 25^\circ\text{C}$**

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V_{CCmax}		14	V
Pin 2 input voltages	V_{IN2}	DC	± 65	Vp-p
Pin 2 input current	I_{IN2}		± 1.5	mA
Allowable power dissipation	P_{dmax}	$T_a \leq 65^\circ\text{C}$	500	mW
Operating temperature	T_{opr}		-10 to +65	$^\circ\text{C}$
Storage temperature	T_{stg}		-55 to +150	$^\circ\text{C}$

Operating Conditions at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V_{CC}		9, 12	V
Operating supply voltage range	V_{CCop}		8.5 to 12.5	V

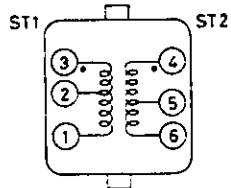
Head Coil Specifications

• R/P Head	58 k Ω (typ)	+15%	(f = 70kHz)
		-15%	
• AE Head	34 Ω (typ)	+25%	(f = 70kHz)
		-25%	
• FE Head	80 Ω (typ)	+20%	(f = 70kHz)
		-10%	

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(2) Application circuit 2 (erase head parallel type)

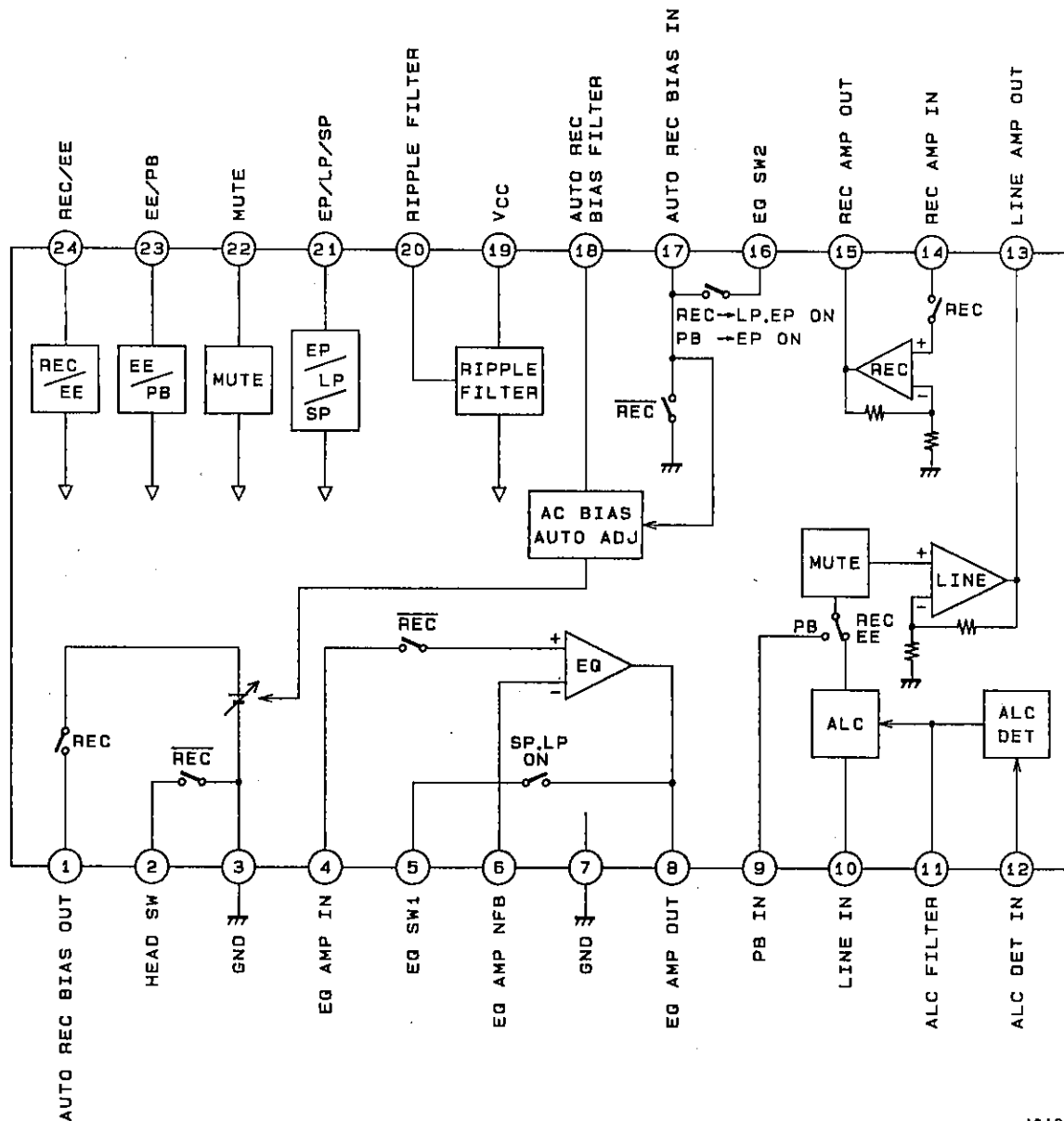
- R/P Head 58 k Ω (typ) +10% -20% ($f = 70\text{kHz}$)
- AE Head 180 Ω (typ) +25% -5% ($f = 70\text{kHz}$)
- FE Head 80 Ω (typ) +20% -20% ($f = 70\text{kHz}$)
- OSC Coil: Model name 7QM3, Prototype No. C-14284, Tokyo Parts Ind. Co., Ltd.



Pin No.	3 to 2	2 to 1	4 to 5	5 to 6
Wire type	2UEW 0.10	←	←	←
Total number of coils	15T	25T	110T	30T

* The head specifications are as agreed upon by Alps Electric and Sanyo.

Block Diagram



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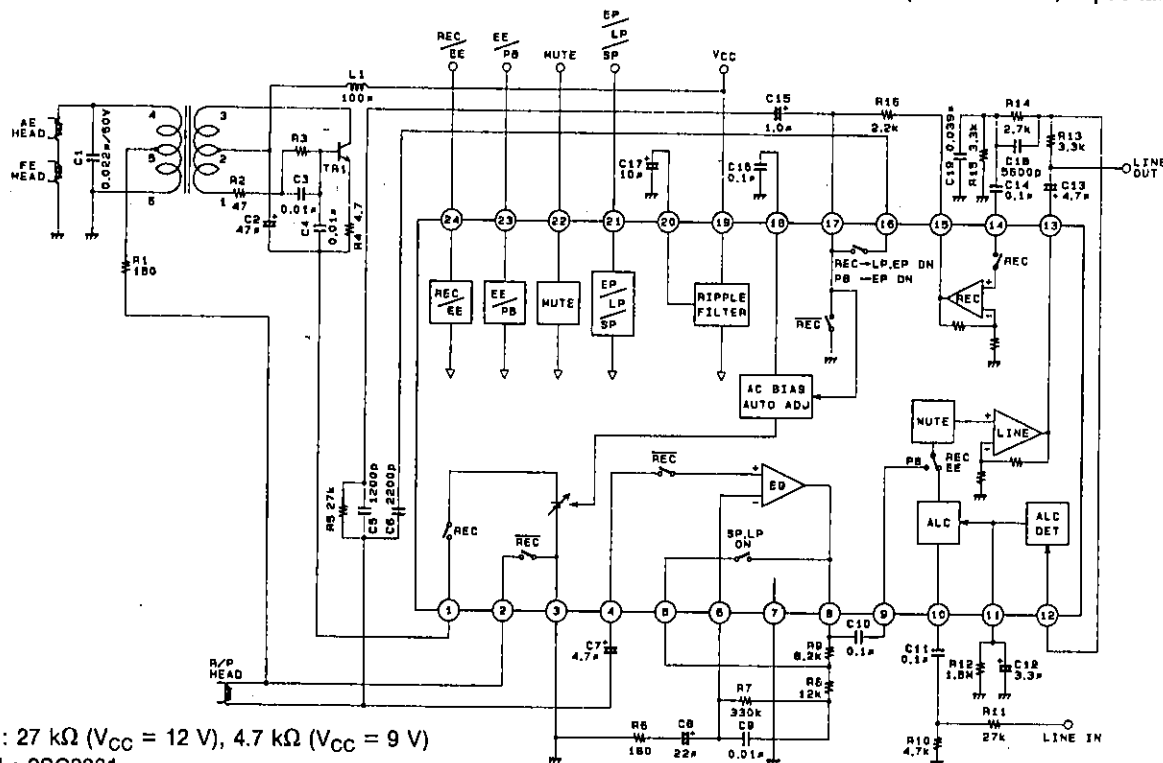
Test Circuit

Unit (resistance: Ω , capacitance: F)

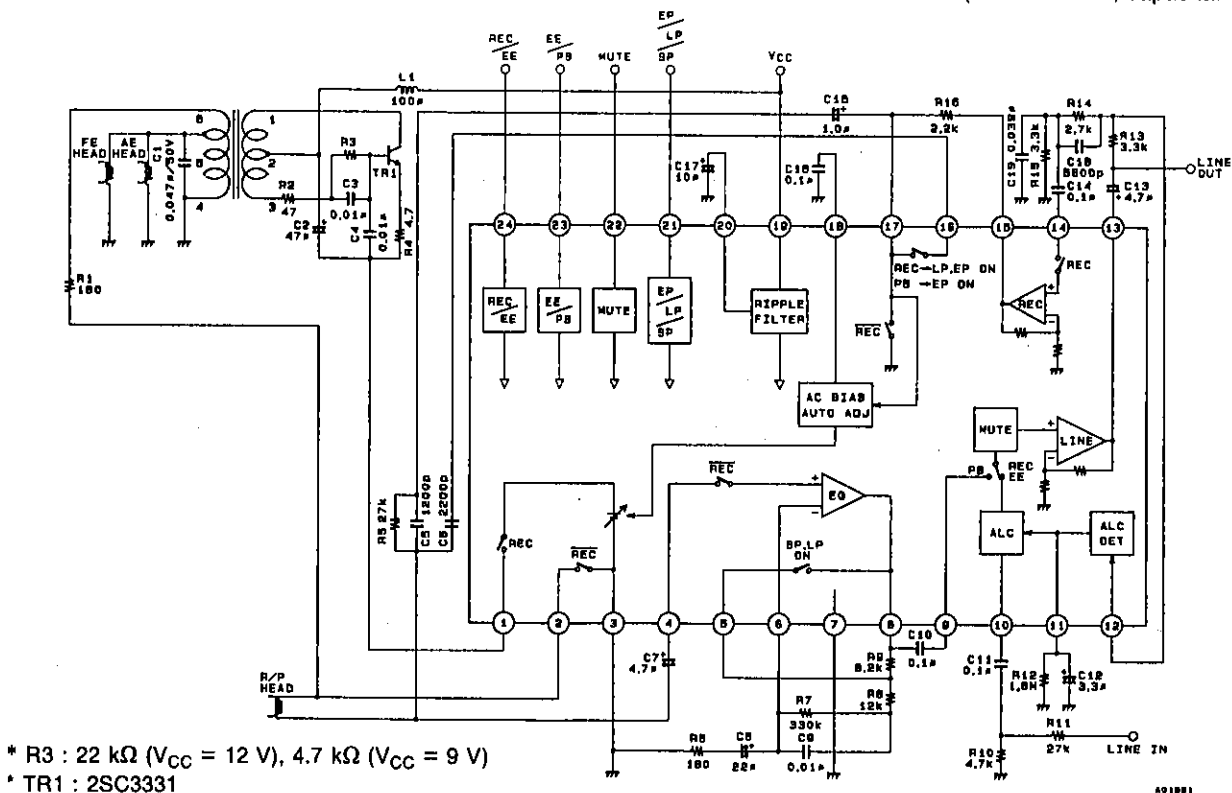
Switch Operation Table

Test item (symbol)	SW1	SW2	SW3	SW4	SW5	SW6	V _M	V _P	V _R	Input	Measure:
I _{CCE}	2	1	3	2	2	1	GND	5 V	GND	—	I _O
I _{CCP}	2	1	3	2	2	1	GND	GND	GND	—	I _O
I _{CCR}	2	1	3	2	2	1	GND	5 V	5 V	—	I _O
V _{GOE}	1	2	3	2	2	1	GND	GND	GND	V _{IN1}	V _{O1}
V _{INE}	2	1	3	2	2	1	GND	GND	GND	—	V _{O1}
V _{GLP} , THD _L , V _{MOL}	2	1	2	2	2	1	GND	GND	GND	V _{IN2}	V _{O2}
V _{GLR}	2	1	3	1	2	1	GND	5 V	GND	V _{IN3}	V _{O2}
V _{ONL}	2	1	3	2	2	1	GND	5 V	GND	—	V _{O2}
V _{OA} , ALC, THD _A	2	1	3	1	1	1	GND	5 V	GND	V _{IN3}	V _{O2}
V _{GR} , THD _R , V _{MOR}	2	1	3	2	2	1	GND	5 V	5 V	V _{IN4}	V _{O3}
M _P	1	1	1	2	2	1	5 V	GND	GND	V _{IN1}	V _{O2}
M _E	2	1	3	1	2	1	5 V	5 V	GND	V _{IN3}	V _{O2}
V _{BIAS}	2	1	3	2	2	2	GND	5 V	5 V	—	V _{O4}
V _{CTL}	2	1	3	2	2	2	GND	5 V	5 V	—	V _I

Sample Application Circuit : Erase head series type

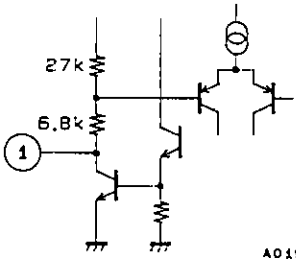
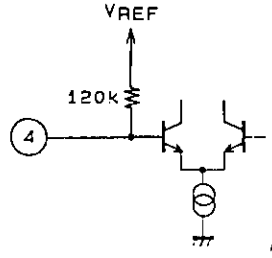
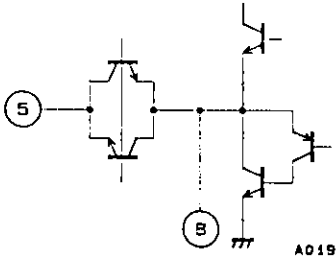
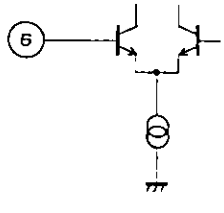
Unit (resistance: Ω , capacitance: F)

Sample Application Circuit : Erase head parallel type

Unit (resistance: Ω , capacitance: F)

Pin Functions

Unit (resistance: Ω)

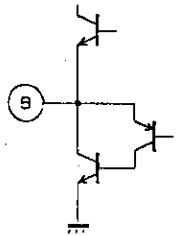
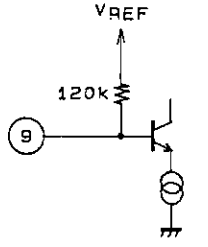
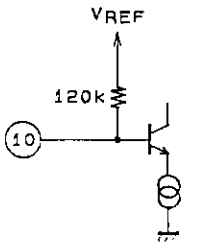
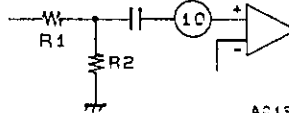
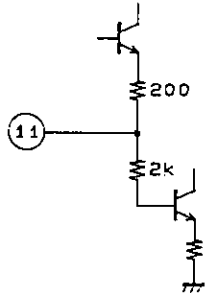
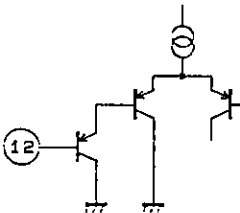
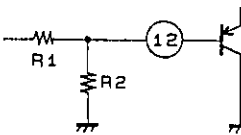
Pin No.	Function name	Internal circuit for pin	Description of function
1	Recording bias automatic control output		EE, PB \rightarrow off REC \rightarrow control voltage
2	Head switch (high withstand voltage)		EE, PB \rightarrow on REC \rightarrow off On resistance $\rightarrow 10 \Omega$ (typ) Withstand voltage when off $\rightarrow \pm 45 \text{ V}$ ($f = 80 \text{ kHz}$)
3	GND		GND for pin 2 head switch and Equalizer Amplifier only
4	EQ AMP input		Input impedance for playback signal input from head $\rightarrow 120 \text{ k}\Omega$ (typ)
5	EQ switch 1		Switches the Playback Equalizer Amplifier high-region frequency voltage gain. LP, SP \rightarrow on EP \rightarrow off On resistance $\rightarrow 20 \Omega$ (typ)
6	EQ AMP NFB		Equalizer Amplifier negative feedback pin
7	GND		GND for all circuit blocks except the pin 2 head switch and Equalizer Amplifier

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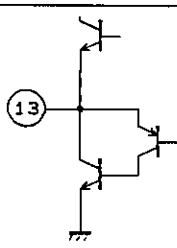
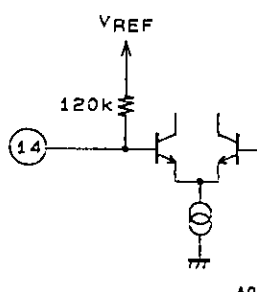
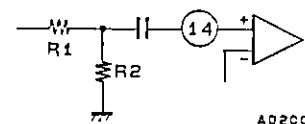
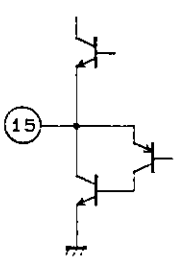
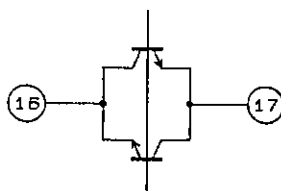
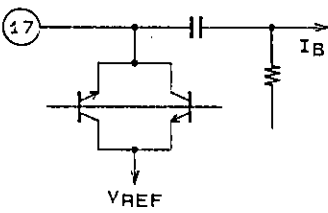
Unit (resistance: Ω)

Pin No.	Function name	Internal circuit for pin	Description of function
8	EQ AMP output	 <p>A01996</p>	Output impedance $\rightarrow 50\Omega$ (typ)
9	LINE AMP PB Input	 <p>A01997</p>	Inputs the playback signal from the Equalizer Amplifier. Because the input impedance is as high as $120\text{ k}\Omega$, a $0.1\text{ }\mu\text{F}$ ceramic capacitor can be used for the coupling capacitor on pin 9.
10	LINE AMP LINE input	 <p>A01998</p>	Inputs EE and REC signals.  <p>A01999</p> <p>The reference input is set by resistors R1 and R2. The amplifier gain is fixed at 21.5 dB. In addition, because the input impedance is as high as $120\text{ k}\Omega$, a $0.1\text{ }\mu\text{F}$ ceramic capacitor can be used for the coupling capacitor on pin 10.</p>
11	ALC FILTER	 <p>A02000</p>	Wave detection is performed when connected to GND through a capacitor. In addition, the attack and recovery time is set by the C and R time constants.
12	ALC Input wave detection	 <p>A02001</p>	 <p>A02002</p> <p>Inputs the Line Amplifier output signal. The ALC level is set by the resistors R1 and R2.</p>

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Unit (resistance: Ω)

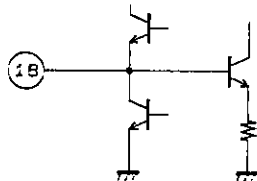
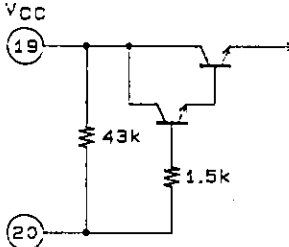
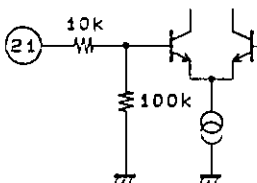
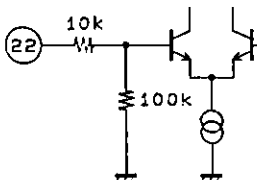
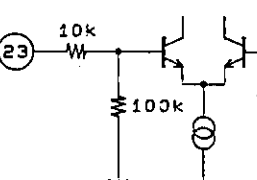
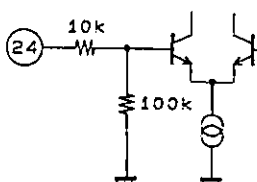
Pin No.	Function name	Internal circuit for pin	Description of function												
13	LINE AMP output	 A02003	Output impedance → 50 Ω (typ)												
14	REC AMP input	 A02004	<p>Inputs the recording signal from Line Amplifier.</p>  A02005	<p>The recording current is set by the resistors R1 and R2. In addition, because the input impedance is as high as 120 kΩ, a 0.1 μF ceramic capacitor can be used for the coupling capacitor on pin 14.</p>											
15	REC AMP output	 A02005	Output impedance → 50 Ω (typ)												
16	EQ switch 2	 A02007	<p>Switches the high-region peaking frequency during recording and playback.</p> <table border="1"><thead><tr><th></th><th>REC</th><th>PB</th></tr></thead><tbody><tr><td>EP</td><td>On</td><td>On</td></tr><tr><td>LP</td><td>On</td><td>Off</td></tr><tr><td>SP</td><td>Off</td><td>Off</td></tr></tbody></table> <p>On resistance → 30 Ω (typ)</p>		REC	PB	EP	On	On	LP	On	Off	SP	Off	Off
	REC	PB													
EP	On	On													
LP	On	Off													
SP	Off	Off													
17	Recording bias automatic control input and PB switch.	 A02008	<p>EE, PB → on REC → off On resistance → 20 Ω (typ)</p>												

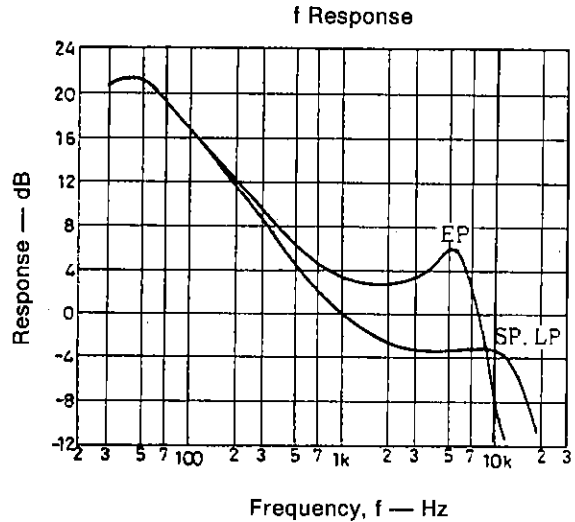
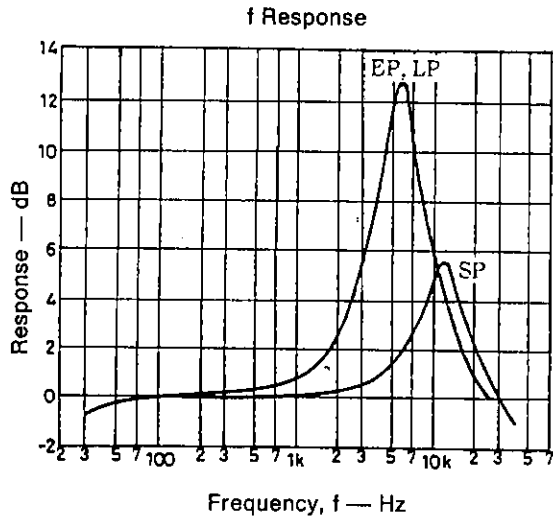
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Unit (resistance: Ω)

Pin No.	Function name	Internal circuit for pin	Description of function												
18	Recording bias automatic control filter	 A02009	Wave detection is performed when connected to GND through a capacitor.												
19	Supply voltage (V _{CC})		V _{CC} max = 14 V V _{CC} = 8.5 V to 12.5 V												
20	Ripple filter	 A02010	Ripple rejection is performed when connected to GND through an electrolytic capacitor for the filter.												
21	EP/LP/SP Control	 A02011	When the voltage on pin 21 is 3.6 V to 6.0 V: EP; when 1.8 V to 2.6 V: LP; when 0 V to 1.0 V: SP Switch On Pin Number <table><tr><td></td><td>REC</td><td>PB</td></tr><tr><td>EP</td><td>16</td><td>16</td></tr><tr><td>LP</td><td>16, 5</td><td>5</td></tr><tr><td>SP</td><td>5</td><td>5</td></tr></table>		REC	PB	EP	16	16	LP	16, 5	5	SP	5	5
	REC	PB													
EP	16	16													
LP	16, 5	5													
SP	5	5													
22	MUTE Control	 A02012	When the voltage on pin 22 is 2.5 V to 6.0 V: MUTE on; when 0 V to 1.5 V: MUTE off												
23	EE/PB Control	 A02013	When the voltage on pin 23 is 3.0 V to 6.0 V: EE; when 0 V to 1.0 V: PB												
24	REC/EE Control	 A02014	When the voltage on pin 24 is 3.0 V to 6.0 V: REC; when 0V to 1.0 V: EE However, REC mode is entered only when the voltage on pin 23 is 3.0 V to 6.0 V.												



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