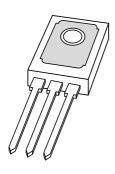
# DISCRETE SEMICONDUCTORS

# DATA SHEET



BD135; BD137; BD139 NPN power transistors

Product specification Supersedes data of 1997 Mar 04 1999 Apr 12





# **NPN** power transistors

BD135; BD137; BD139

### **FEATURES**

- High current (max. 1.5 A)
- Low voltage (max. 80 V).

### **APPLICATIONS**

• Driver stages in hi-fi amplifiers and television circuits.

#### **DESCRIPTION**

NPN power transistor in a TO-126; SOT32 plastic package. PNP complements: BD136, BD138 and BD140.

#### **PINNING**

PIN	DESCRIPTION
1	emitter
2	collector, connected to metal part of mounting surface
3	base

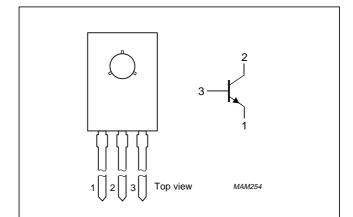


Fig.1 Simplified outline (TO-126; SOT32) and symbol.

### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter			
	BD135		_	45	V
	BD137		_	60	V
	BD139		_	100	V
V <sub>CEO</sub>	collector-emitter voltage	open base			
	BD135		_	45	V
	BD137		_	60	V
	BD139		_	80	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	5	V
I <sub>C</sub>	collector current (DC)		_	1.5	А
I <sub>CM</sub>	peak collector current		_	2	А
I <sub>BM</sub>	peak base current		_	1	А
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> ≤ 70 °C	_	8	W
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

# NPN power transistors

BD135; BD137; BD139

### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-a</sub>	thermal resistance from junction to ambient	note 1	100	K/W
R <sub>th j-mb</sub>	thermal resistance from junction to mounting base		10	K/W

#### Note

1. Refer to TO-126; SOT32 standard mounting conditions.

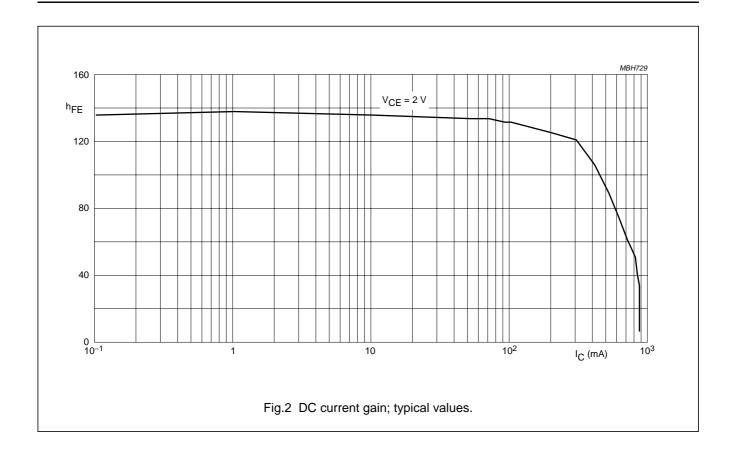
### **CHARACTERISTICS**

 $T_j$  = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	I <sub>E</sub> = 0; V <sub>CB</sub> = 30 V	_	_	100	nA
		$I_E = 0$ ; $V_{CB} = 30 \text{ V}$ ; $T_j = 125 ^{\circ}\text{C}$	_	_	10	μΑ
I <sub>EBO</sub>	emitter cut-off current	I <sub>C</sub> = 0; V <sub>EB</sub> = 5 V	_	_	100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 2 V; (see Fig.2)				
		$I_C = 5 \text{ mA}$	40	_	_	
		I <sub>C</sub> = 150 mA	63	_	250	
		I <sub>C</sub> = 500 mA	25	_	_	
	DC current gain	$I_C = 150 \text{ mA}; V_{CE} = 2 \text{ V};$				
	BD135-10; BD137-10; BD139-10	(see Fig.2)	63	_	160	
	BD135-16; BD137-16; BD139-16		100	_	250	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA	_	_	0.5	V
V <sub>BE</sub>	base-emitter voltage	I <sub>C</sub> = 500 mA; V <sub>CE</sub> = 2 V	_	_	1	V
f <sub>T</sub>	transition frequency	I <sub>C</sub> = 50 mA; V <sub>CE</sub> = 5 V; f = 100 MHz	_	190	_	MHz
h <sub>FE1</sub> h <sub>FE2</sub>	DC current gain ratio of the complementary pairs	$ I_C  = 150 \text{ mA};  V_{CE}  = 2 \text{ V}$	_	1.3	1.6	

# NPN power transistors

# BD135; BD137; BD139



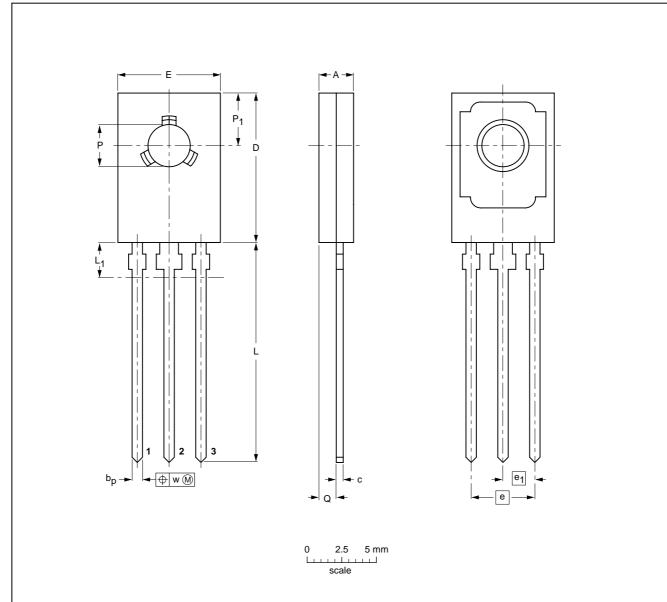
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# NPN power transistors

BD135; BD137; BD139

### **PACKAGE OUTLINE**

Plastic single-ended leaded (through hole) package; mountable to heatsink, 1 mounting hole; 3 leads SOT32



### **DIMENSIONS** (mm are the original dimensions)

UNIT	Α	bp	С	D	E	е	e <sub>1</sub>	L	L <sub>1</sub> <sup>(1)</sup> max	Q	Р	P <sub>1</sub>	w
mm	2.7 2.3	0.88 0.65	0.60 0.45	11.1 10.5	7.8 7.2	4.58	2.29	16.5 15.3	2.54	1.5 0.9	3.2 3.0	3.9 3.6	0.254

### Note

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE		REFERENCES				
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT32		TO-126				97-03-04

## NPN power transistors

BD135; BD137; BD139

#### **DEFINITIONS**

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

#### Limiting values

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

#### **Application information**

Where application information is given, it is advisory and does not form part of the specification.

#### LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

# NPN power transistors

BD135; BD137; BD139

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Printed in The Netherlands 115002/00/03/pp8 Date of release: 1999 Apr 12 Document order number: 9397 750 05576









SCA63

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