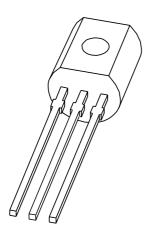
DISCRETE SEMICONDUCTORS

DATA SHEET



MPSA06 NPN general purpose transistor

Product specification Supersedes data of 1998 Jul 21 1999 Apr 27





NPN general purpose transistor

MPSA06

FEATURES

- Low current (max. 500 mA)
- Low voltage (max. 80 V).

APPLICATIONS

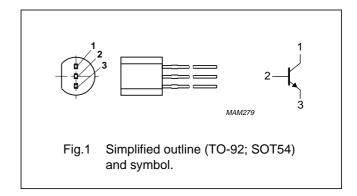
• General purpose switching and amplification.

DESCRIPTION

NPN transistor in a TO-92; SOT54 plastic package. PNP complement: MPSA56.

PINNING

PIN	DESCRIPTION		
1	collector		
2	base		
3	emitter		



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	80	V
V _{CEO}	collector-emitter voltage	open base	_	80	V
V _{EBO}	emitter-base voltage	open collector	_	5	V
I _C	collector current (DC)		_	500	mA
I _{CM}	peak collector current		_	1	Α
I _{BM} peak base current			_	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	625	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

NPN general purpose transistor

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	200	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = 80 V	_	50	nA
I _{EBO}	emitter cut-off current	I _C = 0; V _{EB} = 5 V	_	50	nA
h _{FE}	DC current gain	I _C = 10 mA; V _{CE} = 1 V	100	_	
		I _C = 100 mA; V _{CE} = 1 V	100	_	
V _{CEsat}	collector-emitter saturation voltage	I _C = 100 mA; I _B = 10 mA	_	250	mV
V _{BE}	base-emitter voltage	I _C = 100 mA; V _{CE} = 1 V	_	1.2	V
f _T	transition frequency	I _C = 10 mA; V _{CE} = 2 V; f = 100 MHz	100	_	MHz

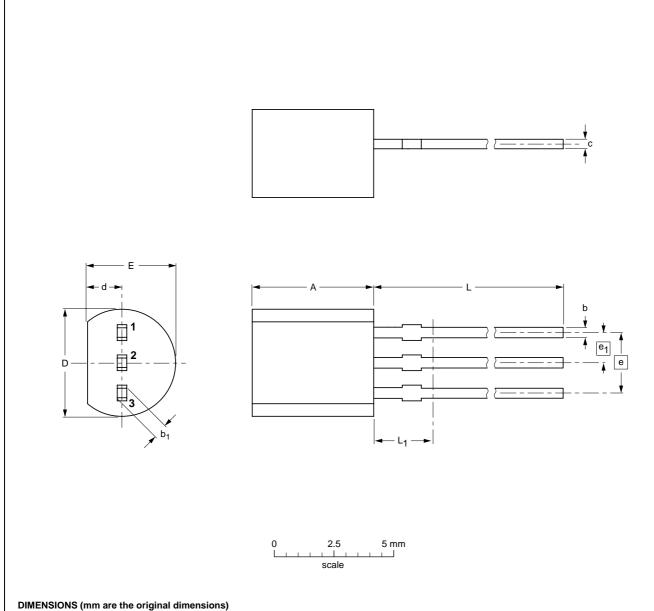
NPN general purpose transistor

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PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; 3 leads

SOT54



UNIT	A	b	b ₁	С	D	d	E	е	e ₁	L	L ₁ ⁽¹⁾
mm	5.2 5.0	0.48 0.40	0.66 0.56	0.45 0.40	4.8 4.4	1.7 1.4	4.2 3.6	2.54	1.27	14.5 12.7	2.5

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE		REFERENCES EUROPEAN			NCES EUROPEAN ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE	
SOT54		TO-92	SC-43			97-02-28	

1999 Apr 27 4

NPN general purpose transistor

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DEFINITIONS

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

NPN general purpose transistor

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NOTES

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NOTES

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