
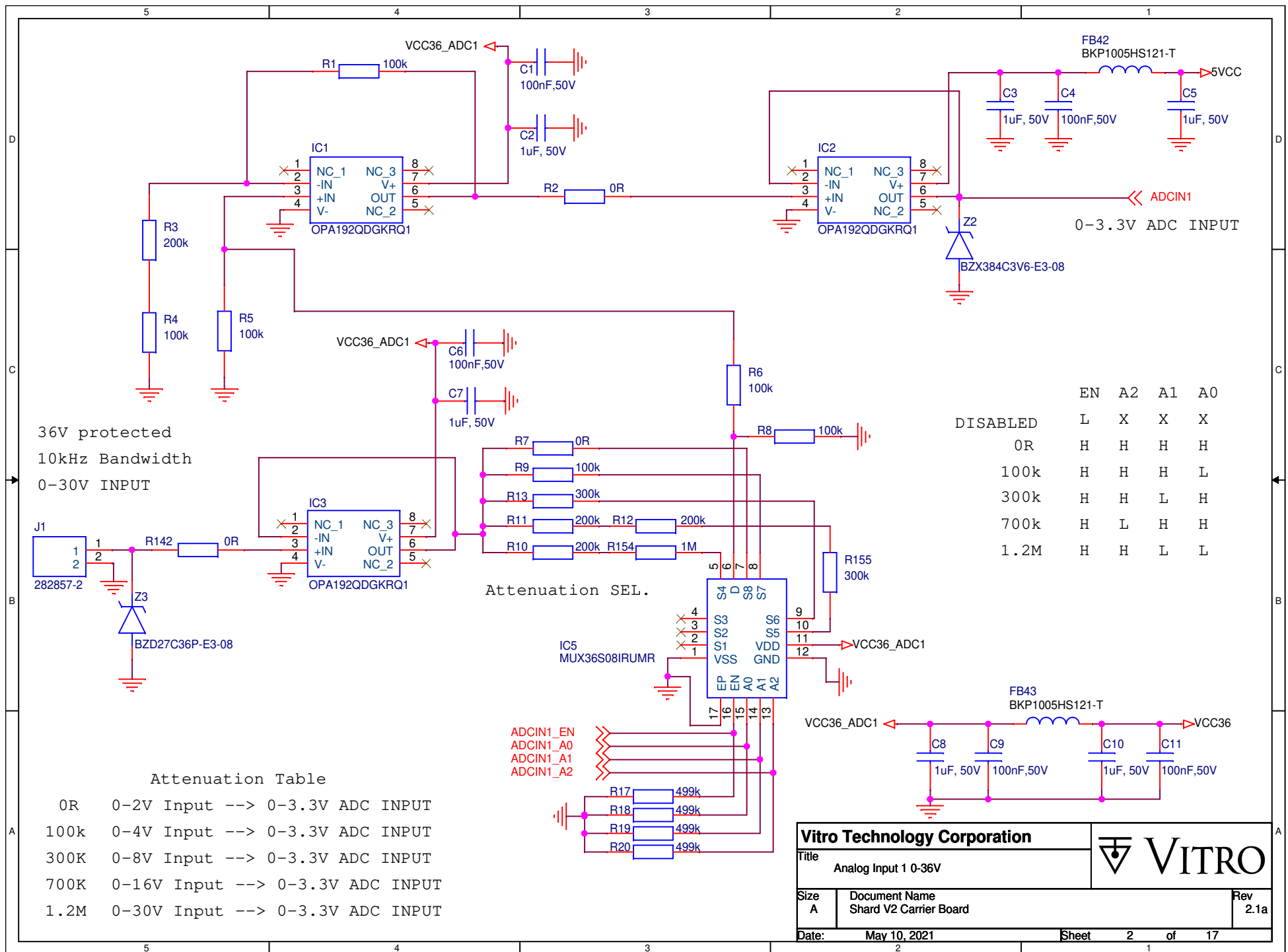
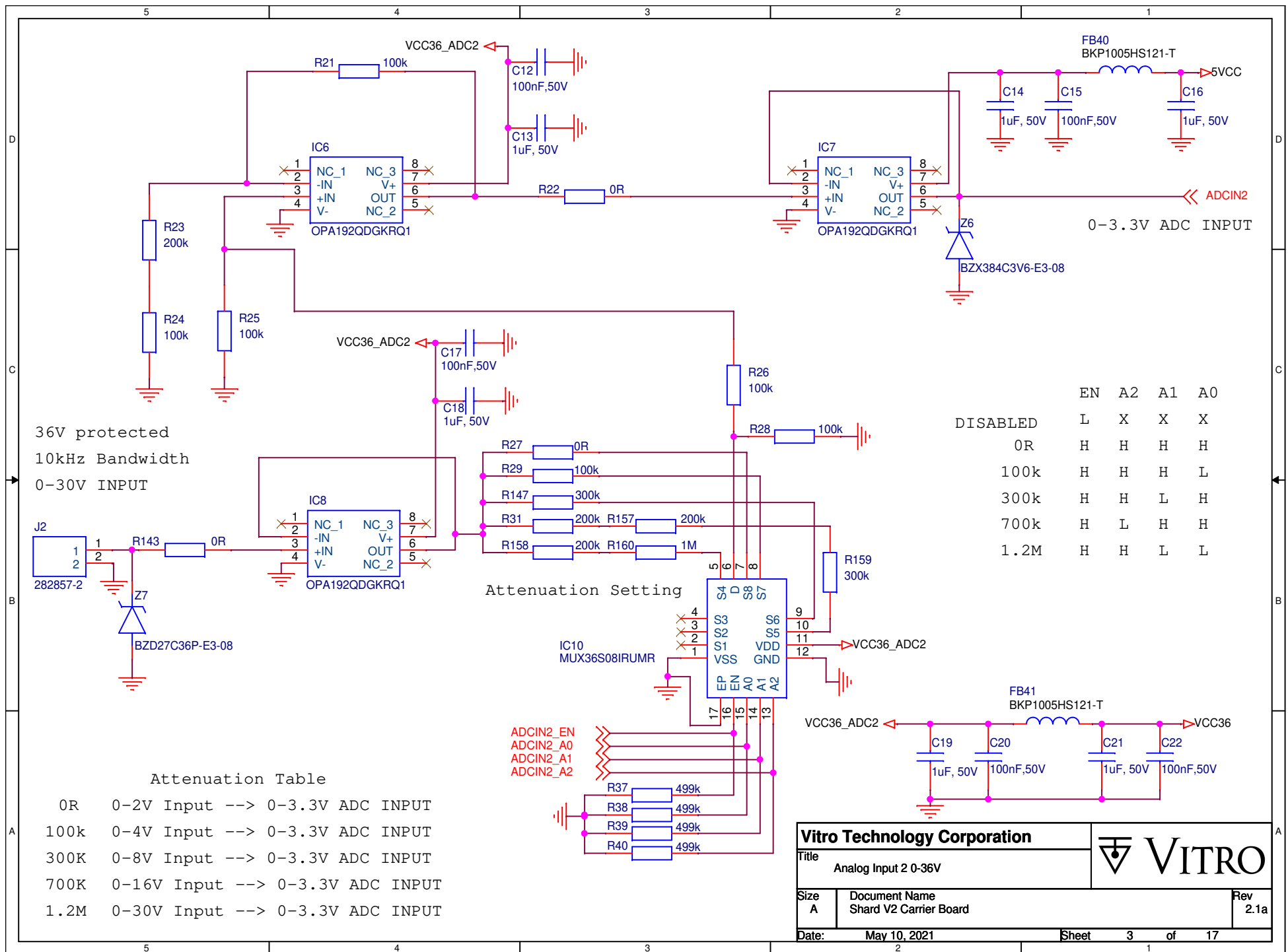
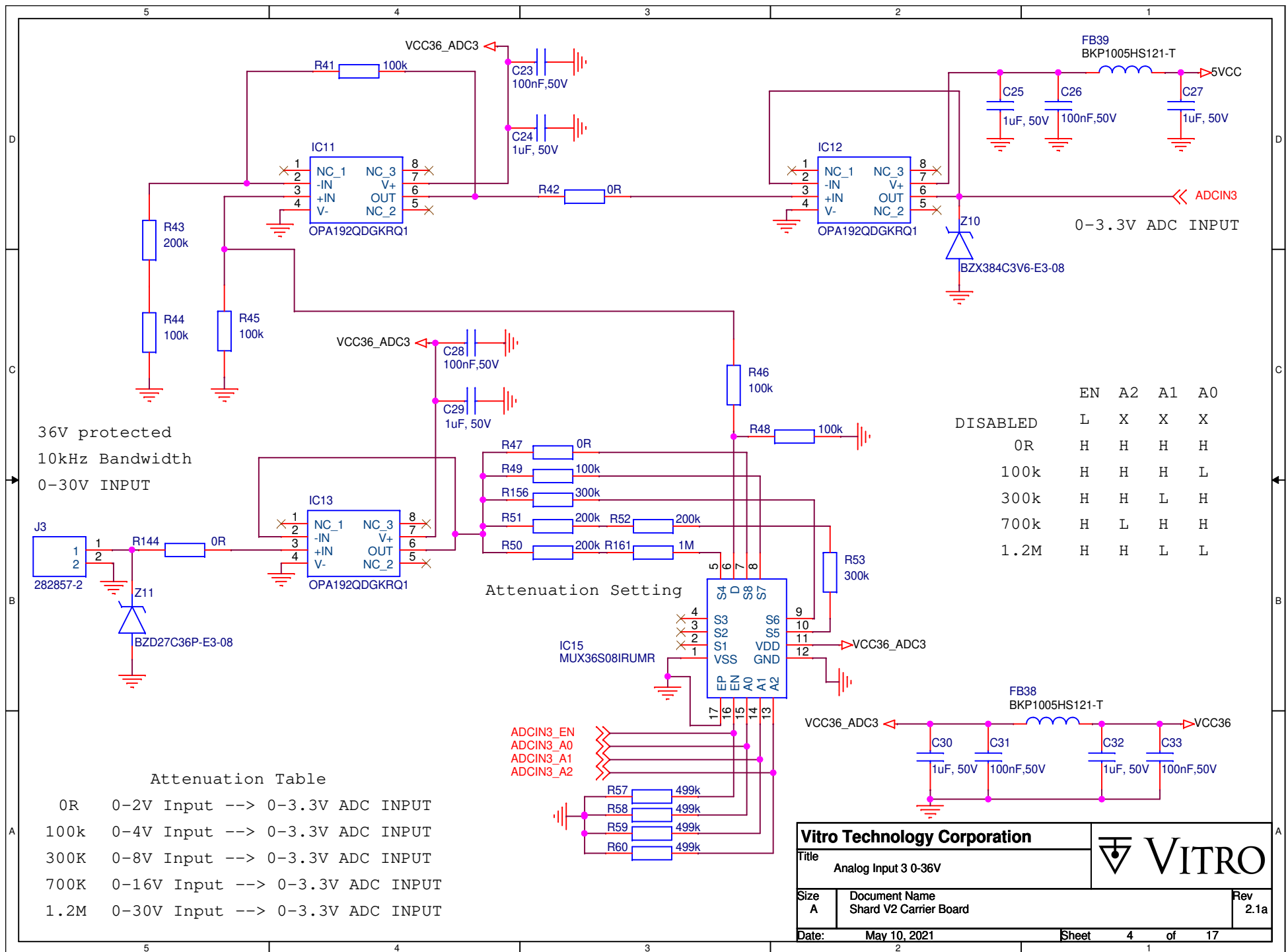


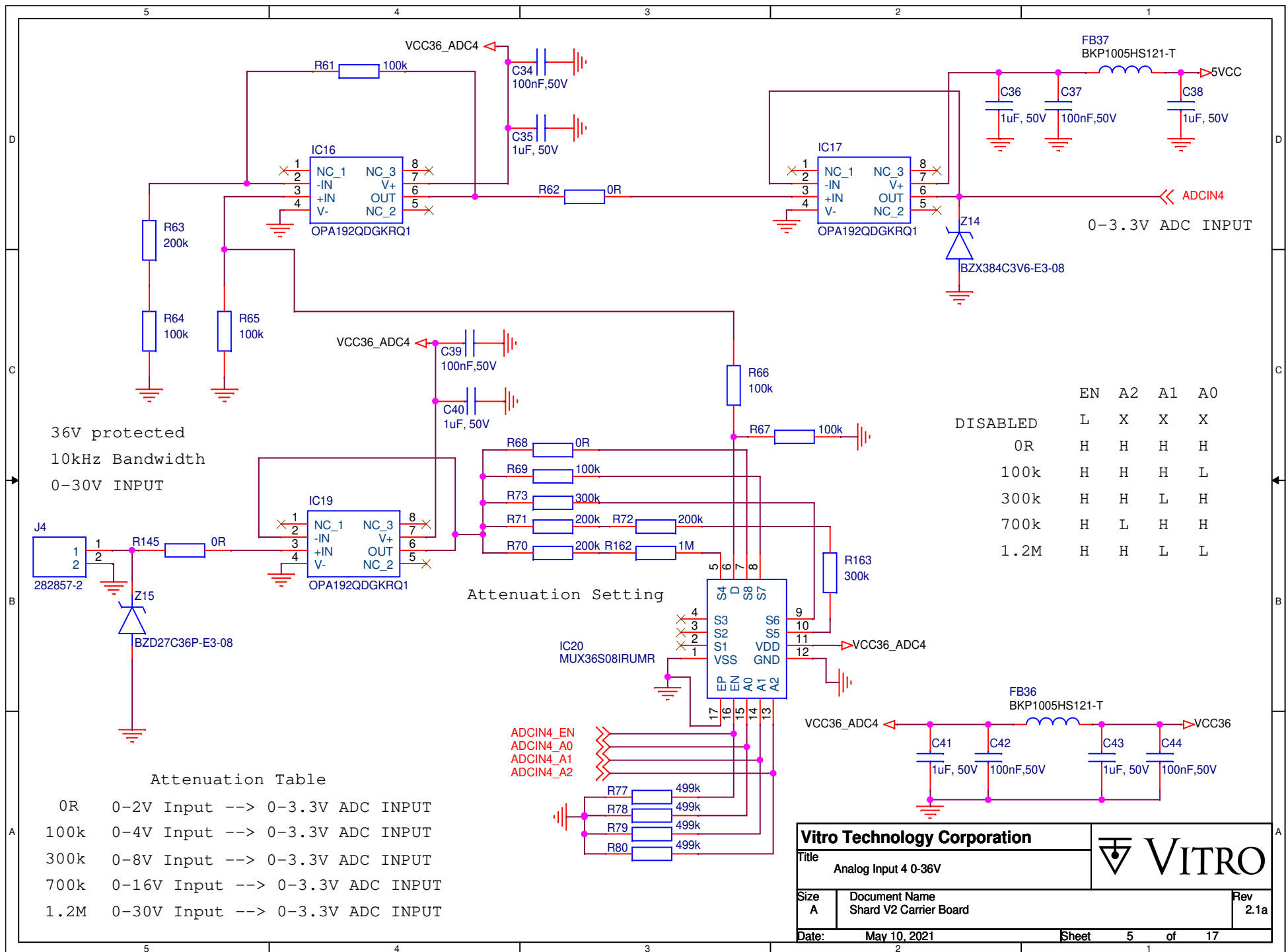
Revision	Date	Description
1.0	July 20, 2020	Initial Schematic Release
2.0	October 23, 2020	Prototype Series Manufacturing Schematic Release
2.1	December 1, 2020	<ul style="list-style-type: none"> - Z2, Z6, Z10, Z14 - PN change from DZ2W03300L to BZX384C3V6-E3-08 - R13, R73, R147, R156 value change from 200k to 300k - R155, R163, R159, R53 value change from 33k to 300k - R10, R70, R158, R50 value change from 300k to 200k - R154, R162, R160, R161 value change from 47k to 1M - Z1,Z5,Z9,Z13 removed - IC4,IC9,IC14,IC18 removed - J53, J54 PN change from 1981568-2 to 614105150621 - D18, D19, D23, D24 PN change from DF2S6P2FU,H3F to D55V0M1B2WSQ-7 - J26 - pinout corrections - RS485 interface: RE signal added DB9 connector pinout updated and 0R resistors added Jumpers for Half/Full Duplex modes selection added Additional terminal blocks added for second pair and GND_ISO connection - RS232 Interface: DB9 connector pinout correction Jumper for Half/Full Duplex mode selection added - RJ9 connectors replaced with RJ12 - 615006143221 - R141 removed
2.1a	May 10, 2021	Visual/cosmetic updates

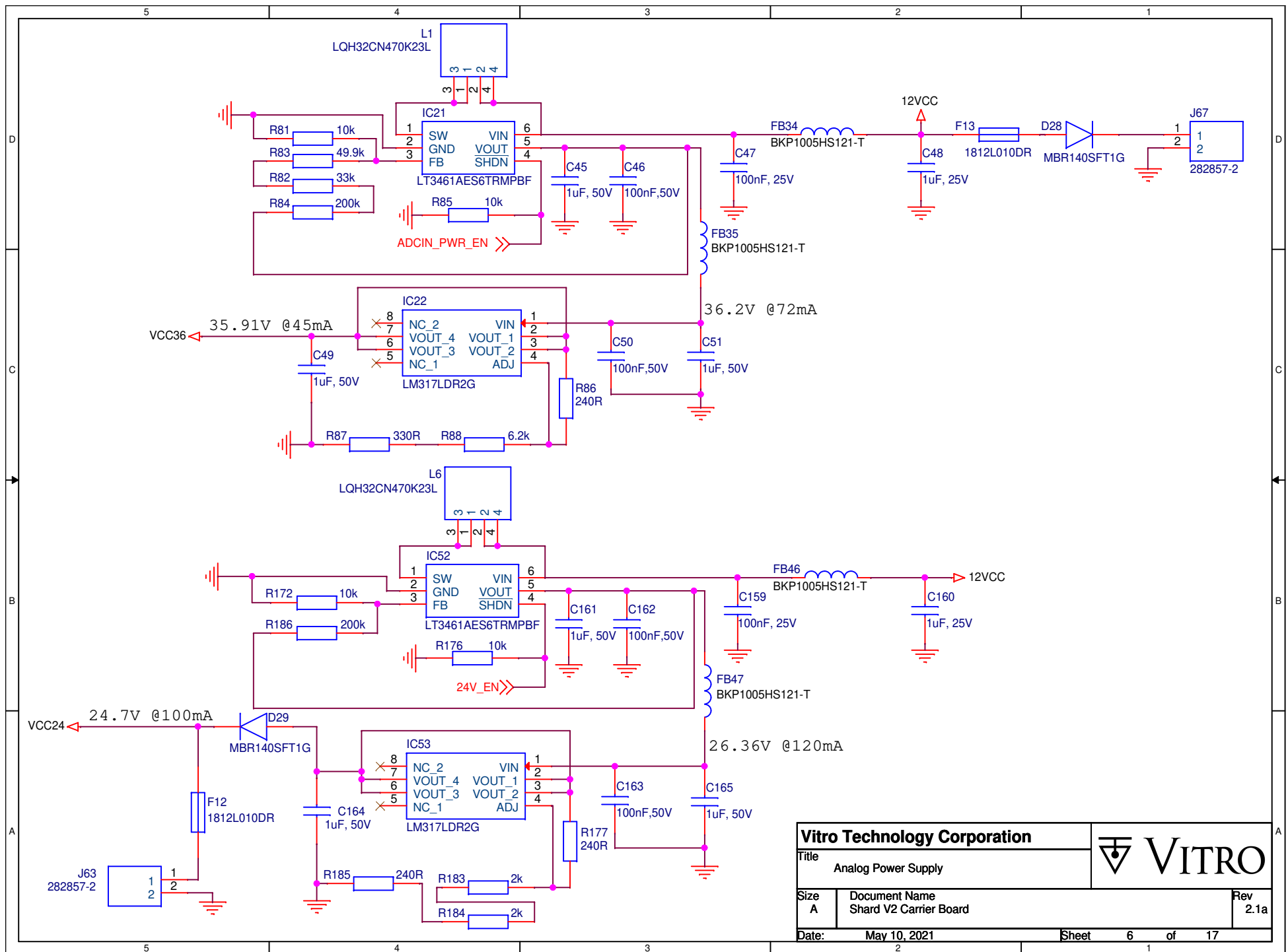
Vitro Technology Corporation		
Title RevisionTable		
Size A	Document Name Shard V2 Carrier Board	Rev 2.1a
Date: May 10, 2021		Sheet 1 of 17











Vitro Technology Corporation

Title
Analog Power Supply

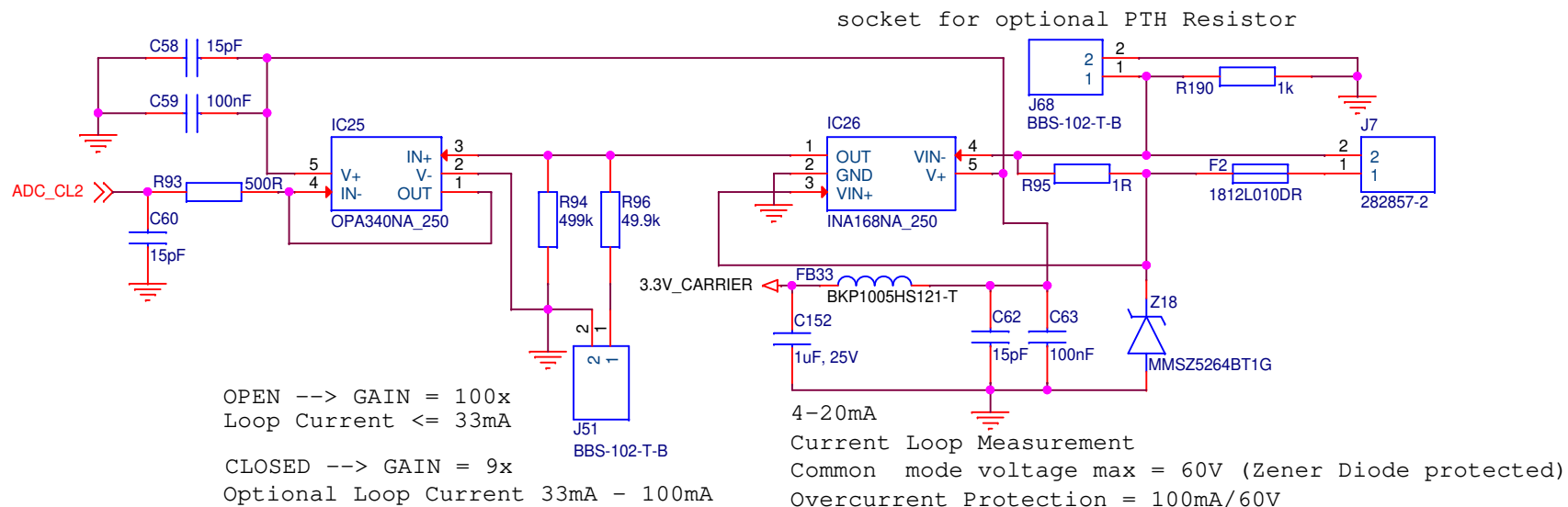
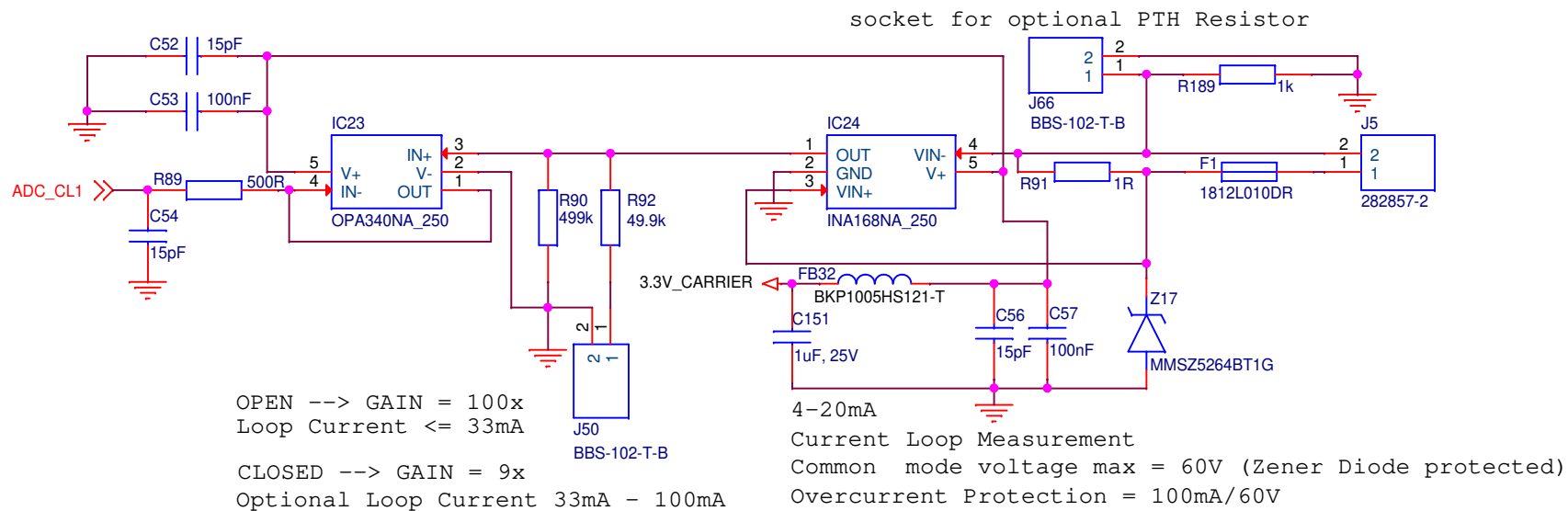


Size
A Document Name
Shard V2 Carrier Board

Rev
2.1a

Date: May 10, 2021

Sheet 6 of 17



Vitro Technology Corporation

Title
4-20mA Current Loop 1-2

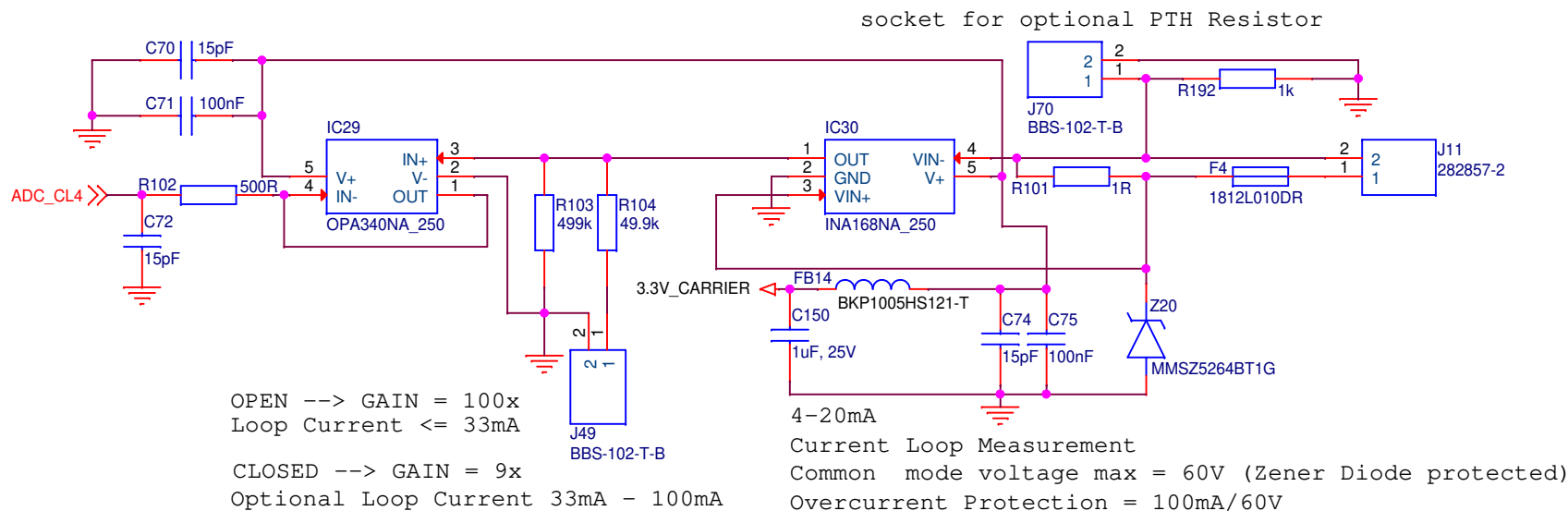
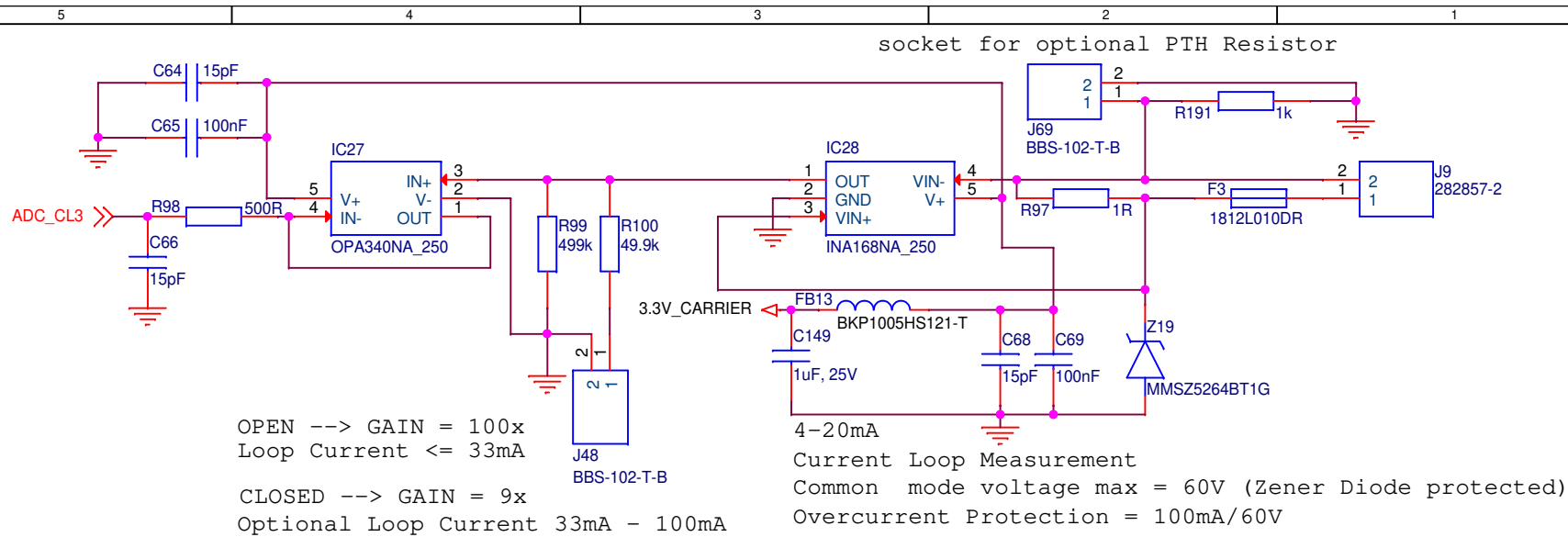
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Size A Document Name
Shard V2 Carrier Board

Rev
2.1a

Date: May 10, 2021

Sheet 7 of 17



Vitro Technology Corporation

Title
4-20mA Current Loop 3-4

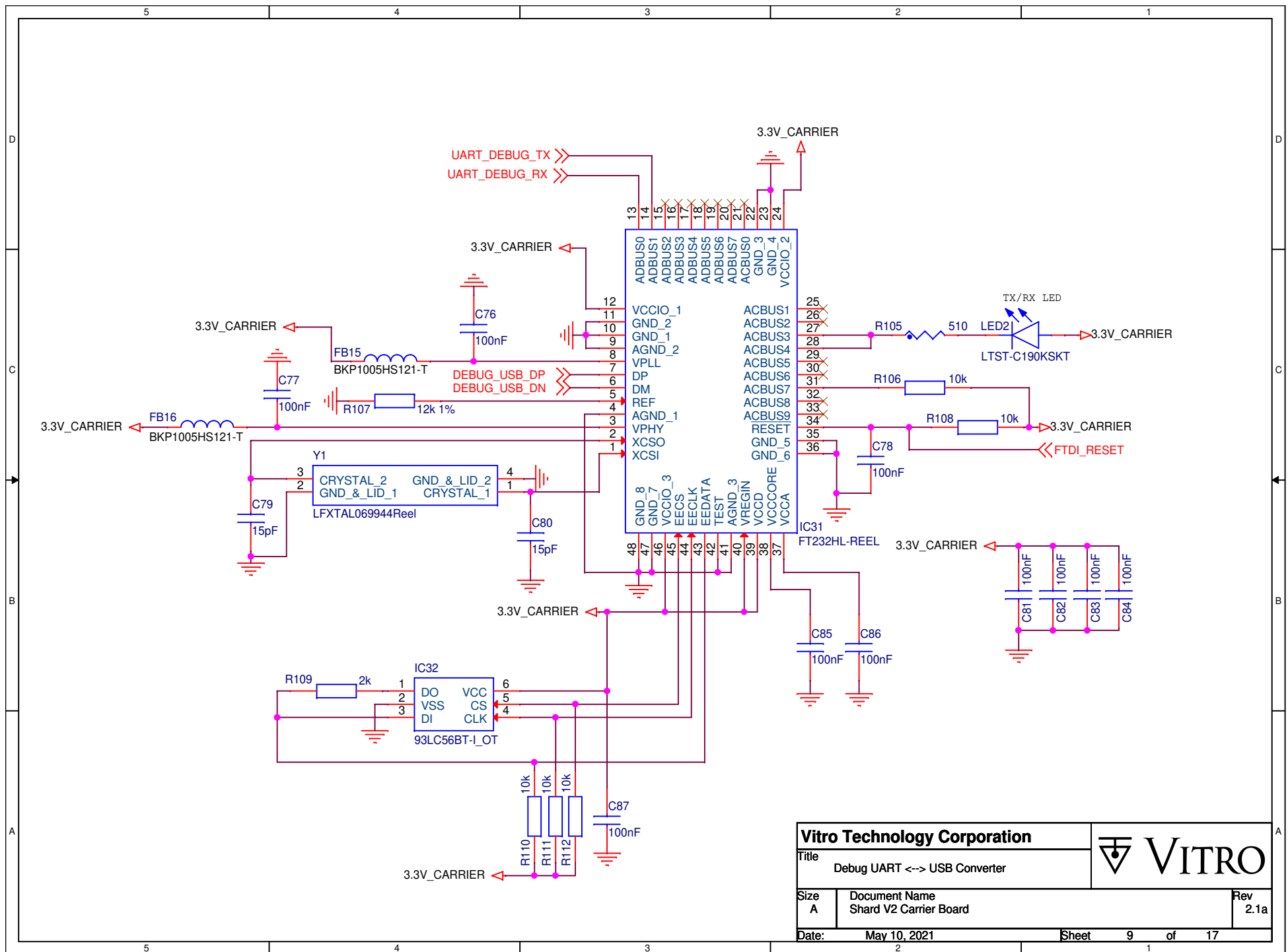
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Shard V2 Carrier Board

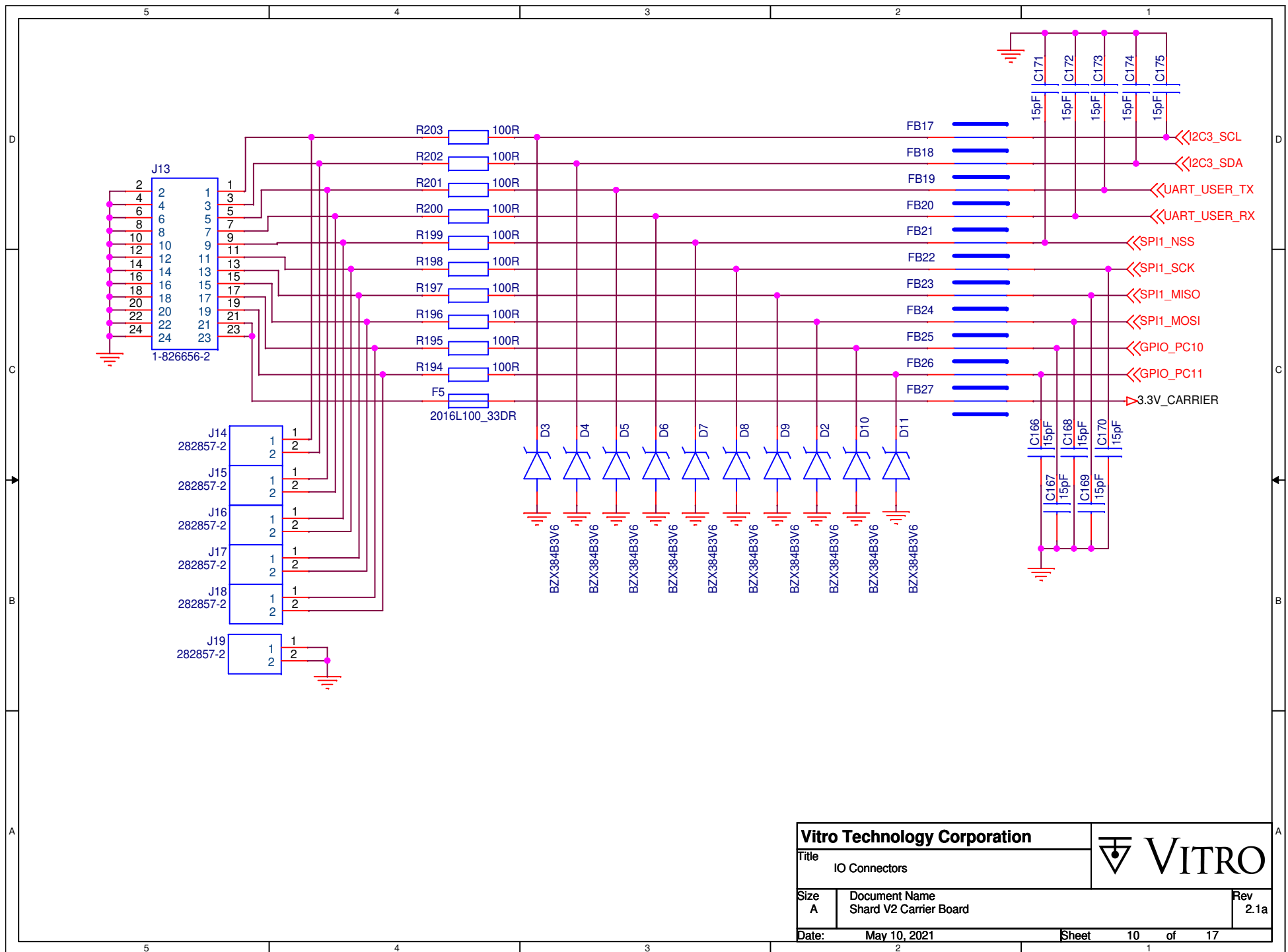
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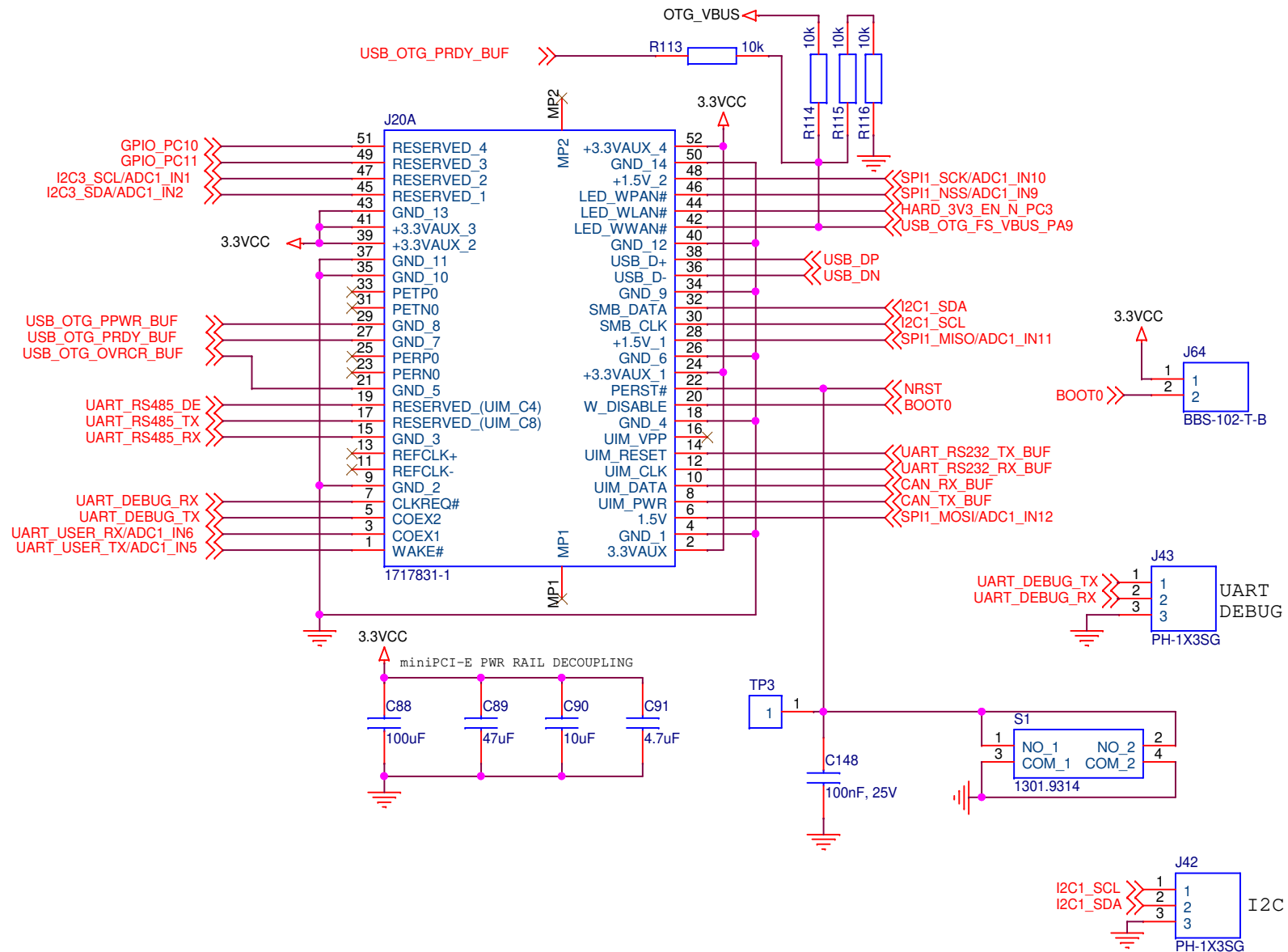
Sheet 8 of 17


Rev
2.1a

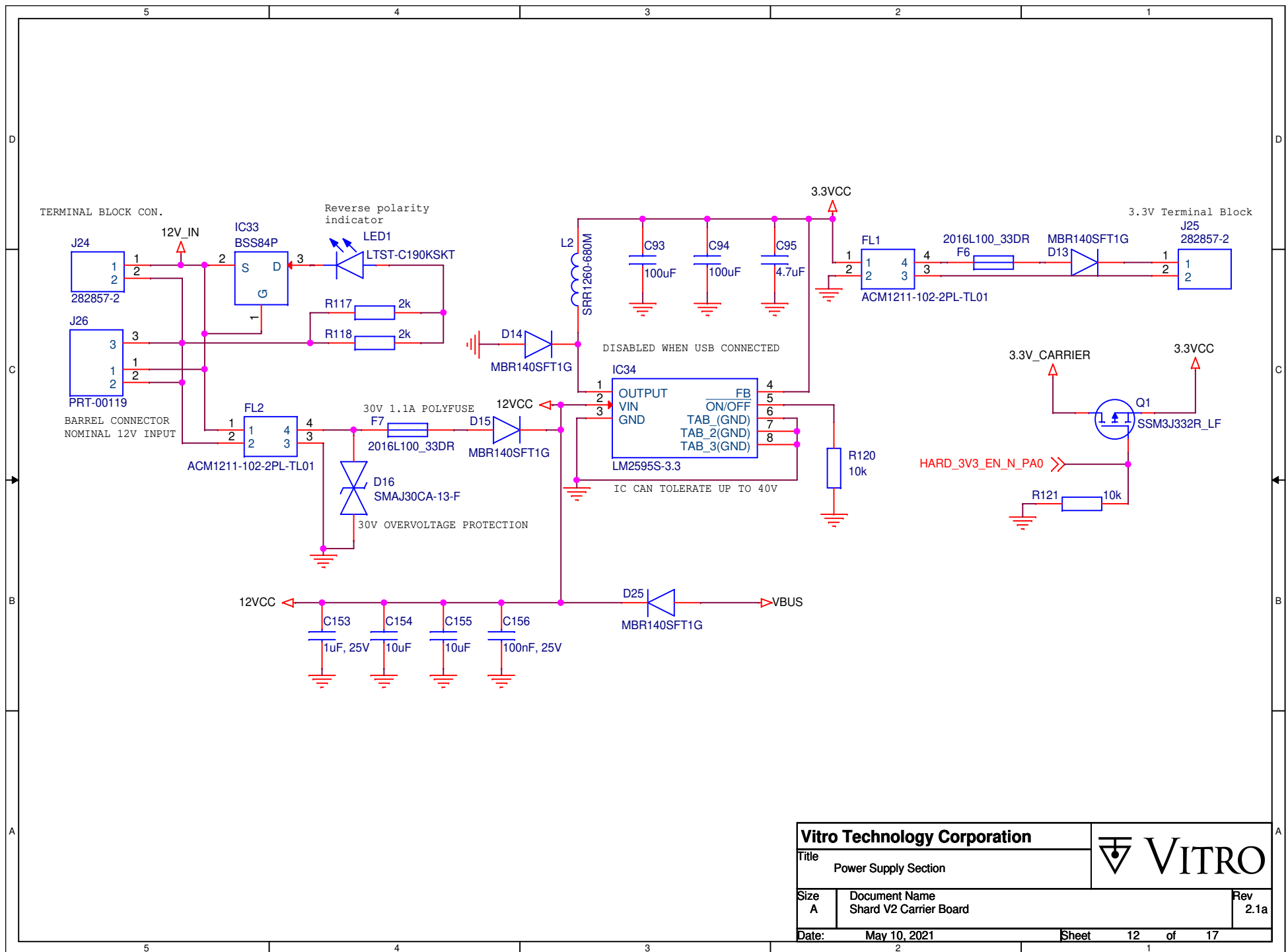
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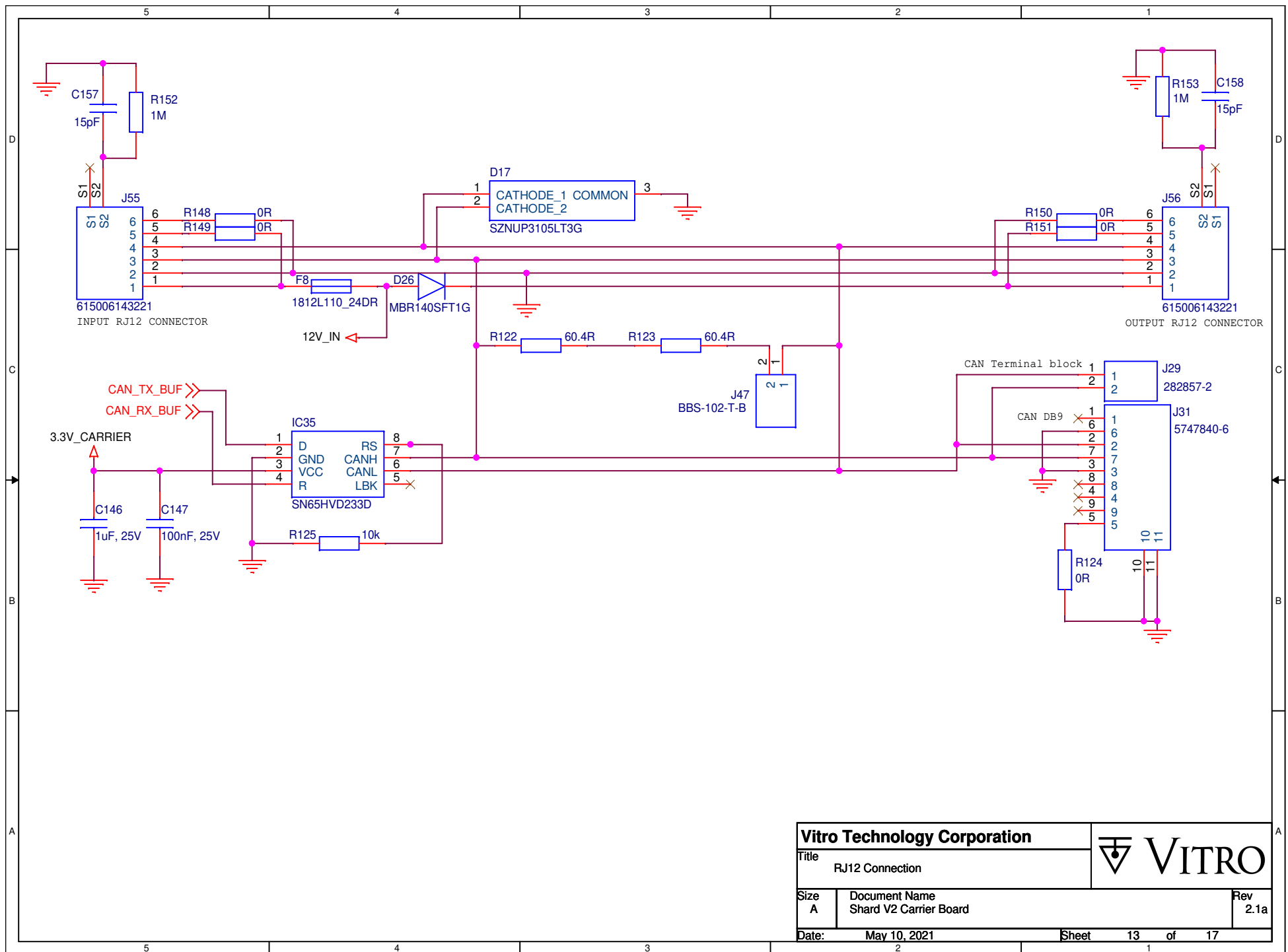


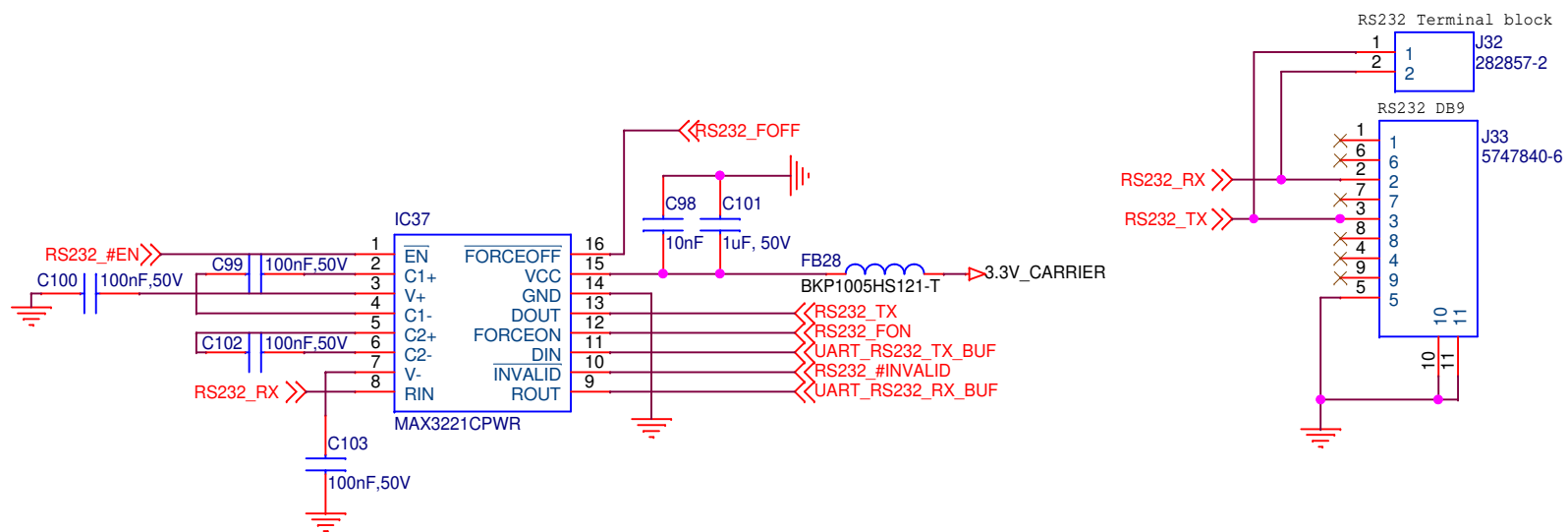


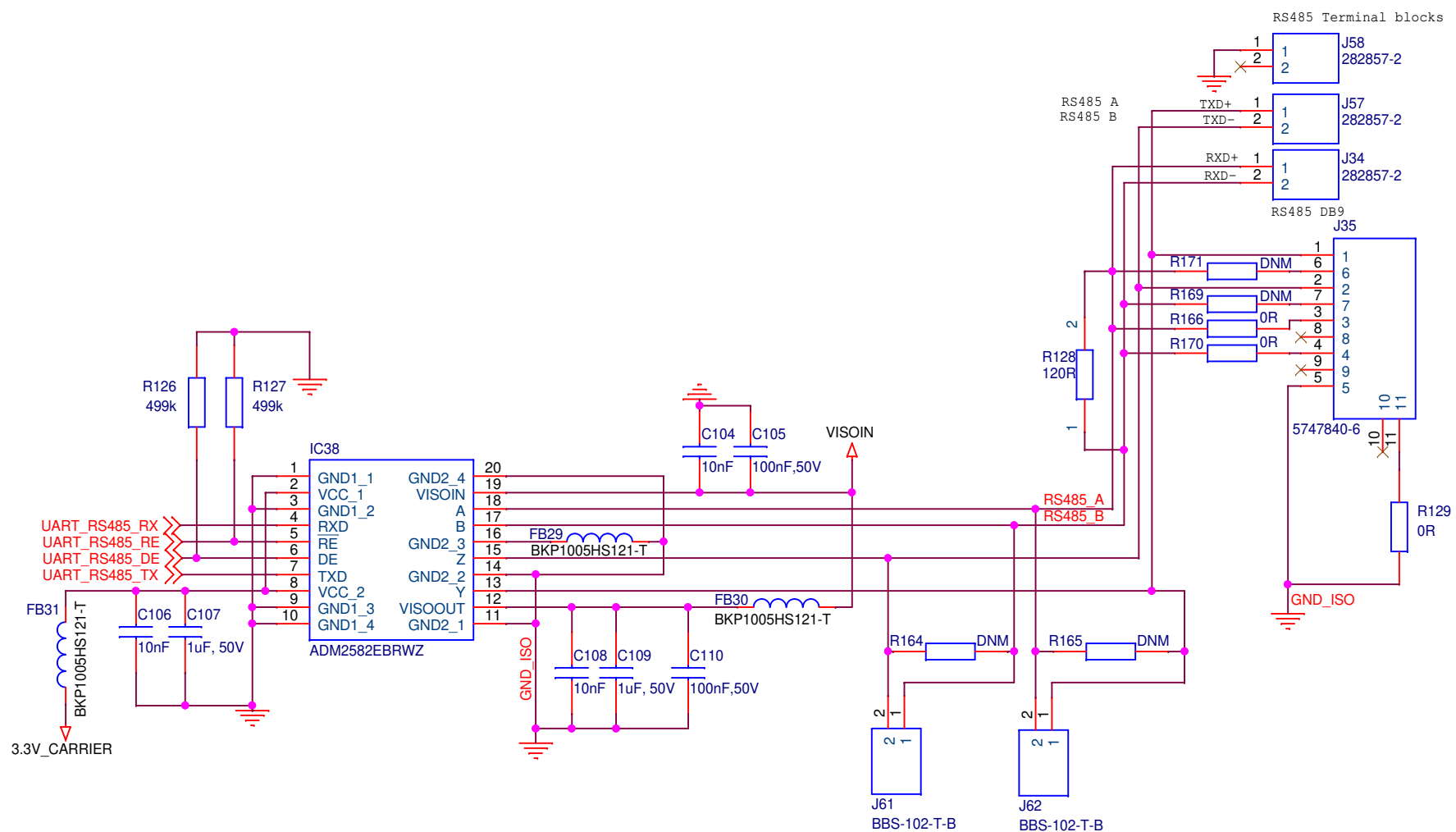


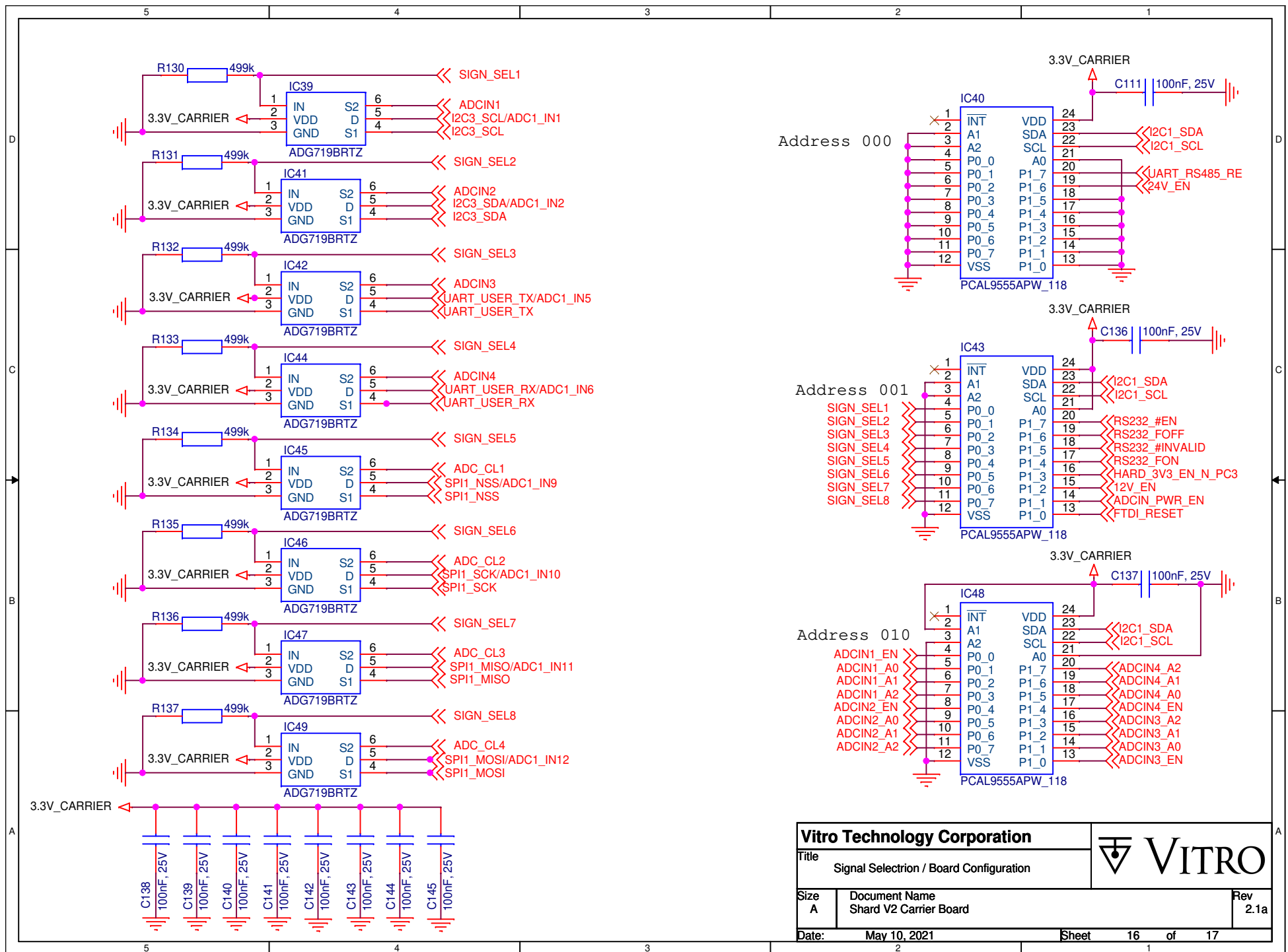
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Title	mini PCI-Express Interface	
Size	Document Name	Rev
A	Shard V2 Carrier Board	2.1a
Date:	May 10, 2021	Sheet 11 of 17












Vitro Technology Corporation		
Title Signal Selection / Board Configuration		
Size A	Document Name Shard V2 Carrier Board	Rev 2.1a
Date: May 10, 2021		Sheet 16 of 17

