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/intgr_t/deco0 UUUU   1101 0011 0111 0001 0110 0111 1101
/intgr_t/deco1 UUUU   1101 0011 0111 0010 0110 0111
/intgr_t/deco2 UUUU   1101 0011 0111 0010 0110
/intgr_t/deco3 UUUU   1101 0011 0111 0010
/intgr_t/deco4 UUUU   1101 0011 0111
/intgr_t/deco5 UUUU   1101 0011
/intgr_t/deco6 UUUU   1101
/intgr_t/debug 11010111011000100111100111101   11010111011000100111100111101
/intgr_t/count 0000 0001 0010 0011 0000 0101 0110 0000   0001 0010 0011 0100 0101 0110 0000

0 ps   2000000 ps   4000000 ps   6000000 ps   8000000 ps   10000000

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