



# Front-End ASICs for CZT and Si Multi-Element Detectors

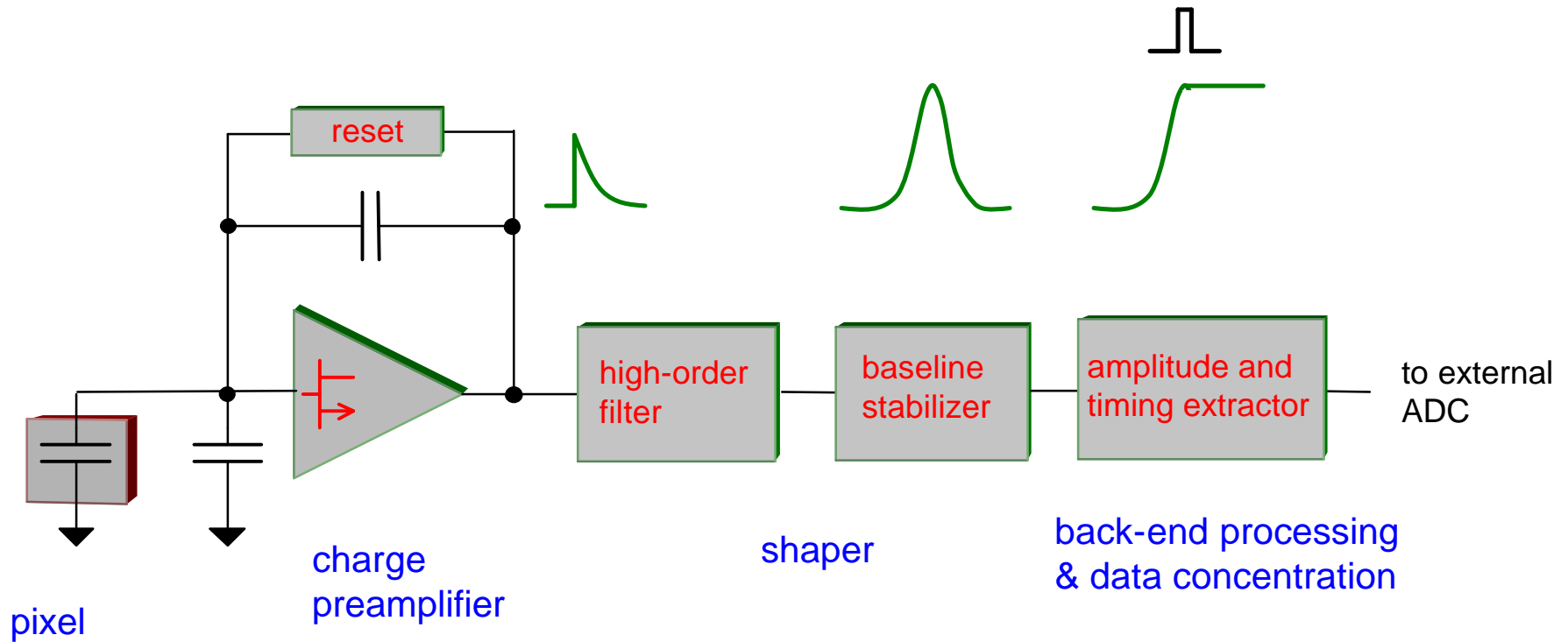
**Gianluigi De Geronimo**

*Microelectronics Group, Instrumentation Division, Brookhaven National Laboratory, Upton, NY*

# Outline

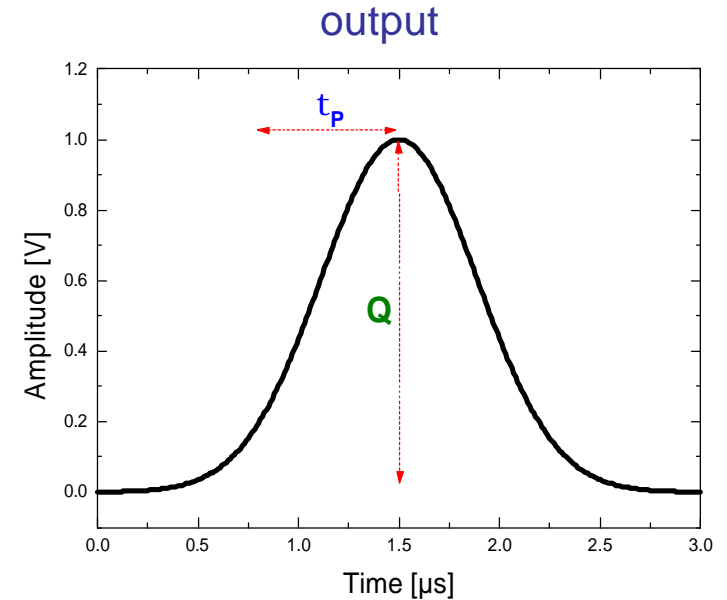
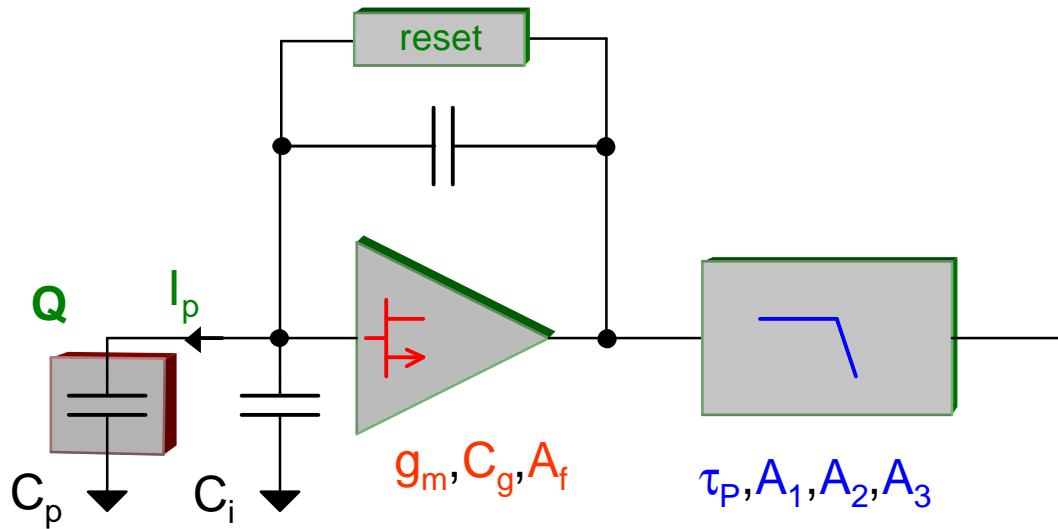
- I. Circuit Solutions
- II. ASICs for CdZnTe Sensors
- III. ASICs for Si Sensors

# Typical front-end channel



- high reliability
- ease of use
- spectroscopic quality
- data concentration optimization

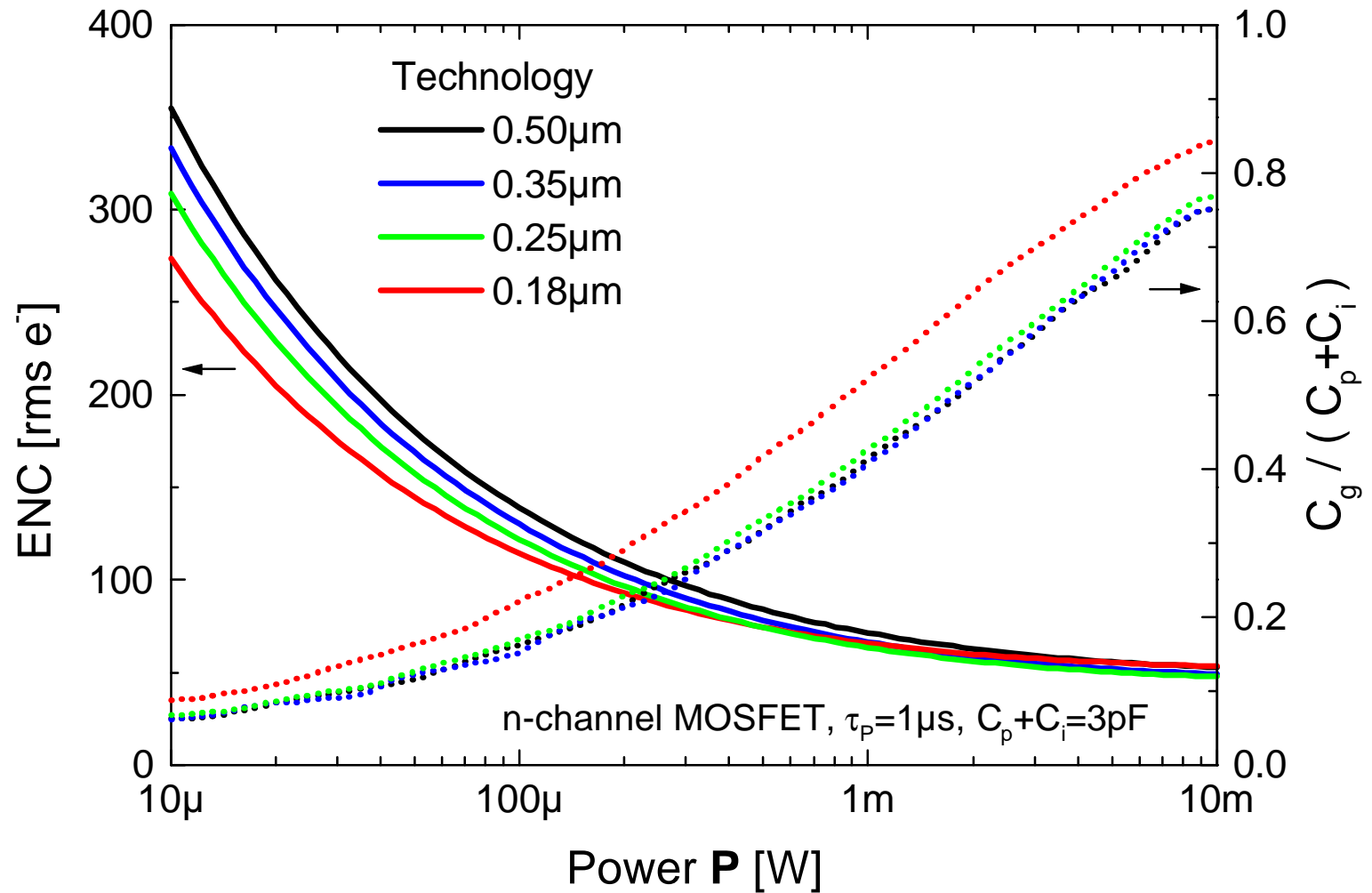
# Input MOSFET optimization



$$\text{ENC}^2 = \frac{A_1}{\tau_P} \frac{(C_p + C_i + C_g)^2}{g_m} + A_2 A_f (C_p + C_i + C_g)^2 + A_3 \tau_P (I_p + I_{rst})$$

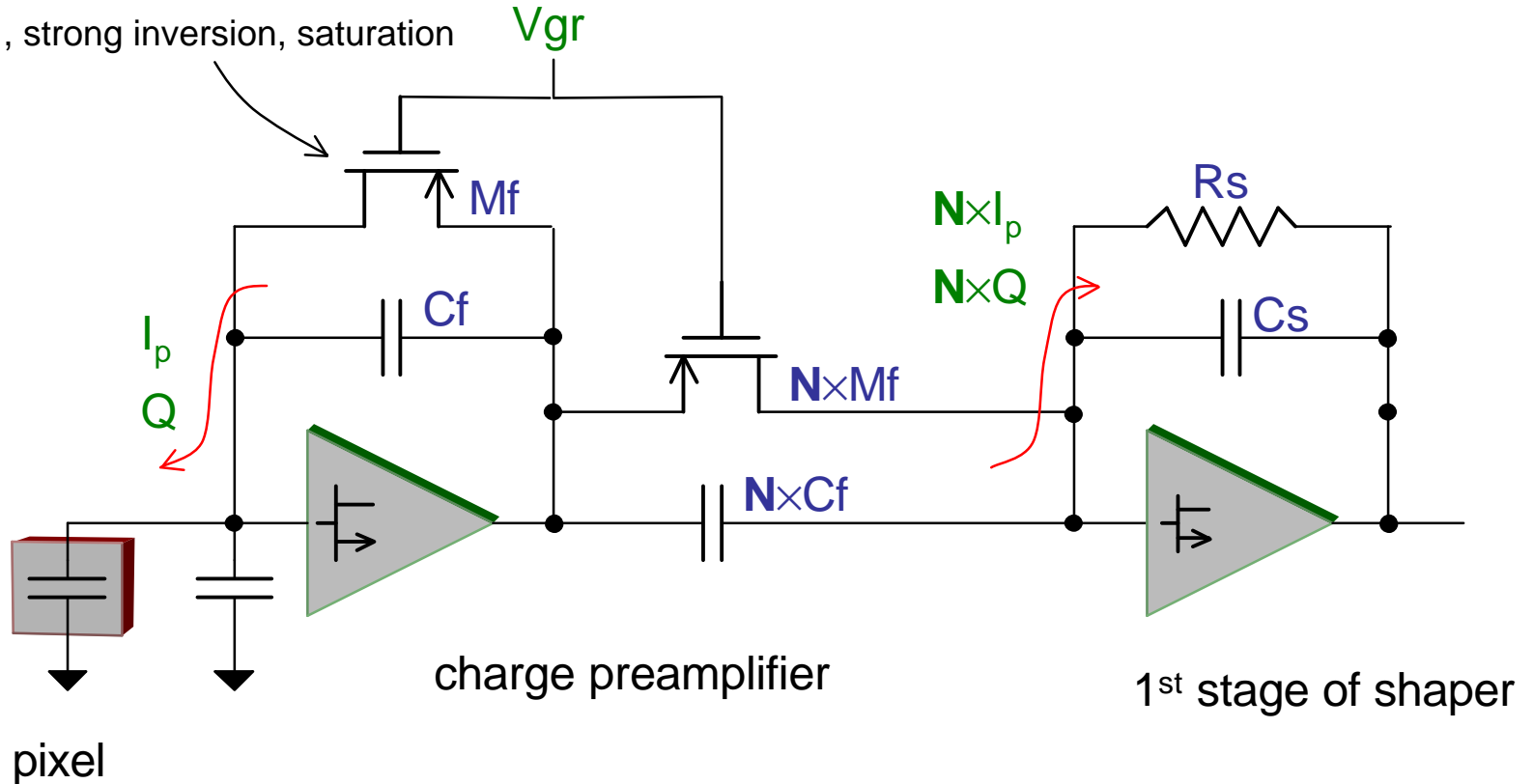
$g_m, C_g, A_f$ , are functions of input MOSFET width **W** and power **P**

# Input MOSFET optimization



# Continuous reset of the preamplifier

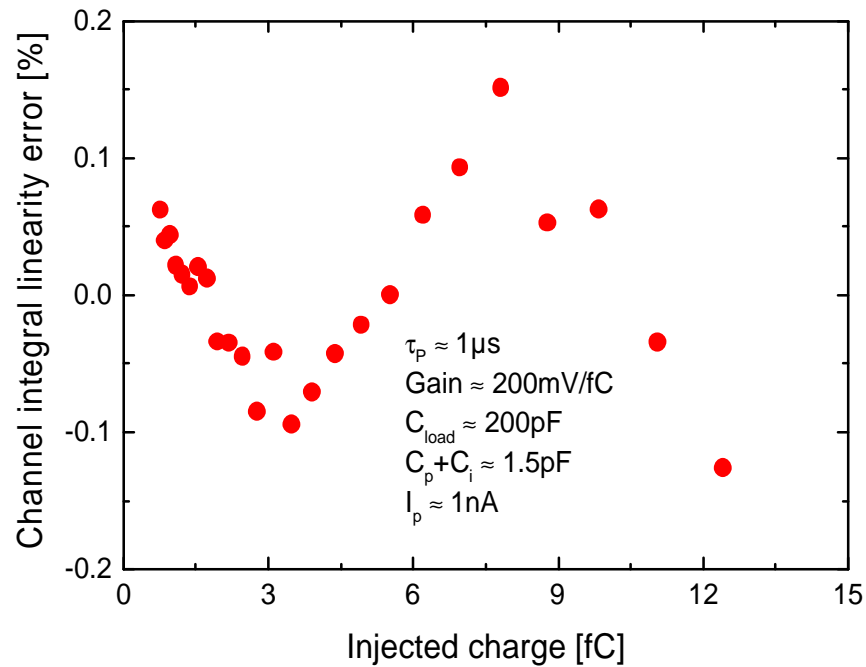
$L/W \gg 1$ , strong inversion, saturation



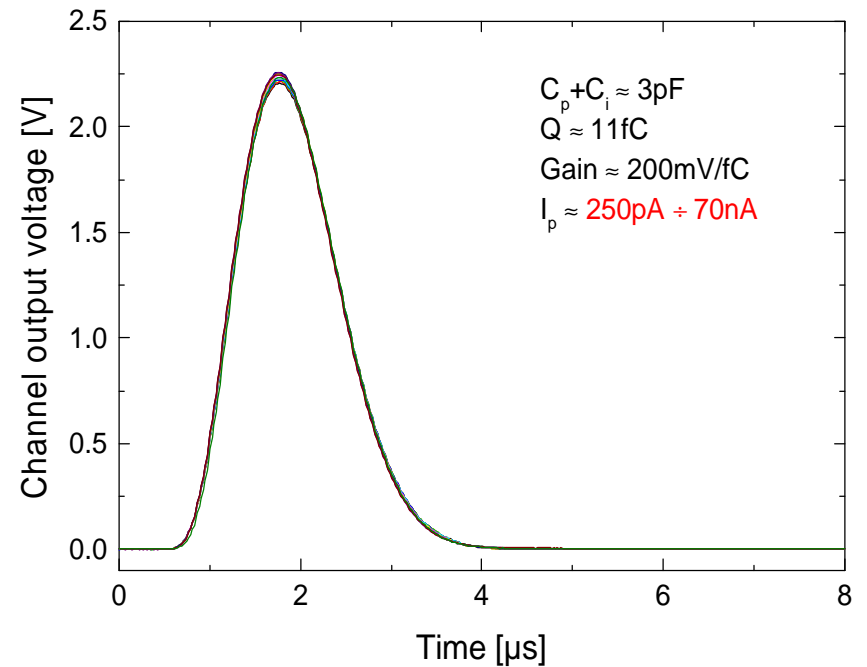
- current gain equal to  $N$
- fully linear
- **self-adapts** to leakage current
- minimum noise contribution

# Continuous reset of the preamplifier

linearity



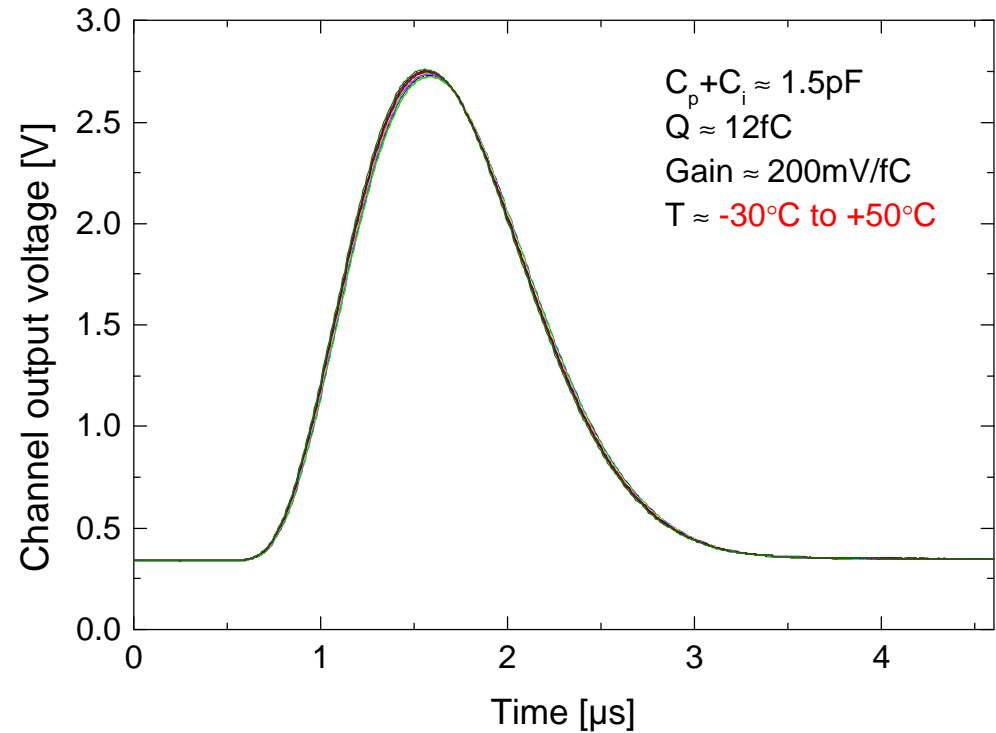
output vs pixel leakage current



# First generation of front-end ASICs

## other features

- plug & play
- per-channel test capacitor
- programmable gain
- programmable peaking time
- high output drive capability
- high stability vs temperature →







# Generation of front-end ASICs for CZT

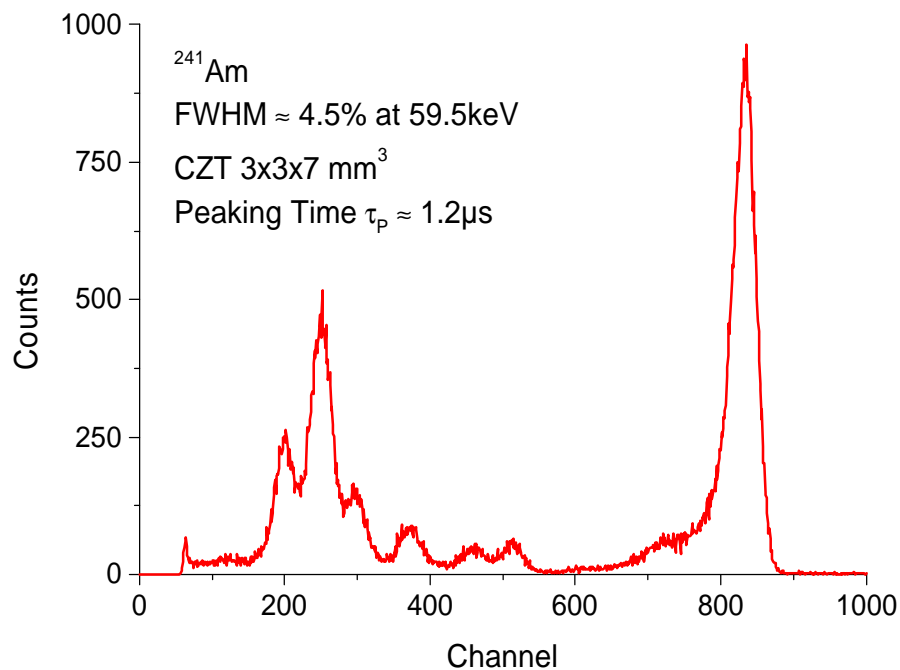


ASIC	Pixel capacitance [pF]	Channel count	Peaking time [ $\mu$ s]	Gain [mV/fC]	Power / channel [mW]	ENC [rms. e <sup>-</sup> ]	Applications
General purpose	3	16	0.6, 1.2, 2.0, 4.0	30, 50, 100, 200	18	30+20/pF	LFOV Gamma Camera SFOV Gamma Camera Nuclear Safeguards
Medium speed	3	4	0.4	200	18	29+27/pF	Down Hole Well Logging X-Ray Diffraction Gauges
High speed bipolar	3	8	0.2	240	18	42+44/pF	Bone Densitometry Pulse Mode CT Industrial X-Ray
High capacitance	12	8	0.6, 1.2, 2.0, 4.0	30, 50, 100, 200	35	57+10/pF	Industrial Strip Detectors Backscatter Gauges Large Area Detector

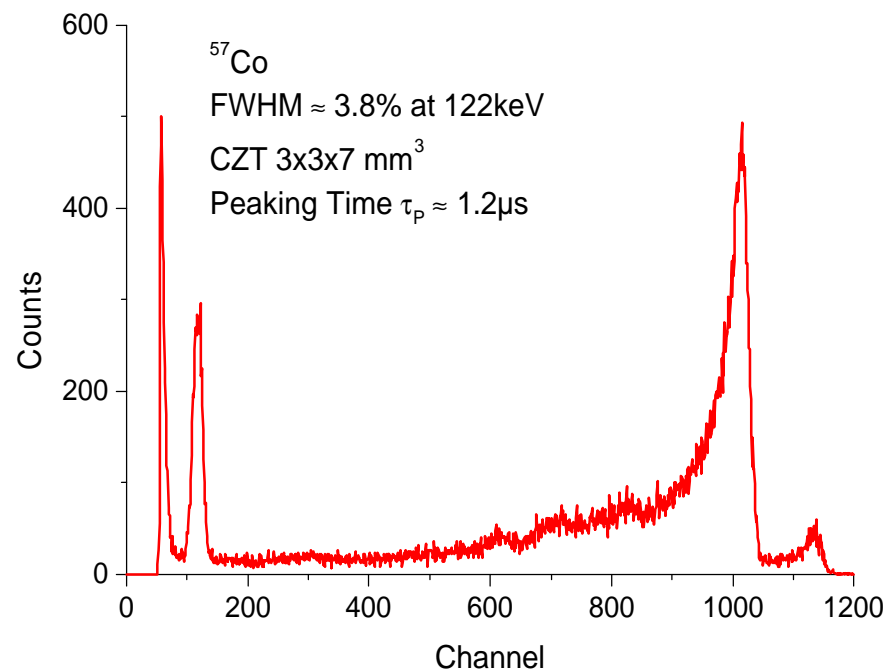
Technology : 0.5 $\mu$ m CMOS SP3M

# CZT – ASIC spectra measurements

<sup>241</sup>Am spectrum

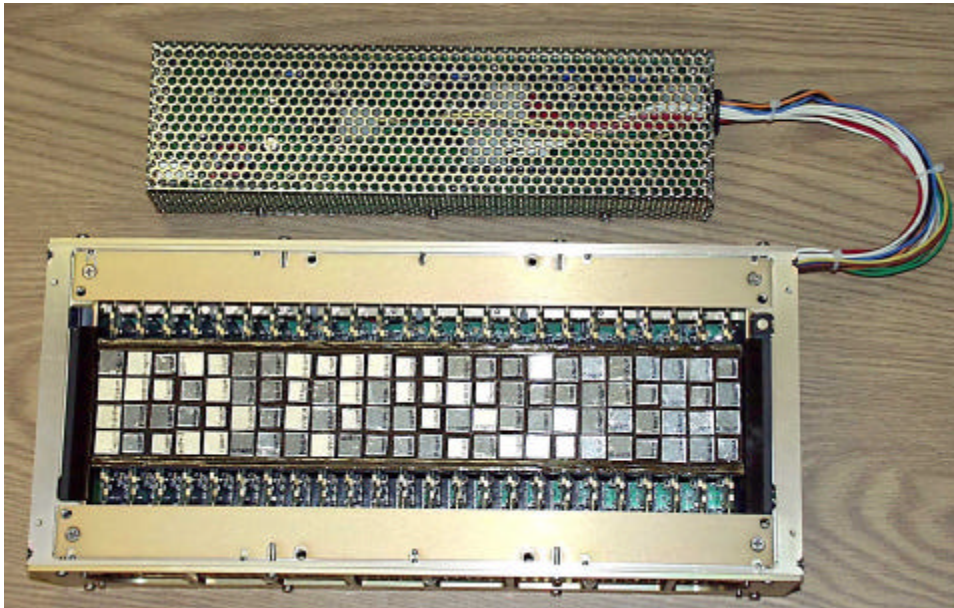


<sup>57</sup>Co spectrum



# CZT – ASIC applications

Solstice Gamma camera



- 96 CZT crystals
- 3072 pixels
- 192 front-end ASICs
- 1.3M events/second
- average FWHM 3.8% at 122keV

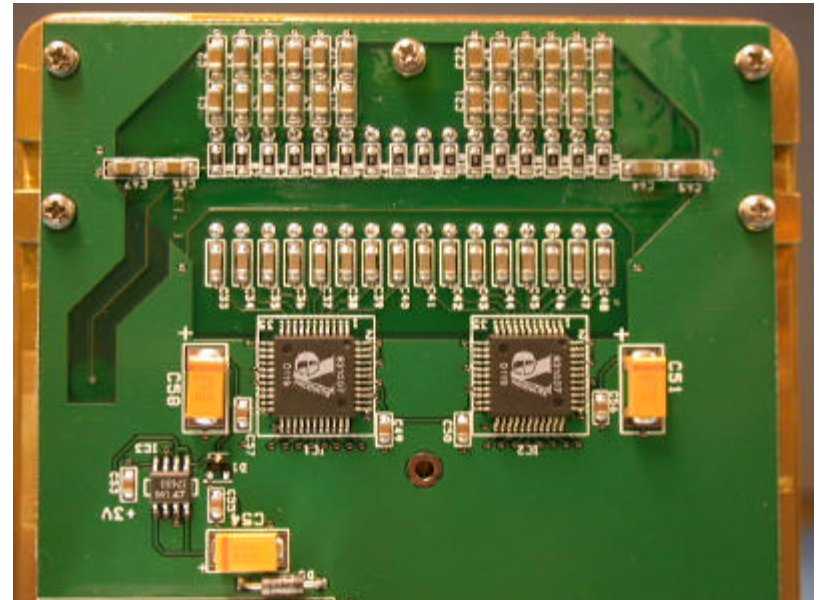
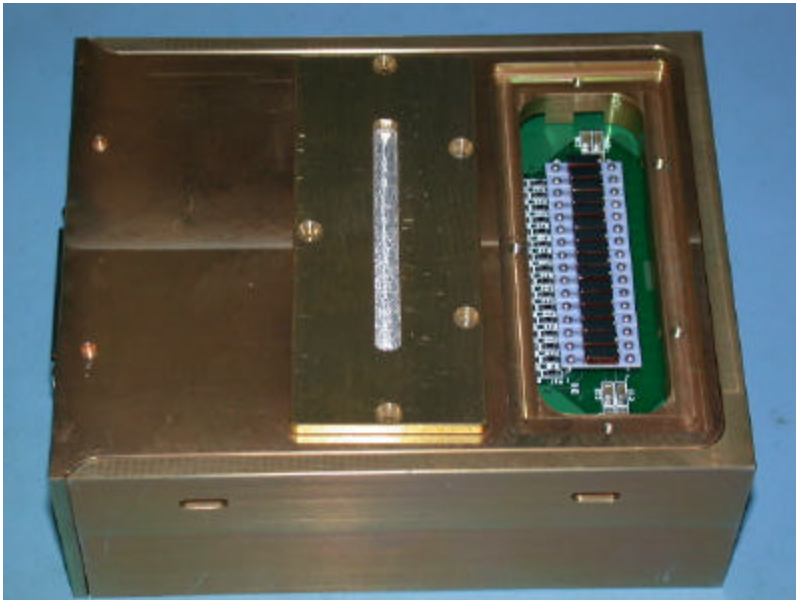
eZ-SCOPE hand held Gamma camera



- 1 CZT crystal
- 256 pixels
- 16 front-end ASICs
- 4.8M events/second
- average FWHM 4.0% at 122keV

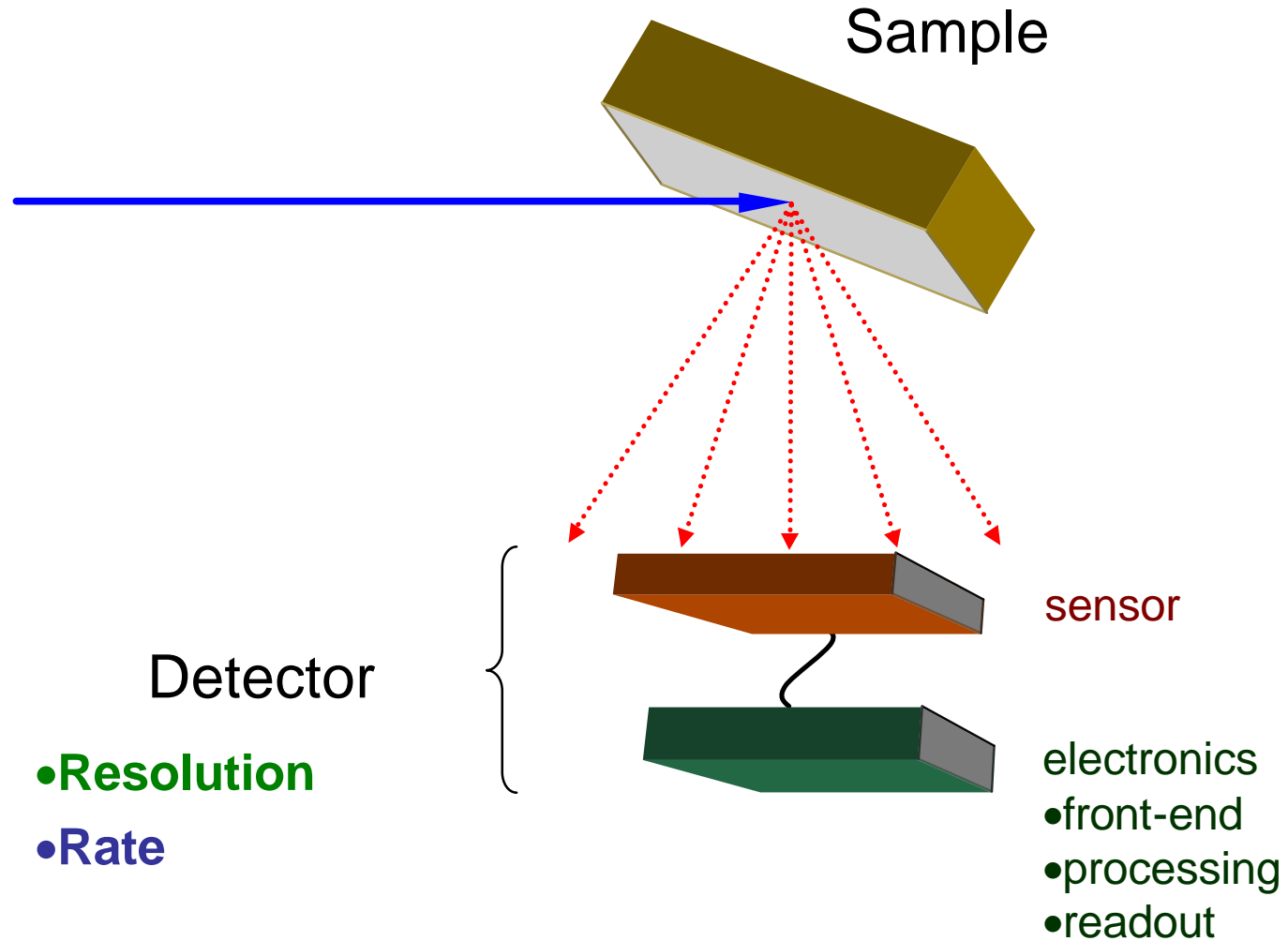
# CZT – ASIC applications

## Bone Densitometry – GE Lunar Detector

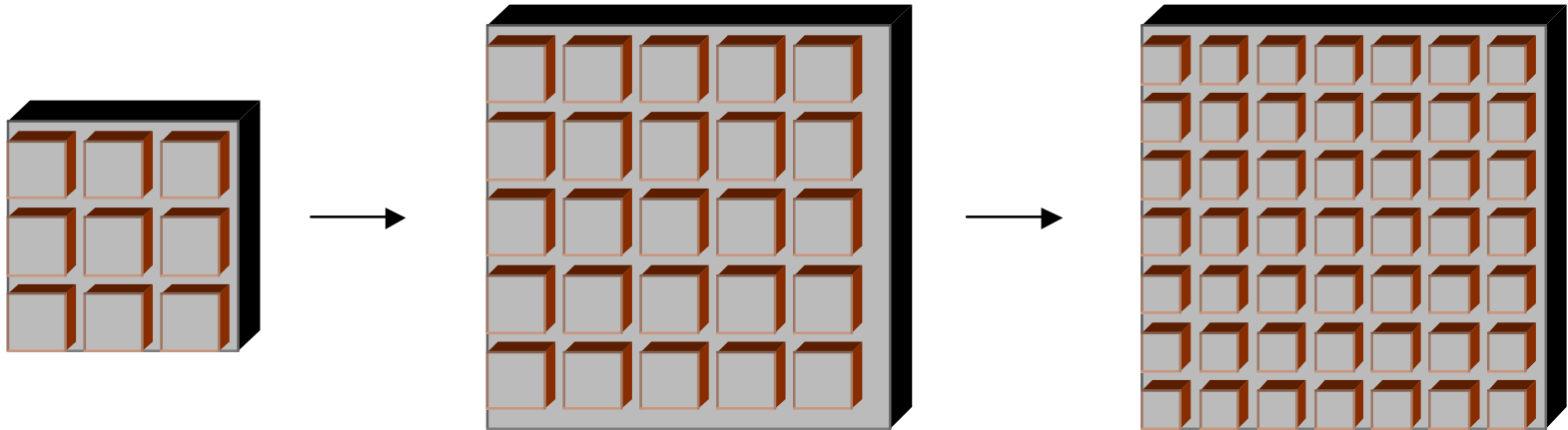


- 16 CZT crystals
- 16 pixels 3 x 7 x 3 mm<sup>3</sup>
- 2 front-end ASICs
- DEXA (Dual Energy X-ray Absorptiometry)
- ASICs replaced 17 circuit boards (over 500 components) and improved performances

# Typical fluorescence EXAFS measurement geometry



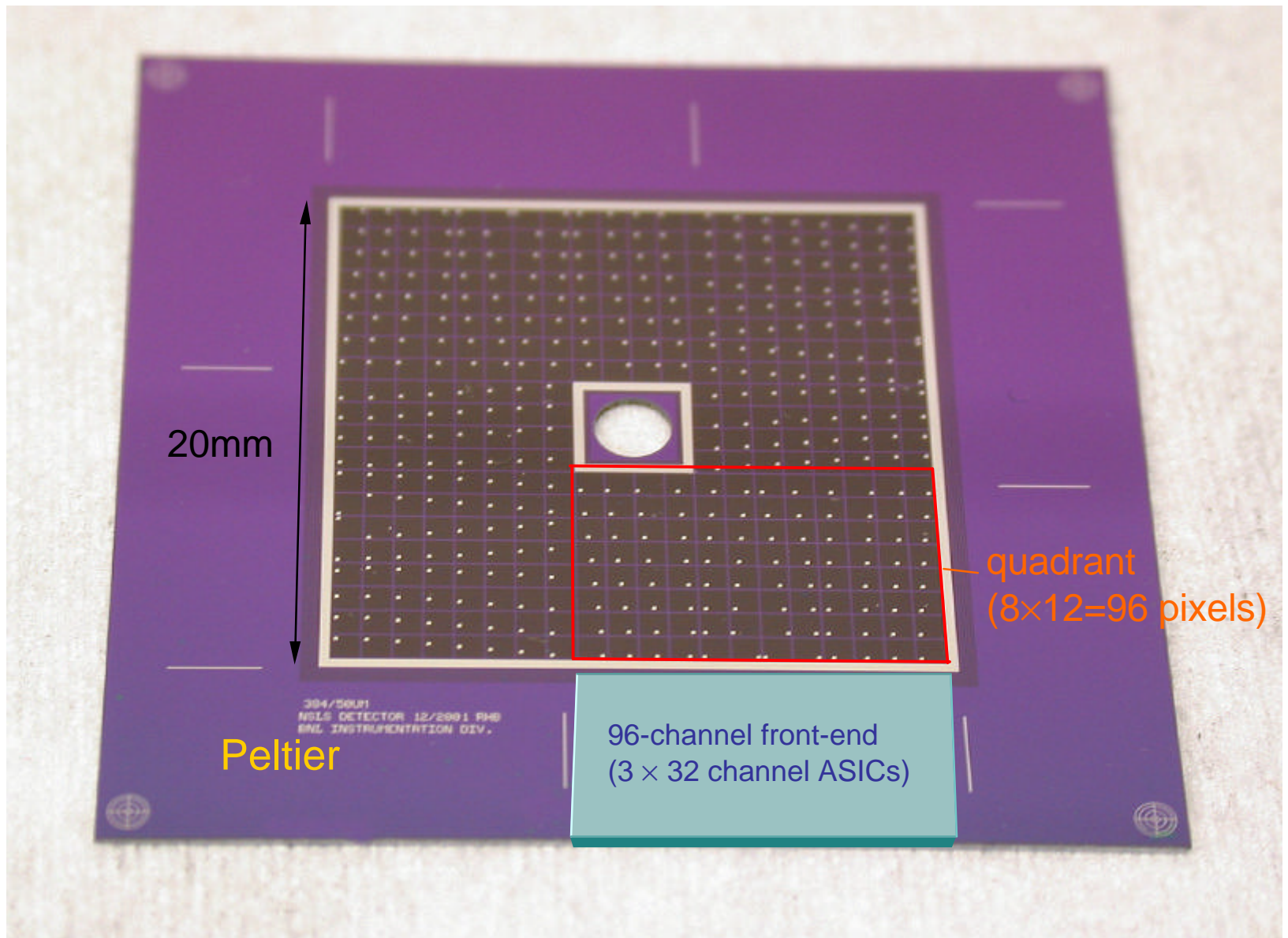
## Optimum pixellation



$$\text{ENC} \div \sqrt{\left(\mathbf{C_p(N)} + \mathbf{C_i}\right)^{\gamma+1} \frac{\frac{\text{Rate}}{\mathbf{N}}}{\left(\frac{\mathbf{P}}{\mathbf{N}} - p_2\right)^{\gamma}}} \quad \gamma = 0.5 \dots 1$$

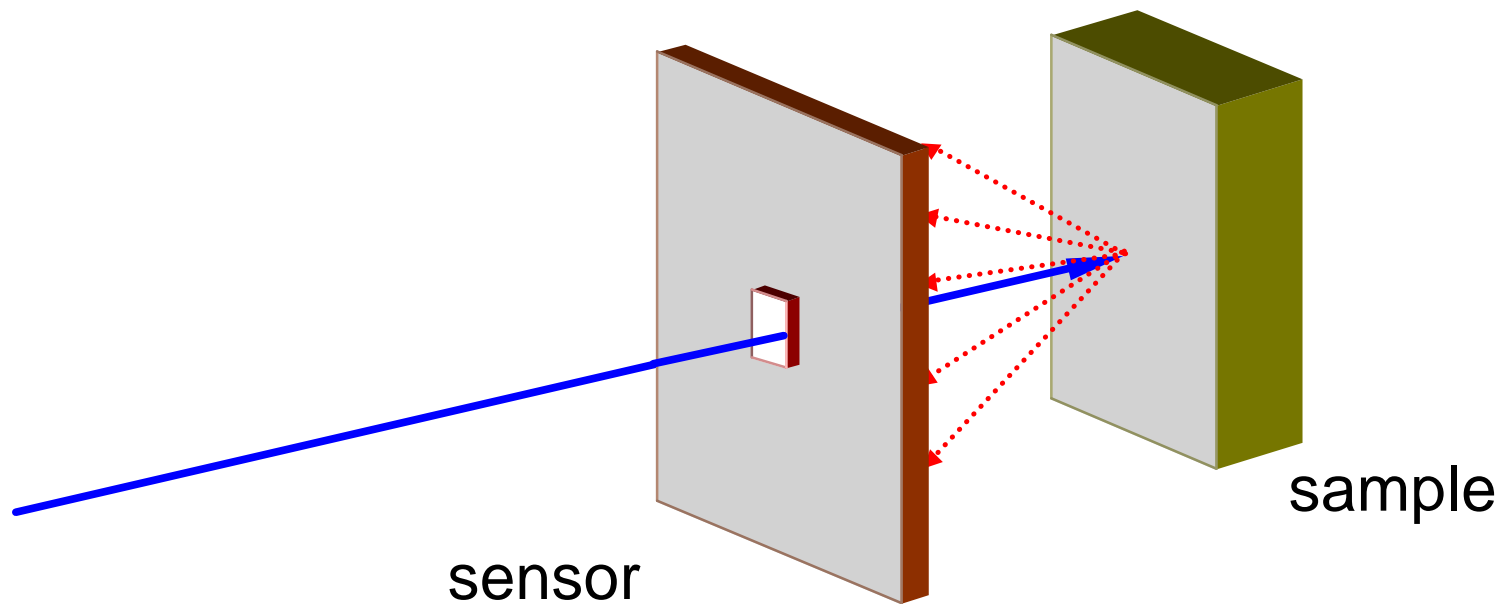
- charge sharing ( $\approx 20\mu\text{m}/\text{side}$ ) and trapping (gap/side) : empirical





Si n-type high resistivity wafer 250 $\mu$ m thick,  
N = 384 p<sup>+</sup>  $\approx$  1mm $\times$ 1mm pixels,  
gaps 10 $\mu$ m, 30 $\mu$ m, 50 $\mu$ m

## Beam through



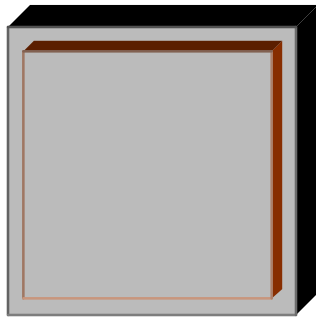


## Sensor – ASIC photo

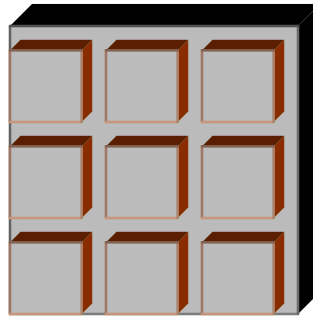


*quadrant*

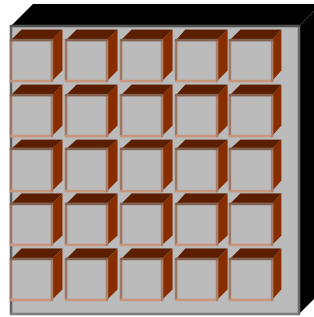
# Highly segmented detectors



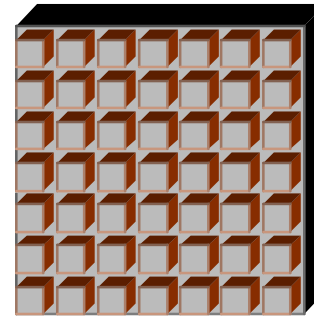
$N=1$



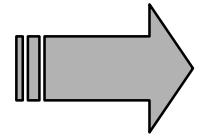
$N=9$



$N=25$



$N=49$



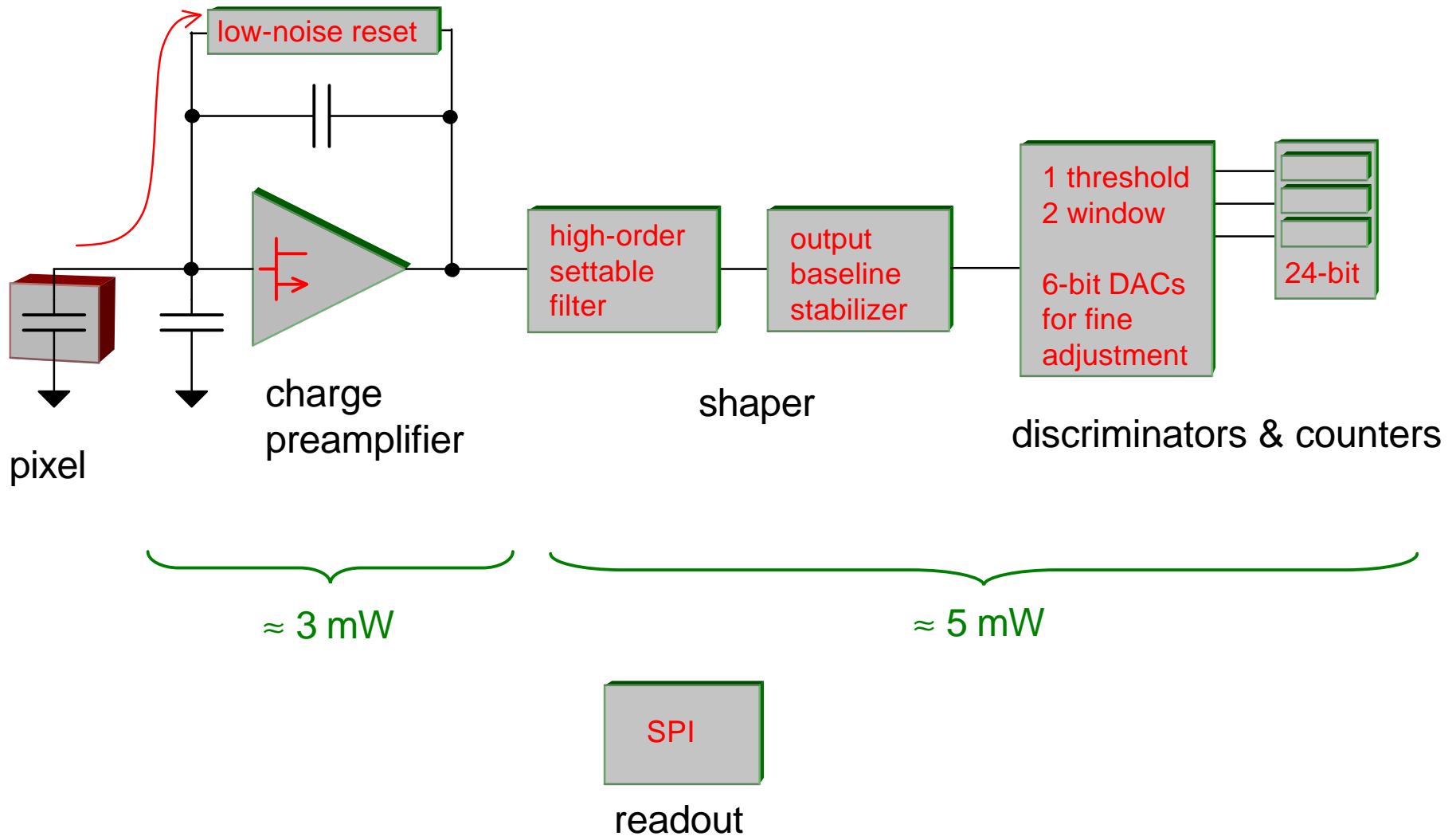
## **Benefits:**

- Position Resolution
  - pixel pitch  $\sim 1/\sqrt{N}$
- Energy Resolution:
  - $C_{\text{DET}} \sim 1/N$
  - $I_{\text{DARK}} \sim 1/N$
  - Pulse Shaping time  $\sim N$
- Rate capability
  - pileup  $\sim 1/N$

## **Drawbacks:**

- Interconnect density
  - density  $\sim N$
- Electronics channel count
  - cost  $\sim N$
  - power  $\sim N$

# Front-end channel overview



Technology CMOS 0.35 $\mu\text{m}$  3.3V 2P4M



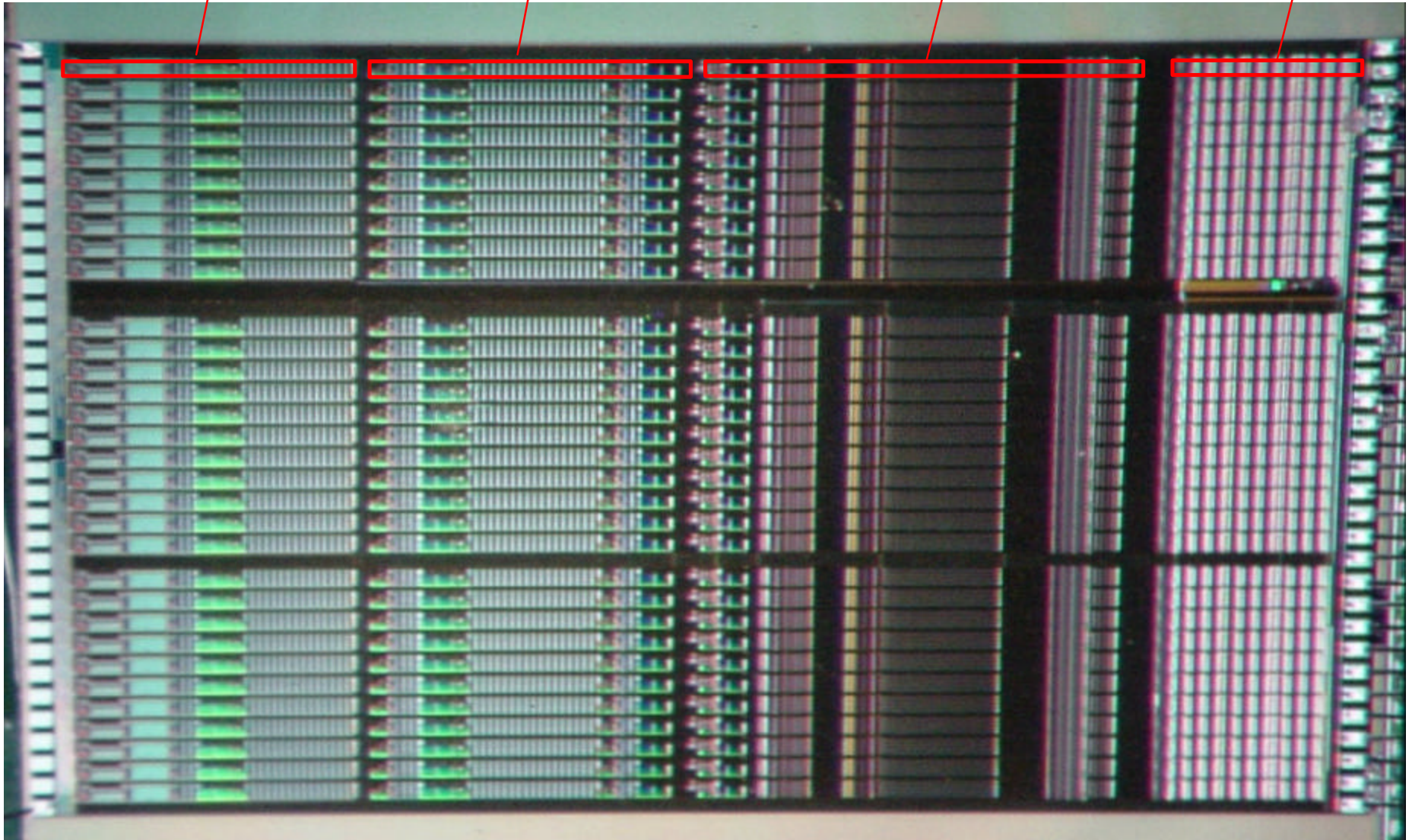
## ASIC photo

charge preamplifier

shaper with BLH

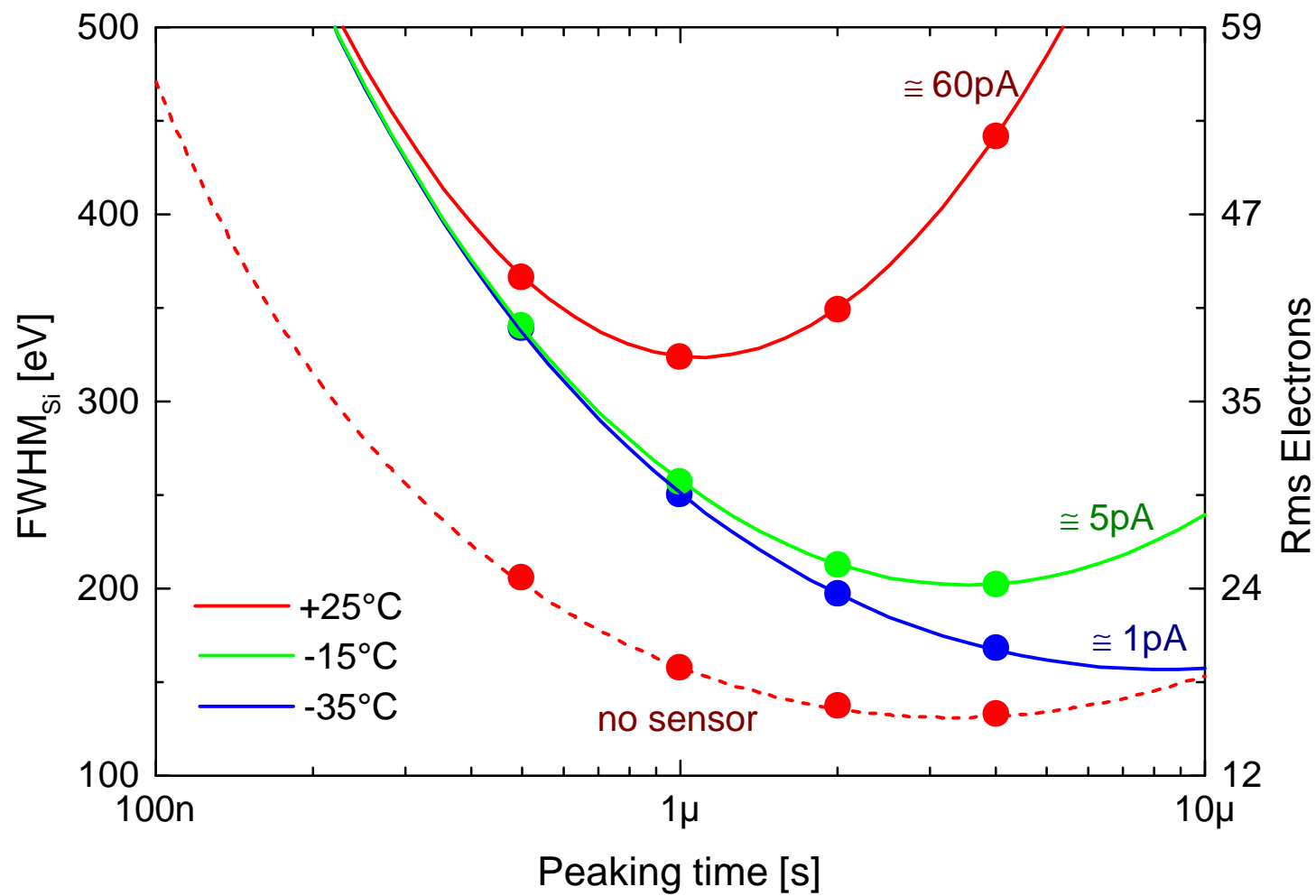
discriminators and DACs

counters

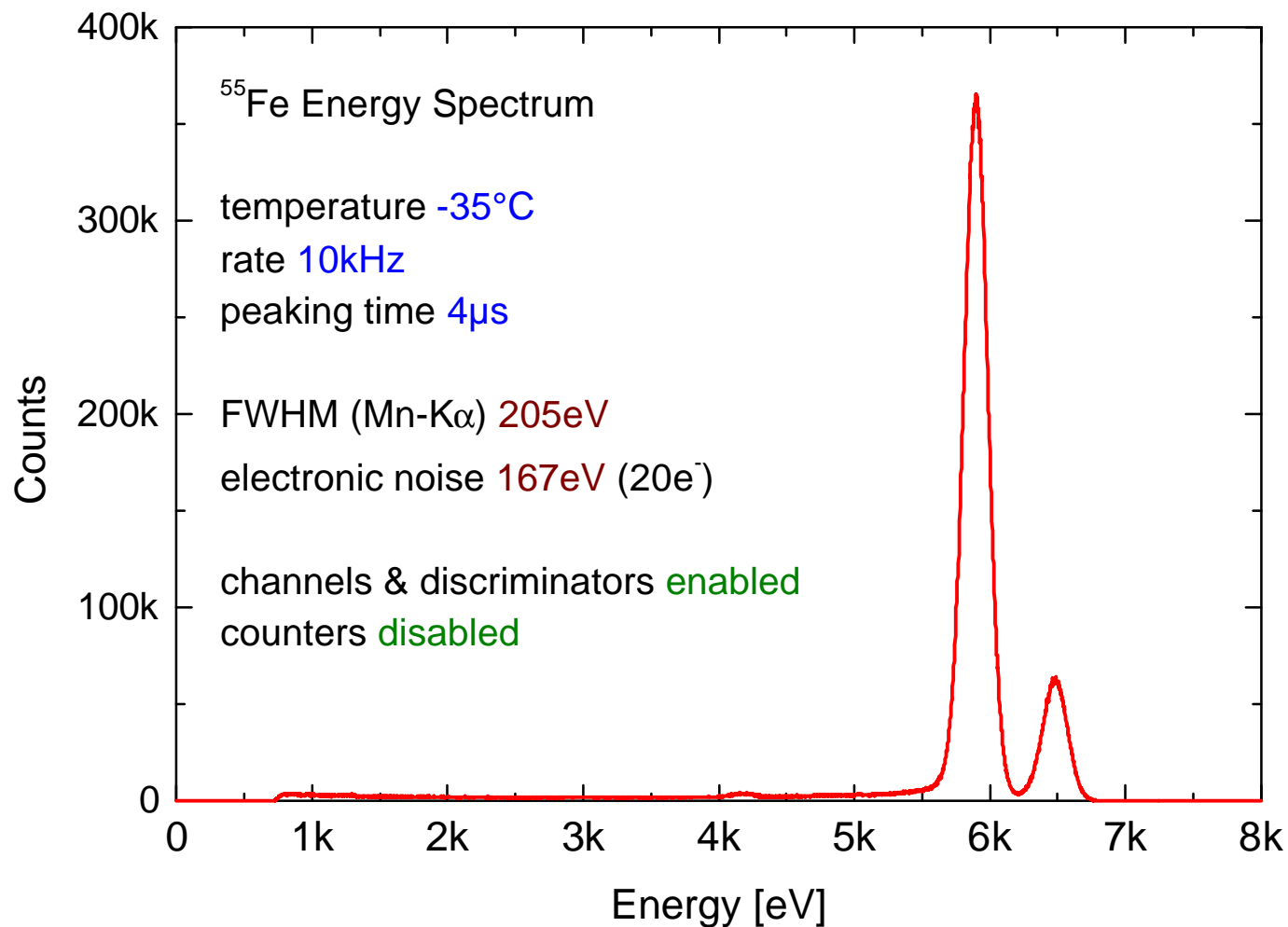


*32 channels, 3.6 × 6.3 mm<sup>2</sup>*

# Energy resolution



# Si – ASIC spectra measurements



# Current EXAFS detector



head - *preamplifiers*

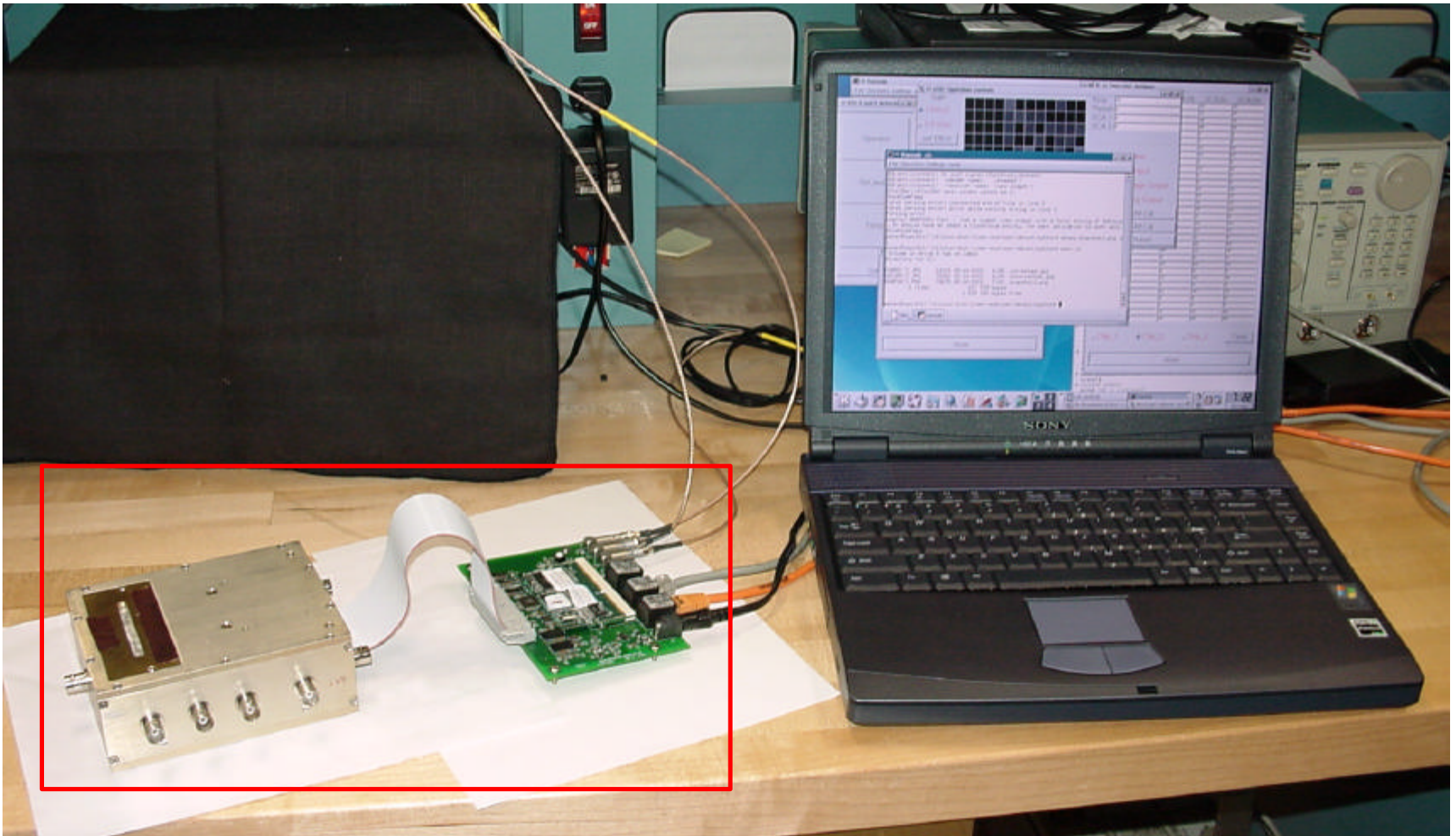
» 100 channels,  $> 350$  eV,  $< 1$  MHz



rack - *shapers ...*



# New EXAFS detector



» 400 channels, < 300 eV, > 10MHz