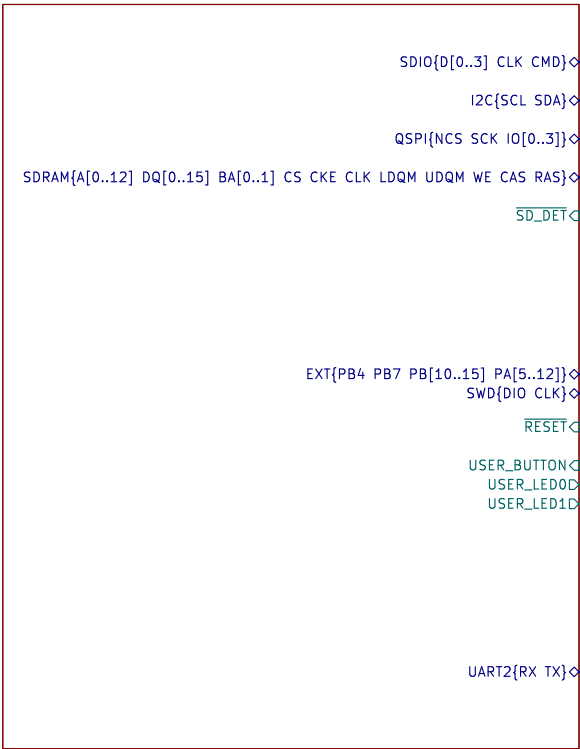


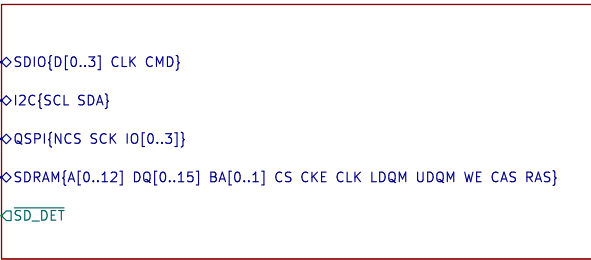


MCU



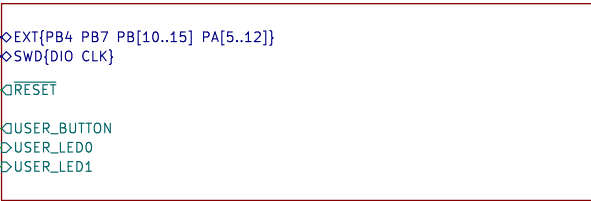
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Memories



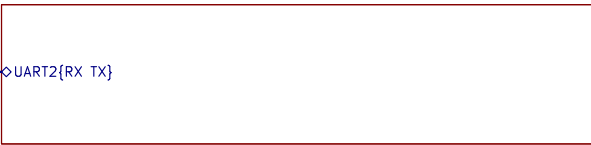
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Low level IO



File: low\_level\_io.kicad\_sch

UART



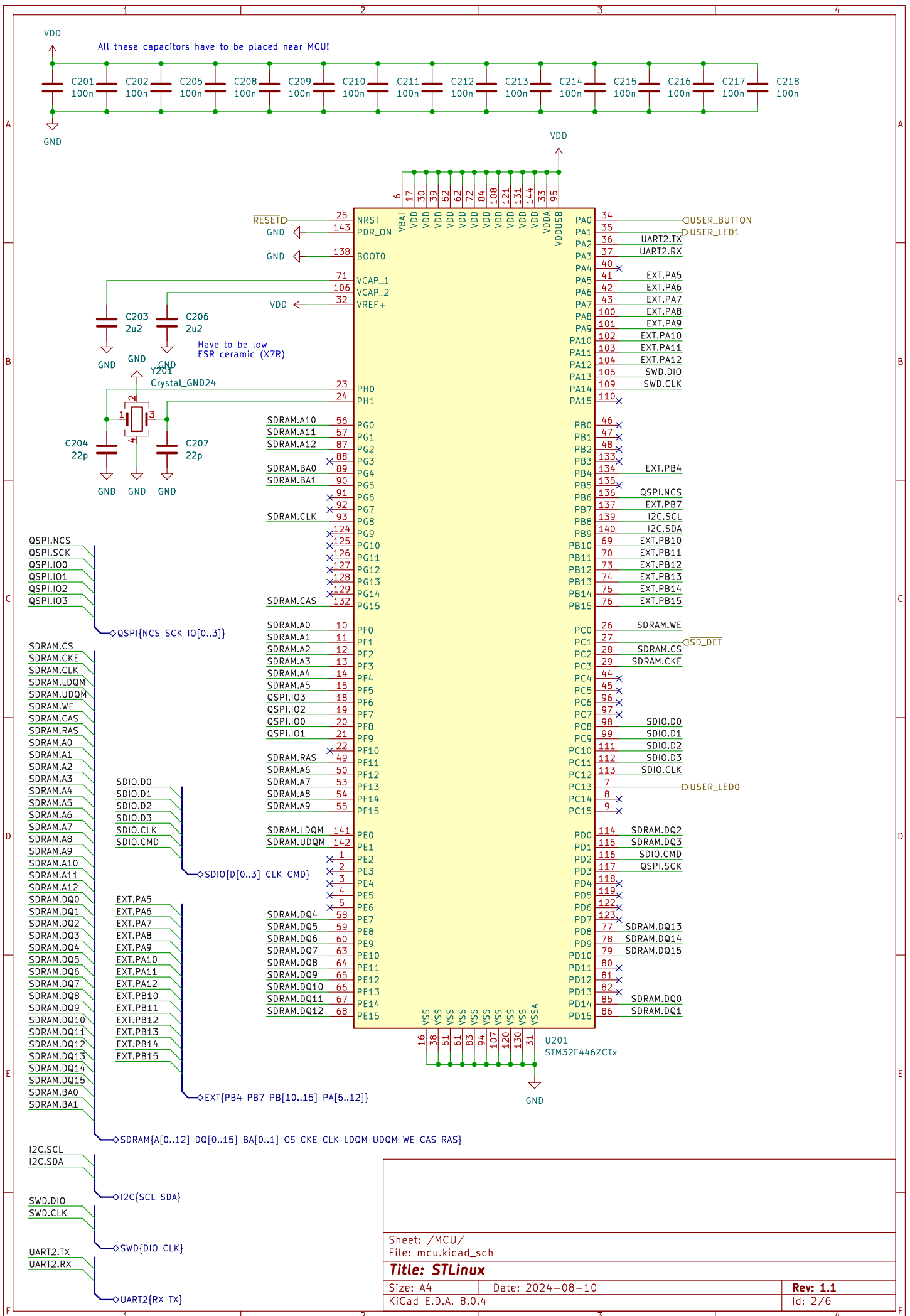
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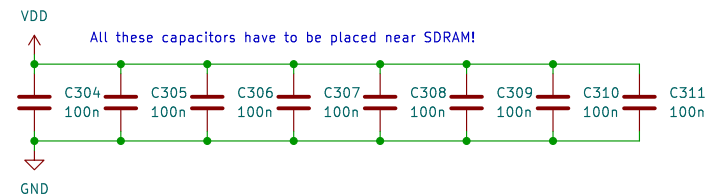
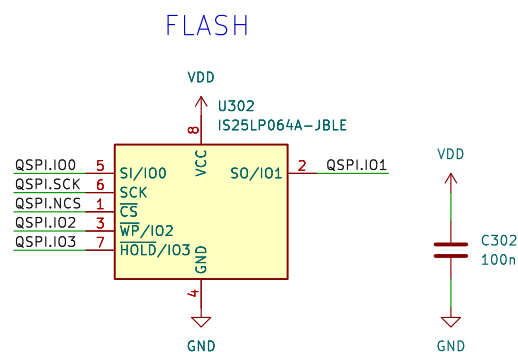
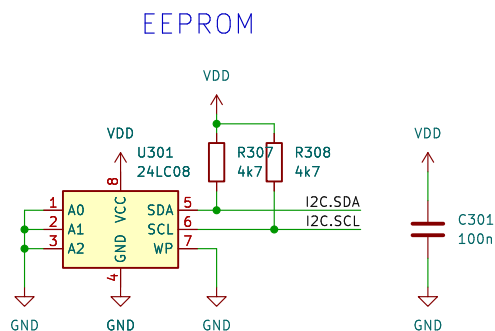
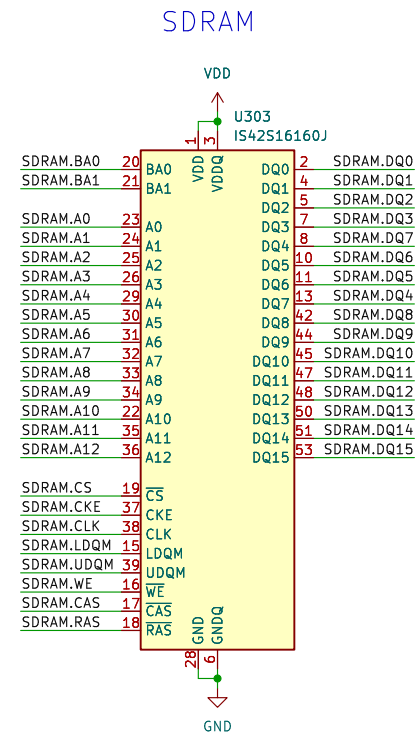
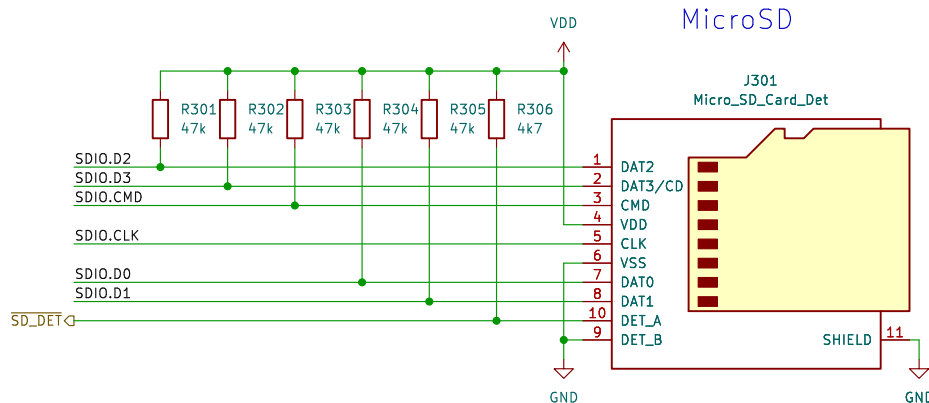
Power



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Sheet: /		
File: stlinux.kicad_sch		
Title: <b>STLinux</b>		
Size: A4	Date: 2024-08-10	Rev: 1.1
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I2C.SCL  
I2C.SDA  
I2C{SCL SDA}

SDRAM.CS  
SDRAM.CKE  
SDRAM.CLK  
SDRAM.LDQM  
SDRAM.UDQM  
SDRAM.WE  
SDRAM.CAS  
SDRAM.RAS  
SDRAM.A0  
SDRAM.A1  
SDRAM.A2  
SDRAM.A3  
SDRAM.A4  
SDRAM.A5  
SDRAM.A6  
SDRAM.A7  
SDRAM.A8  
SDRAM.A9  
SDRAM.A10  
SDRAM.A11  
SDRAM.A12  
SDRAM.DQ0  
SDRAM.DQ1  
SDRAM.DQ2  
SDRAM.DQ3  
SDRAM.DQ4  
SDRAM.DQ5  
SDRAM.DQ6  
SDRAM.DQ7  
SDRAM.DQ8  
SDRAM.DQ9  
SDRAM.DQ10  
SDRAM.DQ11  
SDRAM.DQ12  
SDRAM.DQ13  
SDRAM.DQ14  
SDRAM.DQ15  
SDRAM.BA0  
SDRAM.BA1

QSPI.NCS  
QSPI.SCK  
QSPI.I00  
QSPI.I01  
QSPI.I02  
QSPI.I03  
QSPI{NCS SCK IO[0..3]}

SDIO.D0  
SDIO.D1  
SDIO.D2  
SDIO.D3  
SDIO.CLK  
SDIO.CMD  
SDIO{D[0..3] CLK CMD}

SDRAM{A[0..12] DQ[0..15] BA[0..1] CS CKE CLK LDQM UDQM WE CAS RAS}

Sheet: /Memories/  
File: memories.kicad\_sch

**Title: STLinux**

Size: A4 Date: 2024-08-10

KiCad E.D.A. 8.0.4

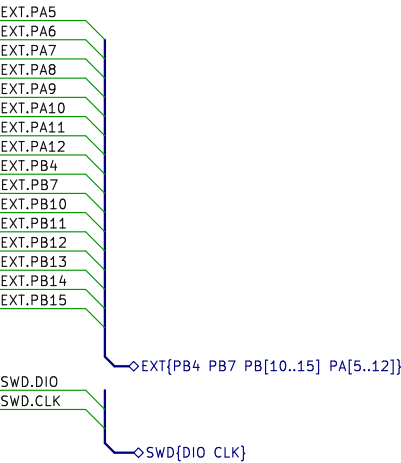
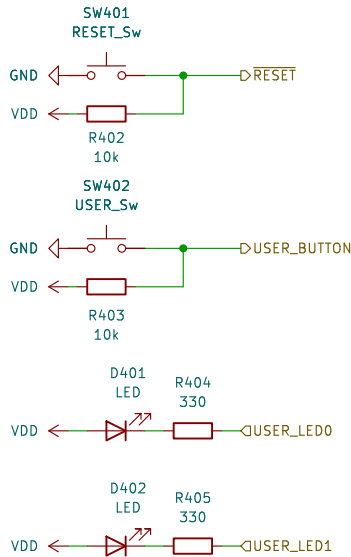
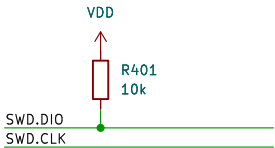
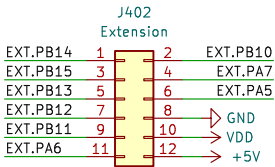
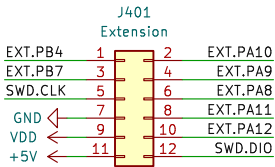
**Rev: 1.1**

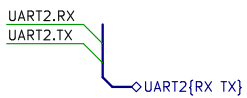
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Extension AF capabilities

Extensions capability is provided by J401 and J402. These can be used to connect various serial based devices. Capabilities with pin names are written down in following table. Part of J401 is also SWD debugging interface. For more information about alternative functions please refer to Cube project attached to the project.

Pin	CAN	I2C	I2S	SPI	USART
PA5				SPI1_SCK	
PA6			I2S2_MCK	SPI1_MISO	
PA7				SPI1_MOSI	
PA8		I2C3_SCL			USART1_CK
PA9		I2C3_SDA	I2S2_CK		USART1_TX
PA10					USART1_RX
PA11	CAN1_RX				USART1_CTS
PA12	CAN1_TX				USART1_RTS
PB4			I2S2_WS		
PB7					
PB10		I2C2_SCL	I2S2_CK		USART3_TX
PB11		I2C2_SDA			USART3_RX
PB12	CAN2_RX	I2C2_SMBA	I2S2_WS	SPI2_NSS	USART3_CK
PB13	CAN2_TX		I2S2_CK	SPI2_SCK	USART3_CTS
PB14				SPI2_MISO	USART3_RTS
PB15			I2S2_SD	SPI2_MOSI	





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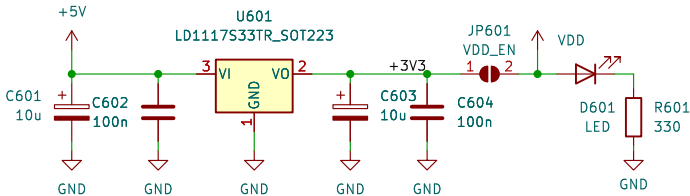
# Power config

Board can be powered from two sources.

- 1. USB connector
- 2. Pinheaders

USB connector is by default used for UART1 using FTDI converter. Using solder bridge it is possible to switch between self-powered and bus-powered configuration. (Also change this in FTDI internal EEPROM!)

When FTDI is set to self-powered one have to provide power using headers. This power can be directly 3.3V and then jumper JP601 have to be open, or you can provide 5V and use voltage regulator to deliver 3.3V. It regulator is in use JP601 have to be closed.



Sheet: /Power/  
File: power.kicad\_sch

## Title:

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