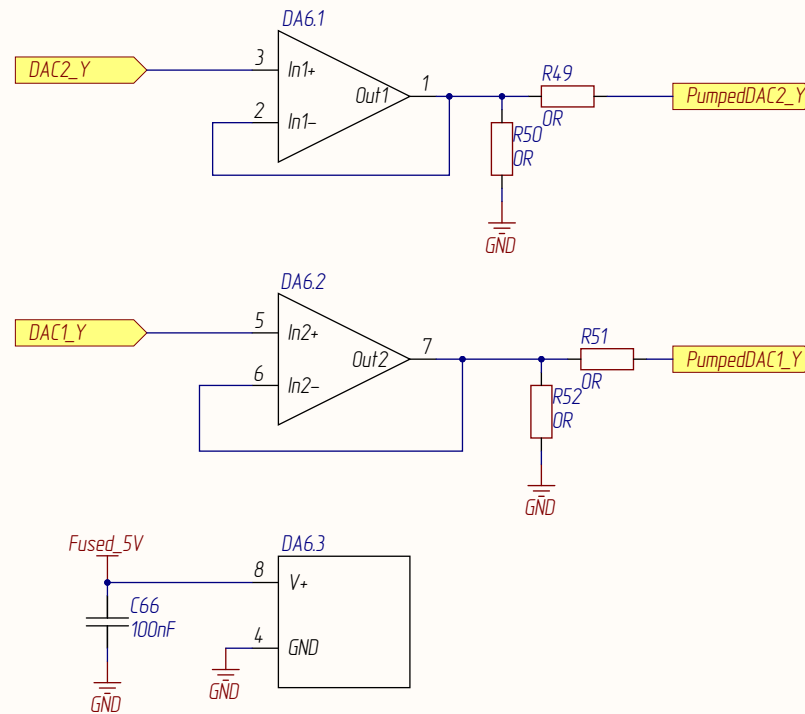


Децимальный номер

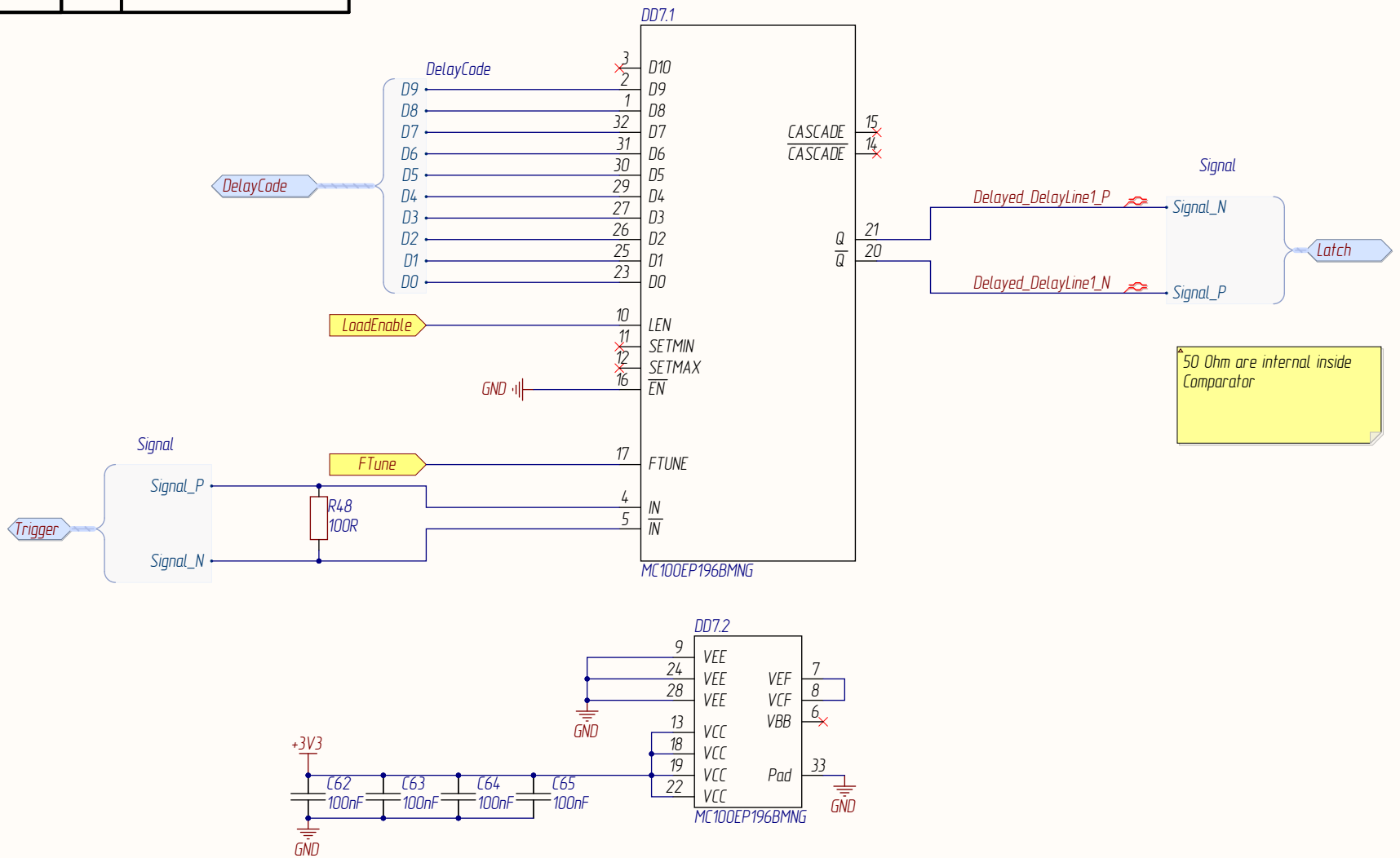


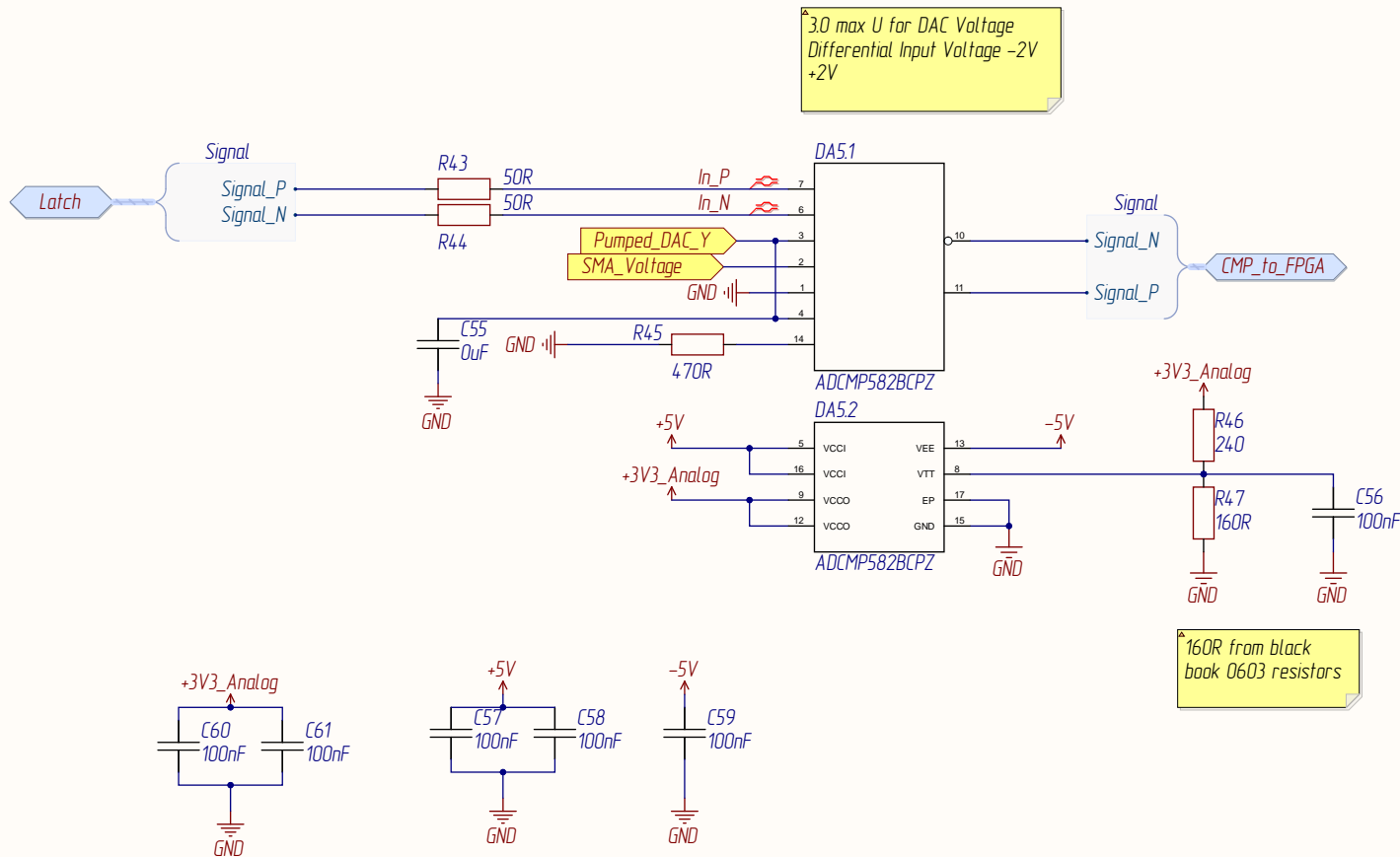
Инов.№ подл.	Подп.и дата	Взам.инв.№	Инов.№ дубл.	Подп. и дата

Изм.	Лист	№ докум.	Подп.	Дата

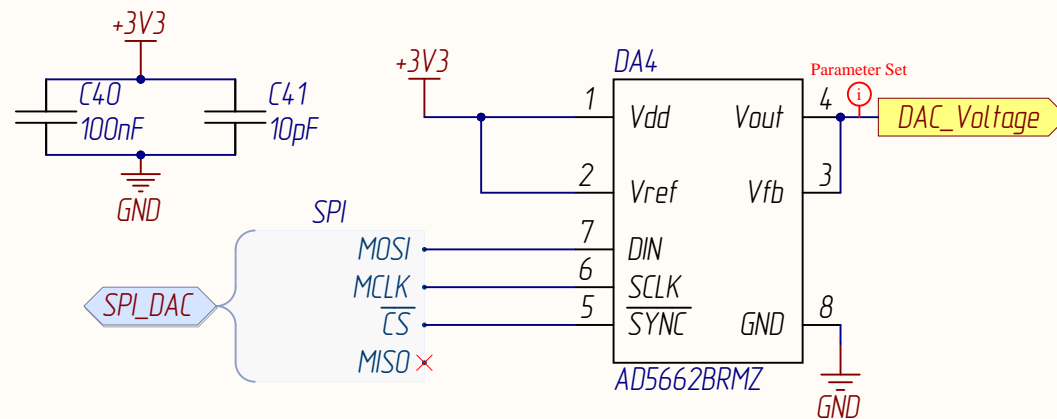
Децимальный номер

Лист
1



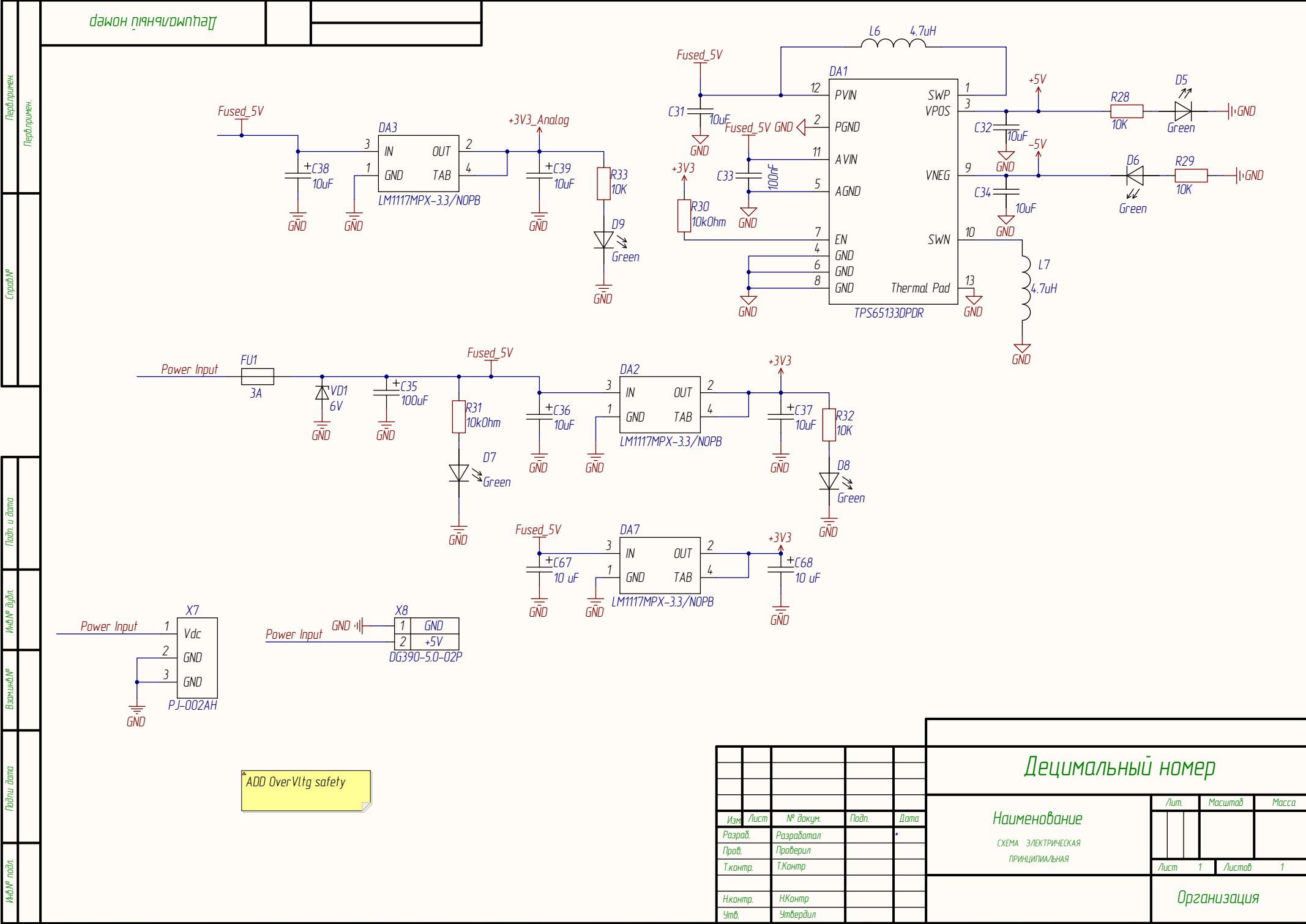


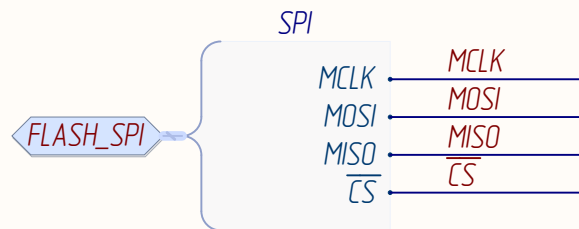




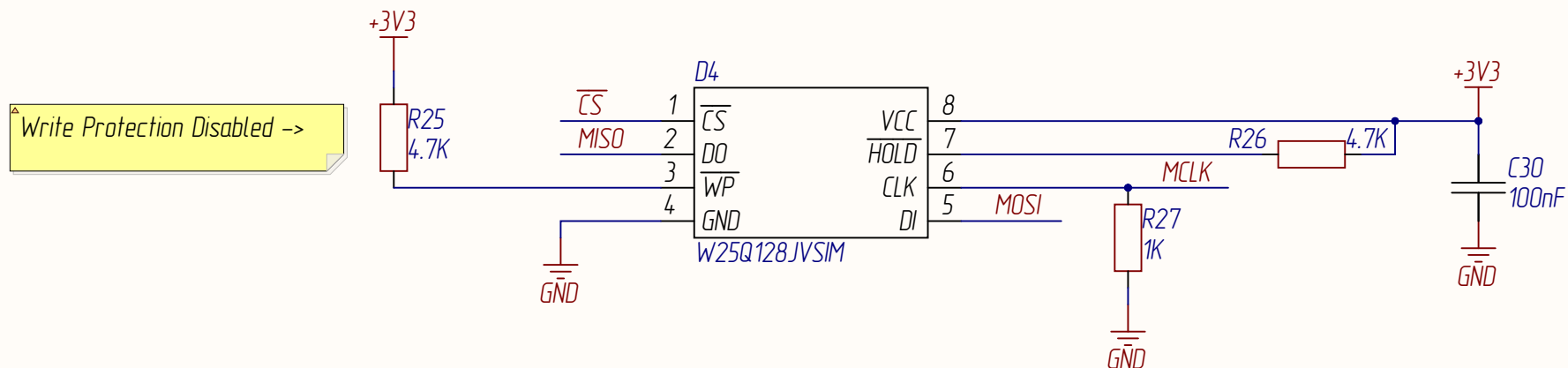
Or just connect
Vdd to Vref and
delete left parts D:

Title <i>Наименование</i>			<div>Altium®</div>
Size: A4	Number:*	Revision:*	
Date: 04.06.2022	Time: 20:54:00	Sheet 1 of 1	
File: C:\Users\Vlad\Documents\GitHub\CalibratorPCB\Mainboard\DAC.SchDoc			

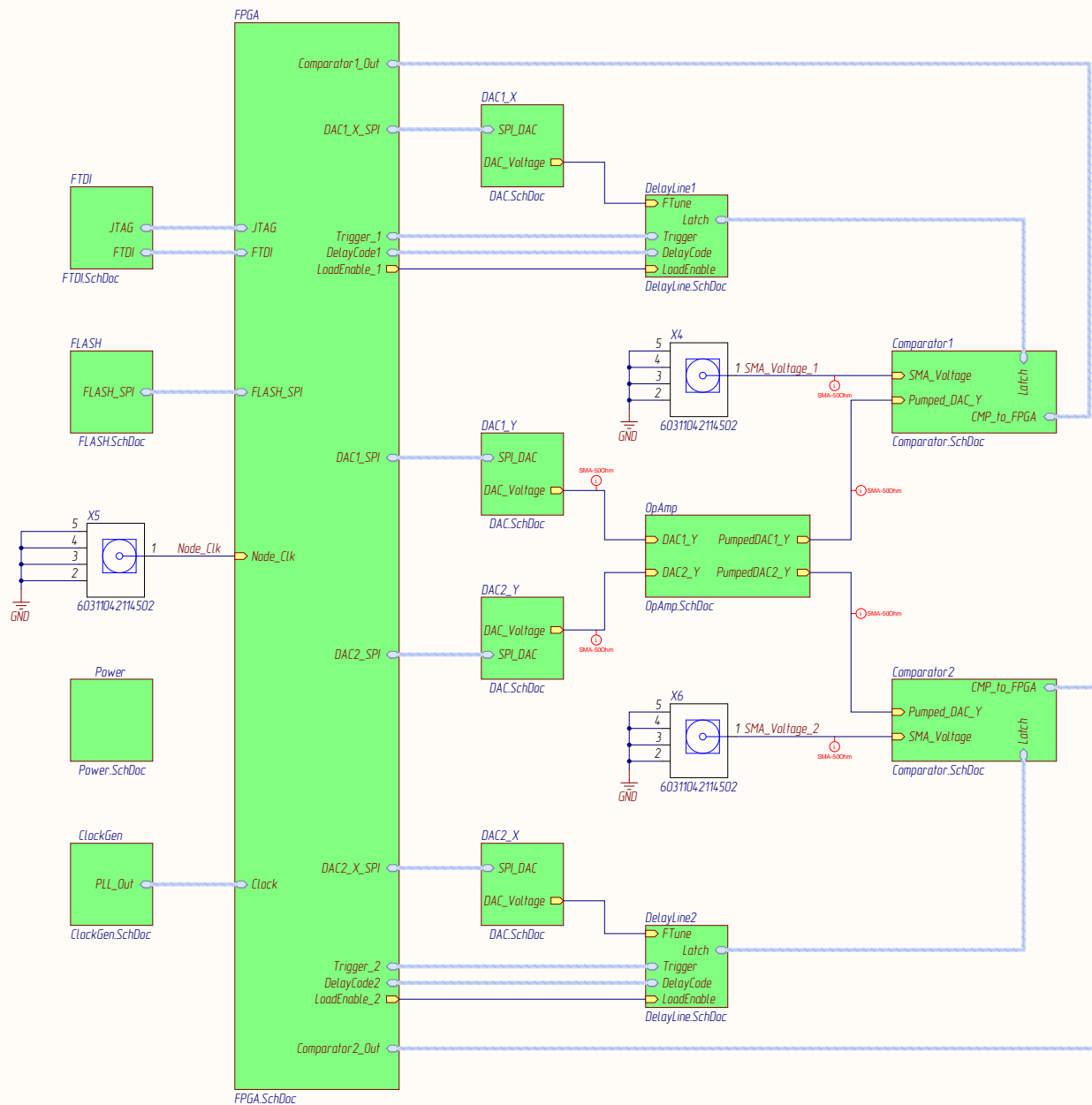


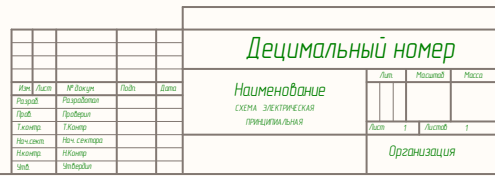
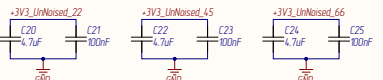
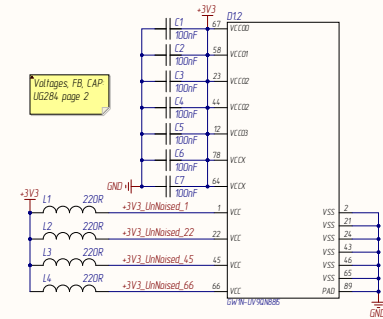
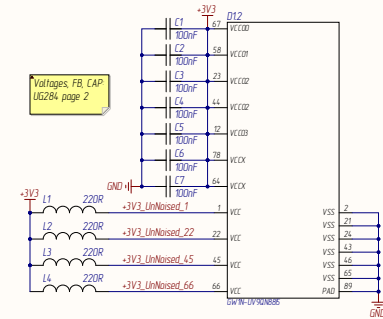
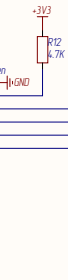
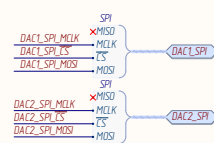
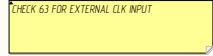


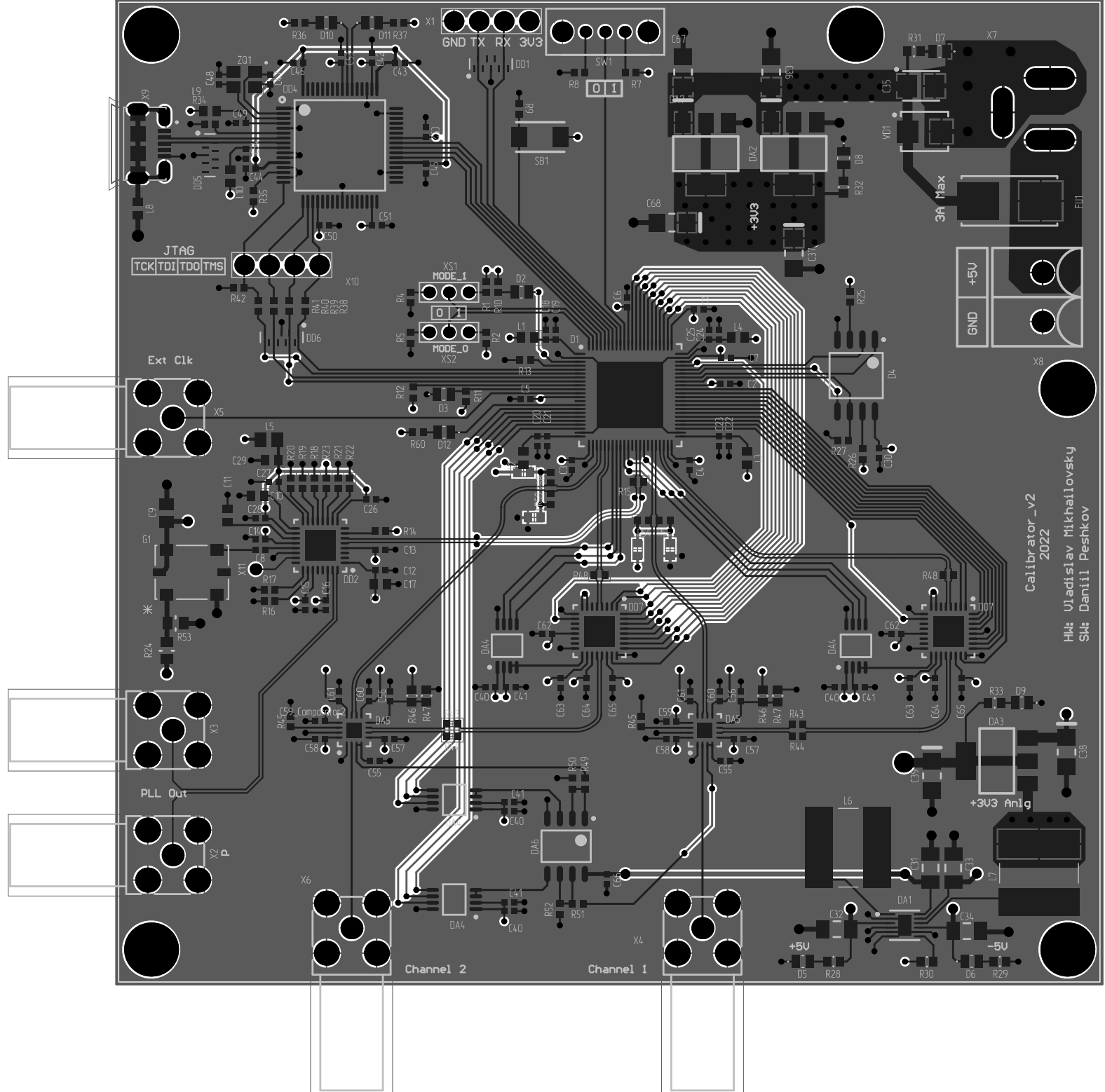
NOR FLASH for FPGA configuration via SPI



Title	Наименование		Altium
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Date:	04.06.2022	Time: 20:54:00	
File:	C:\Users\Vlad\Documents\GitHub\CalibratorPCB\Mainboard\FLASH.SchDoc	Sheet 1 of 1	







Calibrator_v2
2022
HW: Vladislav Mikhailovsky
SW: Daniil Peshkov