

CS 61C Fall 2014 Discussion 2 – MIPS

C	MIPS
<pre>// \$s0 -> a, \$s1 -> b // \$s2 -> c, \$s3 -> z int a = 4, b = 5, c = 6, z; z = a + b + c + 10;</pre>	<pre>addiu \$s0, \$zero, 4 # a = 4 addiu \$s1, \$zero, 5 # b = 5 addiu \$s2, \$zero, 6 # c = 6 addu \$s3, \$s0, \$s1 # z = a + b addu \$s3, \$s3, \$s2 # z = z + c addiu \$s3, \$s3, 10 # z = z + 10</pre>
<pre>// \$s0 -> int * p = intArr; // \$s1 -> a; *p = 0; int a = 2; p[1] = p[a] = a;</pre>	<pre>sw \$zero, 0(\$s0) # *p = 0 addiu \$s1, \$zero, 2 # int a = 2 sw \$s1, 4(\$s0) # p[1] = a sll \$t0, \$s1, 2 # t0 = a << 2 add \$t0, \$t0, \$s0 # t0 = t0 + *p sw \$s1, 0(\$t0) # p[a] /* t0 */ = a</pre>
<pre>// \$s0 -> a, \$s1 -> b int a = 5, b = 10; if(a + a == b) { a = 0; } else { b = a - 1; }</pre>	<pre>addiu \$s0, \$zero, 5 addiu \$s1, \$zero, 10 addu \$t0, \$s1, \$s1 beq \$t0, \$s1, True addui \$s1, \$s0, -1 j End True: addu \$s0, \$zero, \$zero End:</pre>
<pre>// \$s0 -> i, \$s1 -> s1 int i = 0; int s1 = 1; while (i < 30) { s1 *= 2; i++; }</pre>	<pre>addiu \$s0, \$0, 0 addiu \$s1, \$0, 1 addiu \$t0, \$0, 30 loop: beq \$s0, \$t0, exit addu \$s1, \$s1, \$s1 addiu \$s0, \$s0, 1 j loop exit:</pre>
<pre>int sum(int n) { int sum; for(sum=0;n>0;sum+=n--); return sum; }</pre>	<pre>sum: xor \$v0, \$zero, \$zero loop: blez \$a0, exit addu \$v0, \$v0, \$a0 addiu \$a0, \$a0, -1 j loop exit: jr \$ra</pre>

Implement `streq`, which sets `$v0` to true only when its two character pointer arguments (`$a0` and `$a1`) point to equal strings, first in C, then in MIPS.

C	MIPS
<pre>int streq(char * s1, char * s2) { do { if (*s1 != *s2) { return 0; } s2++; } while(*s1++); return 1; }</pre>	<pre>streq: lb \$t0, 0(\$a0) lb \$t1, 0(\$a1) bne \$t0, \$t1, false beq \$t0, \$0, true addiu \$a0, \$a0, 1 addiu \$a1, \$a1, 1 j streq false: xor \$v0, \$0, 0 jr \$ra true: addiu \$v0, \$0, 1 jr \$ra</pre>

What are the instructions to branch on each of the following conditions?

<code>\$s0 < \$s1</code>	<code>\$s0 <= \$s1</code>	<code>\$s0 > 1</code>	<code>\$s0 >= 1</code>
<pre>slt \$t0, \$s0, \$s1 bne \$t0, \$zero, label</pre>	<pre>slt \$t0, \$s1, \$s0 beq \$zero, \$t0, label</pre>	<pre>sltiu \$t0, \$s0, 2 beq \$zero, \$t0, label</pre>	<pre>sltiu \$t0, \$s0, 1 beq \$zero, \$t0, label</pre>

There are a few different meanings that the term “unsigned” takes in MIPS. Describe all three meanings, and give example instructions.

	Unsigned Meaning	Examples
1	1 Do not sign extend loaded data	<code>lbu, lhu</code>
2	2 No errors on signed overflow	<code>addu, subu, addiu</code>
3	3 Perform unsigned operations	<code>multu, divu, sltu, sltiu</code>

What is the distinction between zero extension and sign extension?

zero extension adds only zeros, while sign extension extends the sign

What is the purpose of sign extension, and when is zero extension used instead of sign extension in MIPS?

sign extension is used for signed operations because the value stays the same and the sign is preserved and zero extension is used for everything else except for jump operations