

Hardware Document Package Changes

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Changes Since Version 2.0

Modified Documents

Major modifications of each document are listed below. (Minor corrections that have been made for clarification are excluded.)

CBE Overview

Chapter 6: SPE

- Order of Data Transfers section. Added description of ordering of atomic commands (moved from Appendix A)

Appendix A: Multiprocessor Ordering Rules

- Clarified the relationship between the ordering rules in the document and the rules in "Cell Broadband Engine™ Architecture"
- MFC Ordering Rules section
 - Added completion waits of MFC commands to ordering methods that affect multiprocessor synchronization
 - Corrected information regarding consistency of synchronization mechanisms with PowerPC Architecture, and added specific examples in which ordering is guaranteed
 - Deleted text regarding ordering of atomic commands for a single SPE (moved to Chapter 6: SPE)