1. Description

The purpose of this lab was to code and implement a Proportional plus Integral (PI) Controller to control a DC motor. The block diagram for this system can be found below:

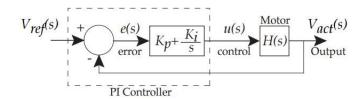


Figure 1: DC Motor/Controller Block Diagram

This feedback control system was implemented using interrupts and three threads: main, timer, and update. The main function thread initialized I/O, created and registered interrupt threads, and finally unregistered and terminated the interrupt threads. The Timer IRQ was pivotal in controlling the motor and was responsible for implementing the controller seen in Figure 1 through calls to vel(), cascade(), and $Aio_Write()$. The Update IRQ was used to update the control table on the LCD screen and allow the user to input new gain values for the control law without interrupting the motor's operation.

```
Hierarchy:
main()
| MyRio Open()
                                                        // Opens a session with the MyRio
| AIO initialize()
                                                        // Initializing Analog Input/Output
_Aio_Write()
                                                        // Further initialization
 EncoderC initialize()
                                                        // Initializes the encoder interface
 Irq RegisterTimerIrq()
                                                        // Register the timer interrupt
 pthread create()
                                                        // Create a new thread for the interrupt
        | Timer Irq Thread() (argument)
                                                        // Interrupt service function
 pthead create()
                                                        // Register the update thread
        | Table Update Thread() (argument)
                                                        // IRO that updates the control table
 ctable()
                                                        // Displays the control Table on the LCD
 pthread join()
                                                        // Terminates the timer IRQ
 pthread join()
                                                        // Terminates the update IRQ
 Irq UnregisterDiIrq()
                                                        // Unregister the timer IRQ
                                                        // Confirmation message on LCD
 printf lcd()
| MyRio Close()
                                                        // Closes the session with the MyRio
Timer Irq Thread()
                                                        // Timer interrupt service function
| while()
                                                        // Interrupt until thread stopped by main
                                                        // wait for IRQ to assert or time out
         Irq Wait()
         NiFpga WriteU32()
                                                        // Scheduling next interrupt
         NiFpga WriteBool()
                                                        // More timer interrupt parameter setting
                                                        // if the IRO is asserted
        | if()
                                                        // calculates the BDI
                | vel()
                                                        // call cascade and process input/output
                cascade()
                _Aio Write()
                                                        // transmit y0 to analog output
                | Irq Acknowledge()
                                                        // Interrupt acknowledged to the scheduler
                                                        // Terminate the new thread
 pthread exit()
```

```
// Update interrupt service function
Table Update Thread()
| while()
        | nanosleep()
                                                         // sleeps for a designated # of nanosec
                                                         // updates the control table
        update()
_pthread_exit()
                                                         // Terminate the thread
cascade()
                                                         // Uses a biquad cascade
                                                         // difference eq to calculate y0
| for()
                                                         // Function calculates the BDI
vel()
| Encoder Counter()
                                                         // gets the current count from the encoder
```

2. Testing

The code was initially tested using recommended values for V_ref, Kp, Ki, and BTI (200/-200, 0.1, 2, and 5 respectively). The system was then tested using different gain, rpm, and BTI values to ensure the response was as expected for a PI controller. The motor speed displayed on the LCD screen was also compared with the value read by a tachometer. Most of the analysis was done qualitatively as there was no data collected and returned in a MATLAB file for this lab.

3. Results

The basic code components provided for this lab functioned as expected. Update() allowed the user to successfully change the system parameters and these changes were implemented by the control system while operating. The ctable() function displayed the table values and allowed the user to shift the table as expected.

The system responded as expected with the recommended values for V ref, Kp, Ki, and BTI. When 200 rpm was used for V ref, the motor turned in the clockwise direction and counter-clockwise when -200 rpm was used. This verifies that negative values will drive the motor in the opposite direction compared to positive values of rpm. When V ref was set to 200rpm, values for V act stayed within roughly 5-10 rpm of this value. The V act value displayed was verified using a tachometer and the values between the LCD and the tachometer agreed (with some reasonable error). The system was also tested using higher gain values for Kp and Ki and an external disturbance. The expected response with higher gain would involve more overshoot compared to lower levels of gain and this was seen in the motor's response. At one point, the system was set to a V ref of 500 rpm, Ki of 10, and Kp of ~5 and a large disturbance was introduced to the system. The system response was large enough that the current amplifier faulted, despite the saturation macro used on the output voltage, and the test was terminated. Future tests should be formulated with the current limitations inherent in the system in mind. Another test was done with a higher BTI and it was observed that the fluctuations in V act were larger than those seen with a lower BTI. This is expected as the system is not comparing the measured and reference velocities as frequently and the system has more time to diverge from the reference value. This larger divergence requires a large response from the system to return it to normal, which leads to more overshoot prior to correction in the opposite direction.

Overall, this experiment was a success and the feedback control for the DC motor acted as desired, with a few supply-related hiccups.