

Bug #	ID	Fixed	Component	Description	
1	35583	NEW	new bugs	NewGVN indeterministic crash	
2	35272	FIXED	new bugs	[AVX512] Assertion "Invalid sext node, dst < src!" in llvm::SelectionDAG::getNode()	
3	34959	NEW	new bugs	Incorrect predication in SKX, opt-bisect blames SLP Vectorizer	
4	34947	FIXED	Backend: X86	-O0 crash: assertion "Cannot BITCAST between types of different sizes!"	
5	34934	NEW	Backend: X86	UNREACHABLE at LegalizeDAG.cpp: "Unexpected request for libcall!" (x86_64, SKX)	
6	34856	FIXED	new bugs	Assertion on vector code: "Invalid constantexpr cast!"	
7	34855	FIXED	Backend: X86	Cannot select shl and srl for v2i64	
8	34841	FIXED	Scalar Optimizati	InstCombine - Assertion "Cannot convert from scalar to/from vector"	
9	34838	FIXED	new bugs	InstCombine - Assertion "Compare known true or false was not folded"	
10	34837	FIXED	new bugs	UNREACHABLE in DAGTypeLegalizer::PromoteIntegerResult	
11	34830	NEW	new bugs	Clang failing with "Cannot emit physreg copy instruction"	
12	34828	FIXED	Register Allocato	Assertion in VirtRegRewriter: `toIndex_(n) < storage_.size() && "index out of bounds!"	
13	34787	FIXED	new bugs	Assertion `TN->getNumChildren() == 0 && "Not a tree leaf!"	
14	34782	FIXED	new bugs	Assertion "Deleted edge still exists in the CFG!"	
15	34381	FIXED	new bugs	Clang produces incorrect code at -O0	
16	34377	NEW	Backend: X86	Clang produces incorrect code with -O1 and higher	
17	34137	FIXED	Backend: X86	clang crash in llvm::SelectionDAG::Combine on -O0	
18	33844	FIXED	Backend: X86	Assertion `loBit <= BitWidth && "loBit out of range" failed with -O1	
19	33828	FIXED	Common Code C	Crash in X86DAGToDAGISel::RunSDNodeXForm: "getConstant with a uint64_t value that doesn't fit in the type!"	
20	33765	FIXED	new bugs	Replacing instructions in instcombine violates dominance relation	
21	33695	FIXED	new bugs	Bit-Tracking Dead Code Elimination (BDCE) fails to invalidate nsw/nuw after transforms	
22	33560	FIXED	Backend: X86	"Cannot emit physreg copy instruction" at lib/Target/X86/X86InstrInfo.cpp:6707	
23	33442	NEW	Frontend	UBSAN: right shift by negative value is not caught for some values	
24	32894	FIXED	Backend: X86	ICE: "Cannot replace uses of with self" in llvm::SelectionDAG::ReplaceAllUsesWith	
25	32830	FIXED	new bugs	Clang produces incorrect code with -O2 and higher	
26	32525	DUPLICATE	Backend: X86	Assertion is hit: "The number of nodes scheduled doesn't match the expected number!"	
27	32515	FIXED	Common Code C	Assertion "DELETED_NODE in CSEMap!" fires a lot with -march=skx	
28	32345	FIXED	Common Code C	Assertion `Op.getScalarValueSizeInBits() == BitWidth && "Mask size mismatches value type size!" failed with -O0.	
29	32340	FIXED	Common Code C	Assertion `N->getOpcode() != ISD::DELETED_NODE && "Deleted Node added to Worklist" failed with -O0	
30	32329	FIXED	new bugs	Silent failure in X86 DAG->DAG Instruction Selection with -march=skx -O3	
31	32316	FIXED	new bugs	Assertion `N1.getValueType() == N2.getValueType() && N1.getValueType() == VT && "Binary operator types must match!" failed with -O1 -march=skx	
32	32284	FIXED	new bugs	Assertion `Num < NumOperands && "Invalid child # of SDNode!" failed with -O0 -march=skx.	
33	32256	FIXED	new bugs	Assertion `!isHReg(DestReg) && "Cannot move between mask and h-reg" failed with -m32 -O0 -march=skx.	
34	32241	FIXED	new bugs	Incorrect result with -march=skx -O0 -m32.	
35	31306	FIXED	Backend: X86	[AVX-512] Compiler crash: Cannot select: t41: v8i64 = X86ISD::SUBV_BROADCAST	
36	31045	FIXED	Backend: X86	Clang fails in insertDAGNode	
37	31044	NEW	Backend: X86	Assertion `Index.getValueType() == MVT::i32 && "Expect to be extending 32-bit registers for use in LEA" failed	
38	30813	FIXED	New Bugs	Assertion `ShiftBits != 0 && MaskBits <= Size' failed.	
39	30783	FIXED	New Bugs	Assertion `New->getType() == getType() && "replaceAllUses of value with new value of different type!" failed	
40	30777	WORKSFORME	New Bugs	Assertion `DestBits == SrcBits && "Illegal cast to vector (wrong type or size)" failed	
41	30775	FIXED	Backend: X86	Assertion `NodeToMatch->getOpcode() != ISD::DELETED_NODE && "NodeToMatch was removed partway through selection" failed.	
42	30693	FIXED	Backend: X86	AVX512 Segfault: alignment incorrect for relative addressing	
43	30286	FIXED	new bugs	[AVX-512] KNL bug at -O0	
44	30256	FIXED	new bugs	Assert in llvm::ReassociatePass::OptimizeAdd	

45	29058	FIXED	new bugs	Assert in llvm::SelectionDAG::Legalize()	
46	28845	NEW	new bugs	Incorrect codegen for "store <2 x i48>" triggered by -fslp-vectorize-aggressive	
47	28661	FIXED	new bugs	[AVX512] incorrect code for boolean expression at -O0.	
48	28313	FIXED	New Bugs	LLVM trunk crash with knl target (Assertion `isSCEVable(V->getType())`)	
49	28312	FIXED	New Bugs	LLVM trunk crash with knl target (Assertion `Res.getValueType() == N->getValueType(0)`)	
50	28301	FIXED	New Bugs	Clang trunk ICE (Assertion `Removed && "Register is not used by this instruction!"`)	
51	28291	FIXED	New Bugs	LLVM trunk crash with knl target (Assertion `C1->getType() == C2->getType()`)	
52	28119	FIXED	llc	[AVX-512] llc crash with UNREACHABLE executed at lib/IR/ValueTypes.cpp:128	
53	27997	FIXED	New Bugs	ICE on trunk Clang, knl target, Assertion `L.isLCSSAForm(DT)` failed	
54	27879	FIXED	Frontend	ICE on trunk llvm (Invalid operands for select instruction)	
55	27873	FIXED	New Bugs	ICE in llvm::TargetLowering::SimplifyDemandedBits on knl	
56	27789	FIXED	llc	Clang trunk crashes on knl target	
57	27638	FIXED	Backend: X86	ICE in llvm::SDValue llvm::X86TargetLowering::LowerSETCC	
58	27591	FIXED	Backend: X86	Clang crash with KNL target, Assertion `Emitted && "Failed to emit a zext!"` failed	
59	27584	FIXED	llc	LLVM trunk crash with knl target	
60	25519	WORKSFORME	llc	[AVX-512] llc generates incorrect code	
61	25518	WORKSFORME	llc	[AVX-512] llc generates incorrect code	
62	25517	WORKSFORME	llc	[AVX-512] llc generates incorrect code	