

# Cornell Notes

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Topic: CALID CVN

Date: 18/09/2025

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Cue Column (Questions, Keywords, or Prompts)

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Notes Section (Main Notes)

## 1. Definition of Term:

- CALID = Calibration ID = Kalibrierungs-Identifikation = Calibration identification
- CVN = Calibration Verification Number = Prüfsumme = Checksum

### 1.1. CALID

- Identification code for a specific software/calibration contained in a server/electronic control unit (ECU)
- Note 1 to entry: If regulations require calibration identifications for emission-related software, those shall be reported in a standardized format as specified in SAE J1979-DA.

### 1.2. CVN

- Server/ECU calculated verification number of a calibration identification number to verify the integrity of the software/calibration contained in a server/ECU
- Note 1 to entry: If regulations require calibration identifications for emission-related software, those shall be reported in a standardised format as specified in ISO 15031-2.

### 1.3. Secondary ECU

- In the following, a specification of the OBD handling in AUTOSAR is introduced. Herein,
- "OBD" is used for automotive OBD with respect to different target markets. For SW- sharing and distributed development reasons as well as aspects of packaging and responsibility of releases, the OBD-relevant information / data structures need to be reported via Standardized AUTOSAR interfaces.
- In a vehicle there can be 3 different kinds of OBD ECUs:
  - Master ECU (one per vehicle), in WWH-OBD referenced as VOBD
  - Primary ECU (several per vehicle)
  - **Dependent / Secondary ECUs** (several per vehicle)
- From the Basic Software point of view **Dependent / Secondary ECUs** doesn't need any specific OBD functionality. In **Dependent / Secondary ECUs** are always related to a Master or a Primary ECU. In **Dependent / Secondary ECUs** OBD- relevant information will not be stored in the Basic Software (e.g. OBD events will be forwarded to the respective Master or Primary ECU via the Bussystem). In Dependent / Secondary ECUs this "reported errors" and other OBD functionality might be handled by a SW component.

### 1.4. AUTOSAR Standard

- Following [AUTOSAR - Remote Event Communication Protocol Specification](#) standard
- Information for [Secondary ECU](#)

### 1.5. Protocol Specification:

#### Message formats

- This chapter specifies all of the message formats of the RecM Bus Protocol.

- Unless otherwise specified, all messages are of the "fire and forget" type; meaning that, after a request is sent, there is no response message.
- For both the **Status** and **Management messages**, the same basic RecM Bus Protocol message format is used. It consists of the Header segment, the Payload segment and the Tail segment.



**Table 5.1: RecM Message Structure**

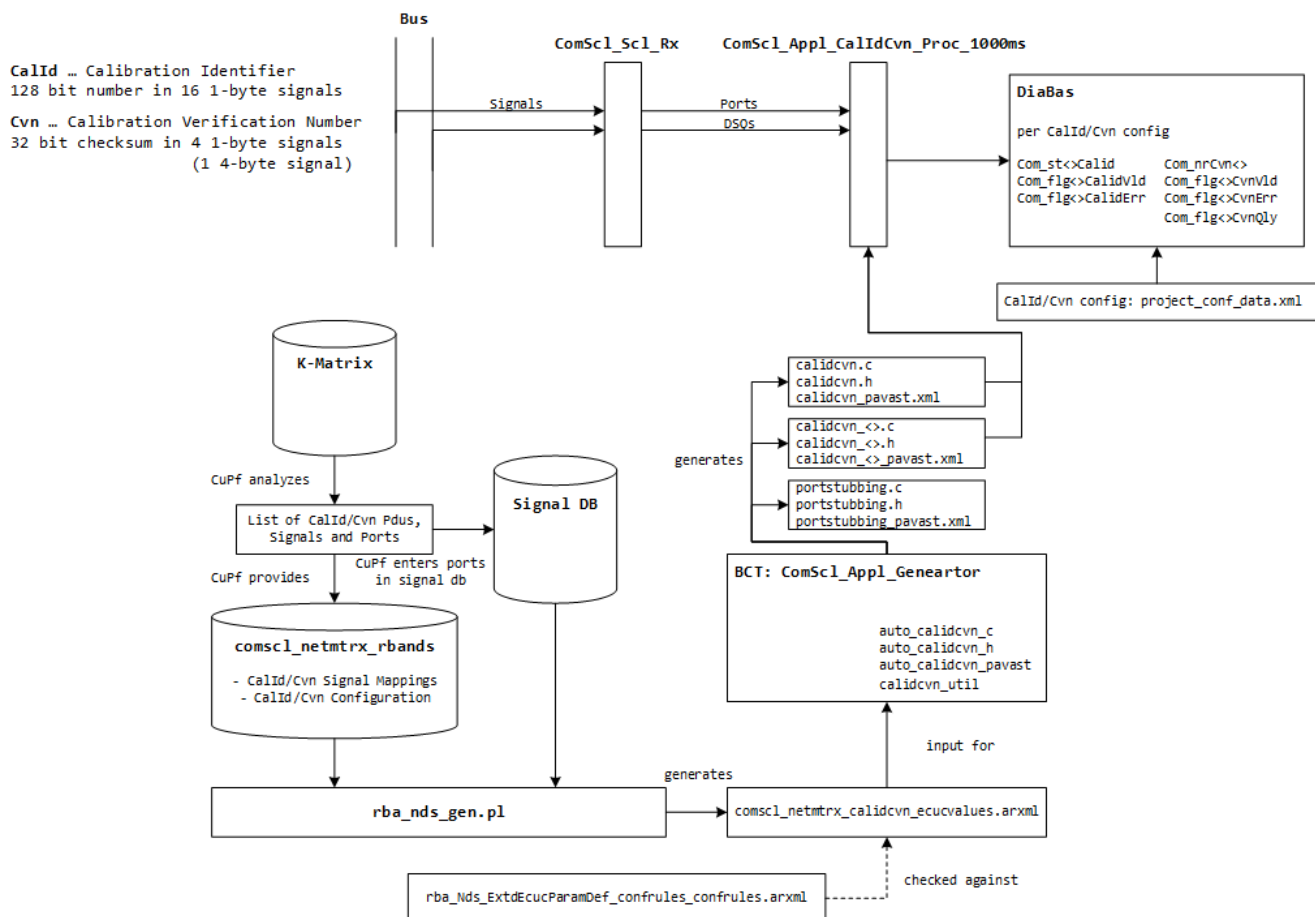
- The Header of all **Status messages** contains three fields: a Message Type, a Client Identifier and a Reserved field.
- The Header of all **Management messages** contains two fields: a Message Type and a Reserved field.
- The Tail of both the **Status** and **Management messages** contains a common Message Counter field.

#### Client Identifier (CLIENT\_ID) field format

- The Client Identifier (CLIENT\_ID) is a Secondary ECU identifier which is obtained from the Diagnostic Management module.

## 2. CALID-CVN-Toolchain and Workflow

### 2.1. CALID-CVN-Toolchain Overview



### 2.2. CALID-CVN-Workflow

Step	Task	Description	Role	Output
1	K-Matrix Analysis	During K-Matrix update, PDUs are checked for new CALID-CVN-PDUs (Naming)	Cupf	List new CALID-CVNs and their PDUs and signals

Step	Task	Description	Role	Output
2	Signal port creation	According to the Signals list, Signal ports are added in Signal-Database	Cupf	Signal ports for CALID-CVN in Signal-Database
3	Signal Mapping configuration	Signal mappings for the CALID- CVN-Signals are created in: <i>SignalMappings-Sheet</i> <i>comscl_netmtrx_rbands.xlsx</i>	Cupf	Signal-Mappings in <i>comscl_netmtrx_rbands.xlsx</i>
4	CALID-CVN- Configuration	CALID-CVN-Configuration is created/provided in: Tab <i>CalIdCvnConfiguration</i> in <i>comscl_netmtrx_rbands.xlsx</i>	Cupf	CALID-CVN-Configuration in <i>comscl_netmtrx_rbands.xlsx</i>
5	ConfigurationValues generation	EcuC-Configuration for CALID- CVN is generated by: running <i>rba_nds_gen.pl</i>	FnD	<i>comscl_netmtrx_calidcvn_ecucvalues.arxml</i> <i>comscl_netmtrx_calidcvn_confdata.xml</i>
6	check-in	EcuC-Configuration is checked- in in: FC-ARB : <i>ComSc1_NetMtrx</i>	FnD	FC-ARB : <i>ComSc1_NetMtrx</i> with check-in: <i>comscl_netmtrx_calidcvn_ecucvalues.arxml</i> <i>comscl_netmtrx_calidcvn_confdata.xml</i>
7	SWB, Testing, ...	Further steps as per ProLib	...	Released BC

### 3. CALID-CVN Configuration

#### 3.1. CALID-CVN-Configuration in *comscl\_netmtrx\_rbands.xlsx*

- CALID-CVN-Configuration is done in tab "CalIdCvnConfiguration" in *comscl\_netmtrx\_rbands.xlsx*
- Configuration Tags:

Tag	Description
<i>CalIdCvn</i>	Name of the CALID-CVN-ECU
<i>BusName</i>	Name of the bus where the CALID-CVN-ECU is located – shall equal tag <i>NetworkName</i> in tab <i>Configuration</i>
<i>CalId_Type</i>	Type of the CALID. <b>Valid types:</b> - <i>split</i> : CALID is sent via 16 8-bit signals, distributed over 1 or more PDUs. Signals are numbered in ascending order 1–16.
<i>CalId_PduCount</i>	Count of CALID-PDUs. Usual are 1 or 2.
<i>CalId_Pdus</i>	Name of the CALID-PDUs. <b>Valid syntax:</b> - <b>Single-PDU</b> : <i>&lt;PduName&gt;</i> - <b>Multiple-PDUs</b> : <i>&lt;CommonName&gt;_&lt;X&gt;</i> (... <i>&lt;X&gt;</i> placeholder for 1-digit running number) <i>&lt;CommonName&gt;_&lt;XX&gt;</i> (... <i>&lt;XX&gt;</i> placeholder for 2-digit running number)
<i>CalId_DataSignals</i>	Name of the CALID-Signals. <b>Valid syntax:</b> <i>&lt;CommonName&gt;_&lt;XX&gt;</i> (... <i>&lt;XX&gt;</i> placeholder for 2-digit running number)

Tag	Description
<b>Cvn_Type</b>	Type of the CVN. <b>Valid types:</b> - <b>split</b> : CVN is sent via 4 8-bit signals. Signals are numbered in ascending order 1–4. - <b>atomic</b> : CVN is sent via 1 32-bit Signal.
<b>Cvn_PduCount</b>	Count of CVN-PDUs. Usual 1.
<b>Cvn_Pdus</b>	Name of the CVN-PDU. <b>Valid syntax:</b> <PduName>
<b>Cvn_DataSignals</b>	Name of the CVN-Signals. <b>Valid syntax:</b> - <b>One Signal:</b> <Signal_Name> - <b>More Signals:</b> <CommonName>_<X> (... <X> placeholder for 1-digit running number) <CommonName>_<XX> (... <XX> placeholder for 2-digit running number)
<b>Cvn_VldSignal</b>	Name of the CVN-Valid (Vld)-Signal. If there is no CVN-Valid-Signal, value <b>none</b> must be entered.

- Signalmappings for CalId/Cvn ports must be provided in Signal Mappings Sheet
- Please consider: Port for CvnVld Signal must not have name "**Com\_flg<CalId>CvnVld**". An interface with the same name is expected by the DiaBas module. So the name of the port mapping of the CvnVld Signal must be changed slightly. E.g. In above example the signalport name for CvnVld Signal is "ComFlgEKATCvnVldPort". Ist recommended to use same or similar nomenclature.

### 3.2. CALID-CVN-Configuration-Example

- E.g. EKAT Calid on Motor\_SUBCAN of E3P:
  - CalId PDUs: EKAT\_CALID\_01, EKAT\_CALID\_02
  - CalId Signals:
    - EKAT\_CALID\_01: EKAT\_CALID\_Index\_01 — EKAT\_CALID\_Index\_07
    - EKAT\_CALID\_02: EKAT\_CALID\_Index\_08 — EKAT\_CALID\_Index\_16
  - Cvn PDU: EKAT\_CVN
  - Cvn Signals: EKAT\_CVN\_Byte\_1 — EKAT\_CVN\_Byte\_4, EKAT\_CVN\_VLD

A	B	C	D	E	F	G	H	I	J	K
CalIdCvn	BusName	CalId_Type	CalId_PduCount	CalId_Pdus	CalId_DataSignals	Cvn_Type	Cvn_PduCount	Cvn_Pdus	Cvn_DataSignals	Cvn_VldSignal
EKAT	Motor_SUBCAN	split		2 EKAT_CALID_<XX>	EKAT_CALID_Index_<XX>	split		1 EKAT_CVN	EKAT_CVN_Byte_<X>	EKAT_CVN_VLD

A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P
BusName	PduName	SignalName	SignalDir	Signal	SignalPortName	SignalPortCol	Signal	SignalPortSignalQuality	SignalPortSignalStatus	SignalPortVirtualId	SignalPortVirtualIdC	SignalPortVirtualIdS	SignalPortVirtualIdV	SignalPortVirtualIdV	SignalPortVirtualIdV
Motor_SUBCAN	EKAT_CALID_01	EKAT_CALID_Index_01	receive	Com_dataEKATCalldByt1	Value_Calculated	DSQ_ComDataEKATCalldByt1	Com_stSigDataEKATCalldByt1	DFC_ComDataEKATCalldByt1Virt	63						
Motor_SUBCAN	EKAT_CALID_01	EKAT_CALID_Index_02	receive	Com_dataEKATCalldByt2	Value_Calculated	DSQ_ComDataEKATCalldByt2	Com_stSigDataEKATCalldByt2	DFC_ComDataEKATCalldByt2Virt	63						
Motor_SUBCAN	EKAT_CALID_01	EKAT_CALID_Index_03	receive	Com_dataEKATCalldByt3	Value_Calculated	DSQ_ComDataEKATCalldByt3	Com_stSigDataEKATCalldByt3	DFC_ComDataEKATCalldByt3Virt	63						
Motor_SUBCAN	EKAT_CALID_01	EKAT_CALID_Index_04	receive	Com_dataEKATCalldByt4	Value_Calculated	DSQ_ComDataEKATCalldByt4	Com_stSigDataEKATCalldByt4	DFC_ComDataEKATCalldByt4Virt	63						
Motor_SUBCAN	EKAT_CALID_01	EKAT_CALID_Index_05	receive	Com_dataEKATCalldByt5	Value_Calculated	DSQ_ComDataEKATCalldByt5	Com_stSigDataEKATCalldByt5	DFC_ComDataEKATCalldByt5Virt	63						
Motor_SUBCAN	EKAT_CALID_01	EKAT_CALID_Index_06	receive	Com_dataEKATCalldByt6	Value_Calculated	DSQ_ComDataEKATCalldByt6	Com_stSigDataEKATCalldByt6	DFC_ComDataEKATCalldByt6Virt	63						
Motor_SUBCAN	EKAT_CALID_01	EKAT_CALID_Index_07	receive	Com_dataEKATCalldByt7	Value_Calculated	DSQ_ComDataEKATCalldByt7	Com_stSigDataEKATCalldByt7	DFC_ComDataEKATCalldByt7Virt	63						
Motor_SUBCAN	EKAT_CALID_01	EKAT_CALID_Index_08	receive	Com_dataEKATCalldByt8	Value_Calculated	DSQ_ComDataEKATCalldByt8	Com_stSigDataEKATCalldByt8	DFC_ComDataEKATCalldByt8Virt	63						
Motor_SUBCAN	EKAT_CALID_02	EKAT_CALID_Index_09	receive	Com_dataEKATCalldByt9	Value_Calculated	DSQ_ComDataEKATCalldByt9	Com_stSigDataEKATCalldByt9	DFC_ComDataEKATCalldByt9Virt	63						
Motor_SUBCAN	EKAT_CALID_02	EKAT_CALID_Index_10	receive	Com_dataEKATCalldByt10	Value_Calculated	DSQ_ComDataEKATCalldByt10	Com_stSigDataEKATCalldByt10	DFC_ComDataEKATCalldByt10Virt	63						
Motor_SUBCAN	EKAT_CALID_02	EKAT_CALID_Index_11	receive	Com_dataEKATCalldByt11	Value_Calculated	DSQ_ComDataEKATCalldByt11	Com_stSigDataEKATCalldByt11	DFC_ComDataEKATCalldByt11Virt	63						
Motor_SUBCAN	EKAT_CALID_02	EKAT_CALID_Index_12	receive	Com_dataEKATCalldByt12	Value_Calculated	DSQ_ComDataEKATCalldByt12	Com_stSigDataEKATCalldByt12	DFC_ComDataEKATCalldByt12Virt	63						
Motor_SUBCAN	EKAT_CALID_02	EKAT_CALID_Index_13	receive	Com_dataEKATCalldByt13	Value_Calculated	DSQ_ComDataEKATCalldByt13	Com_stSigDataEKATCalldByt13	DFC_ComDataEKATCalldByt13Virt	63						
Motor_SUBCAN	EKAT_CALID_02	EKAT_CALID_Index_14	receive	Com_dataEKATCalldByt14	Value_Calculated	DSQ_ComDataEKATCalldByt14	Com_stSigDataEKATCalldByt14	DFC_ComDataEKATCalldByt14Virt	63						
Motor_SUBCAN	EKAT_CALID_02	EKAT_CALID_Index_15	receive	Com_dataEKATCalldByt15	Value_Calculated	DSQ_ComDataEKATCalldByt15	Com_stSigDataEKATCalldByt15	DFC_ComDataEKATCalldByt15Virt	63						
Motor_SUBCAN	EKAT_CALID_02	EKAT_CALID_Index_16	receive	Com_dataEKATCalldByt16	Value_Calculated	DSQ_ComDataEKATCalldByt16	Com_stSigDataEKATCalldByt16	DFC_ComDataEKATCalldByt16Virt	63						
Motor_SUBCAN	EKAT_CVN	EKAT_CVN_Byte_1	receive	Com_dataEKATCvnByt1	Value_Calculated	DSQ_ComDataEKATCvnByt1	Com_stSigDataEKATCvnByt1	DFC_ComDataEKATCvnByt1Virt	0						
Motor_SUBCAN	EKAT_CVN	EKAT_CVN_Byte_2	receive	Com_dataEKATCvnByt2	Value_Calculated	DSQ_ComDataEKATCvnByt2	Com_stSigDataEKATCvnByt2	DFC_ComDataEKATCvnByt2Virt	0						
Motor_SUBCAN	EKAT_CVN	EKAT_CVN_Byte_3	receive	Com_dataEKATCvnByt3	Value_Calculated	DSQ_ComDataEKATCvnByt3	Com_stSigDataEKATCvnByt3	DFC_ComDataEKATCvnByt3Virt	0						
Motor_SUBCAN	EKAT_CVN	EKAT_CVN_Byte_4	receive	Com_dataEKATCvnByt4	Value_Calculated	DSQ_ComDataEKATCvnByt4	Com_stSigDataEKATCvnByt4	DFC_ComDataEKATCvnByt4Virt	0						
Motor_SUBCAN	EKAT_CVN	EKAT_CVN_VLD	receive	Com_flgEKATCvnVldPort	Value_Calculated	DSQ_ComFlgEKATCvnVldPort	Com_stSigFlgEKATCvnVldPort	DFC_ComFlgEKATCvnVldPortVirt	0						

### 4. CALID-CVN-Configuration for DiaBas

#### Implementation of CVN / CALID

- If a CALID and/or CVN is provided by a sensor within one or more PDUs then:
  - For each PDU only one DSQ shall be configured in PL / FRM --> DSQ\_ComdataXXX (XXX = PDU name without "\_"; e.g for SCR\_DEF2\_CAL\_ID2 = DSQ\_ComdataSCRDEF2CALID2)

- Only for FR: for each CALID and / or CVN NO signal status is needed, because it will not be used!
- The following configuration parameters need to be provided to the ASW as per the DiaBas programmer's guide:

Com-Interface Name	Com-Interface Meaning	ASW Configuration Tag	ASW Configuration Parameter Explanation
Com_stYYYCalid[X]	<b>CALID array</b> <i>The ASW expects the complete CALID to be provided within one array (even if more than one PDU is used for receiving or multiplexed within one PDU).</i>	&lt;CALID_REF_VALUE&gt;	16 byte array message containing the Cal ID from external ECU / module. <i>Hint:</i> The message array has to be provided in the external tester format. E.g. Message_array[0] = DATA A of Cal ID to tester, Message_array[1] = DATA B ... Message_array[15] = DATA P of Cal ID.
Com_flgYYYCalidVld	<b>CALID valid bit</b> <i>Provides information on the validity of the CALID, i.e. whether the complete CALID was received on the bus.</i>	&lt;CALID_REF_STATUS_VALUE&gt;	Message containing the validity of Cal ID from external ECU / module.
Com_flgYYYCalidErr	<b>CALID error bit</b> <i>Provides information on the error status of the CALID, i.e. whether there has been a bus transmission failure.</i>	&lt;CALID_REF_ERROR_VALUE&gt;	Message containing the error status of the Cal ID from external ECU / module.
Com_nrCvnYYY[X]	<b>CVN array</b> <i>The ASW expects the complete CVN to be provided within one array</i> (Currently all implemented CVNs have 4 byte → programmers guide from DiaBas).	&lt;CVN_REF_VALUE&gt;	4 byte array message containing the CVN from external ECU / module. <i>Hint:</i> The message array has to be provided in the external tester format. E.g. Message_array[0] = DATA A of CVN ... Message_array[3] = DATA D of CVN.

Com-Interface Name	Com-Interface Meaning	ASW Configuration Tag	ASW Configuration Parameter Explanation
<code>Com_flgYYYCvnVld</code>	<b>CVN valid bit</b> <i>Provides information on the validity of the CVN, i.e. whether the complete CVN was received on the bus (for some CVN, a separate valid bus signal is available e.g. <code>BOOST_CVN_VLD</code>).</i>	<code>&amp;lt;CVN_REF_STATUS_VALUE&amp;gt;</code>	Message containing the validity of CVN from external ECU / module.
<code>Com_flgYYYCvnQly</code>	<b>CVN quality bit</b> <i>Provides information on the quality of the received CVN, i.e. whether the content of the received CVN is proper.</i>	<code>&amp;lt;CVN_REF_STATUS2_VALUE&amp;gt;</code>	Message containing the quality of CVN from external ECU / module.
<code>Com_flgYYYCvnErr</code>	<b>CVN error bit</b> <i>Provides information on the error status of the CVN, i.e. whether there has been a bus transmission failure.</i>	<code>&amp;lt;CVN_REF_ERROR_VALUE&amp;gt;</code>	Message containing the error status of the CVN from external ECU / module.

- **YYY** = PDU name without "\_" and without the corresponding PDU number (e.g. for SCR DEF2 two PDUs are used SCR\_DEF2\_CAL\_ID1 and SCR\_DEF2\_CAL\_ID2 --> `Com_stSCRDEF2CALIDCalid[x]`)
- A reference implementation can be seen in `SnsrECU_Boost 1.2.0;0`
- For reference see: `DOCMISC : diabas_programmersguide_pdf / 30000.9.0; 0`

## 5. CALID-CVN-Testing

### 5.1. Definition of Term

#### CALID:

- The CALID consists of 16 bytes, for easier handling they are called DATA A — P.
- For correct display of the CALID on the tester, the tester expects the CALID as an uint8 array: `Com_st<ecu-name>Calid[]`.
- The CALID needs to be prepared in the array in that format:

DATA A	<code>Com_st&lt;ecu-name&gt;Calid[0]</code>
DATA B	<code>Com_st&lt;ecu-name&gt;Calid[1]</code>
DATA C	<code>Com_st&lt;ecu-name&gt;Calid[2]</code>
DATA D	<code>Com_st&lt;ecu-name&gt;Calid[3]</code>
DATA E	<code>Com_st&lt;ecu-name&gt;Calid[4]</code>
DATA F	<code>Com_st&lt;ecu-name&gt;Calid[5]</code>
DATA G	<code>Com_st&lt;ecu-name&gt;Calid[6]</code>
DATA H	<code>Com_st&lt;ecu-name&gt;Calid[7]</code>
DATA I	<code>Com_st&lt;ecu-name&gt;Calid[8]</code>
DATA J	<code>Com_st&lt;ecu-name&gt;Calid[9]</code>

DATA K	Com_st<ecu-name>Calid[10]
DATA L	Com_st<ecu-name>Calid[11]
DATA M	Com_st<ecu-name>Calid[12]
DATA N	Com_st<ecu-name>Calid[13]
DATA O	Com_st<ecu-name>Calid[14]
DATA P	Com_st<ecu-name>Calid[15]

**CVN:**

- The CVN consists of 4 bytes, for easier handling they are called: DATA A – D.
- For correct display of the CVN on the tester, the tester expects the CAN provided as uint8 array: Com\_nrCvn<ecu-name>[].
- The CVN needs to be prepared in the array in that format:

DATA A	Com_nrCvn<ecu-name>[0]
DATA B	Com_nrCvn<ecu-name>[1]
DATA C	Com_nrCvn<ecu-name>[2]
DATA D	Com_nrCvn<ecu-name>[3]

**Preconditions:**

- The CALID- and CVN-PDUs need to be enabled: Bit 0
- The CALID and CVN to be displayed on the tester needs to be calibrated in I15031\_srv9\_CALIDCVN\_Seq\_CA and I15031\_CalidCvn\_SrvTstr\_Seq\_CA
- The CVN in the tester output has to be read from left to right, beginning with DATA A - B - C - D (e.g. 0A 0B 0C 0D in hex)
- The CALID has to be read equal in the tester, to the CVN, starting with DATA A --> P
- To provide the tester the CVN in a proper format we also have to know how DATA A-D is provided on the bus.

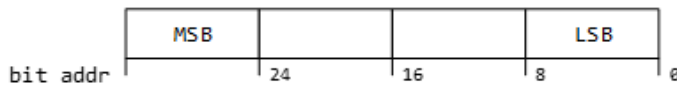
According to our understanding the CVN is written on the bus signals:

- CVN provided in one PDU with 4x 1byte bus signals (e.g. CCU- and Boost-CVN)
  - CCU\_CVN\_Byte\_1 [Byte 5 Bit [8 <— 0]] --> DATA A
  - CCU\_CVN\_Byte\_2 [Byte 6 Bit [8 <— 0]] --> DATA B
  - CCU\_CVN\_Byte\_3 [Byte 7 Bit [8 <— 0]] --> DATA C
  - CCU\_CVN\_Byte\_4 [Byte 8 Bit [8 <— 0]] --> DATA D
- CVN provided in one PDU with 1x 32bit (4Byte) bus signal (e.g. GW-CVN)
  - GW\_CVN [Byte 5 Bit [8 <— 0]] --> DATA D
  - GW\_CVN [Byte 6 Bit [8 <— 0]] --> DATA C
  - GW\_CVN [Byte 7 Bit [8 <— 0]] --> DATA B
  - GW\_CVN [Byte 8 Bit [8 <— 0]] --> DATA A
- CVN / CALID have always to be tested with Diagra / Tester!! Only with INCA, the testing is not enough
- CVN / CALID have always to be tested for IFX and JDP to verify the proper output and endianness
- Before testing take care that the CVN / CALID is properly configured in your test PVER
  - check CVN / CALID config in I15031\_srv9\_confdata.xml
  - proper application of I15031\_srv9\_CALIDCVN\_Seq\_CA and I15031\_CalidCvn\_SrvTstr\_Seq\_CA
  - take care that necessary PDUs are activated / available in ComVeh

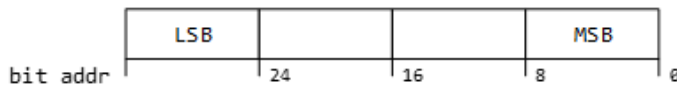
**6. Little Endian - Big Endian**

- **Little Endian (LE) / Big Endian (BE) explanation. (examples given for a 32 bit system).**

- Endianness describes in which Byte order Words are stored in the memory.
- In a 32 bit system, a Word is 32 bits long and consists of 4 Bytes.
- Little Endian (LE) means, the Least Significant Byte (LSB) of a Word is stored at the lowest memory address of that Word.

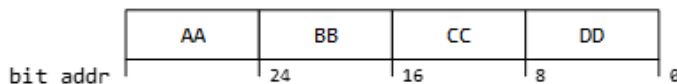


- Big Endian (BE) means, the Most Significant Byte (MSB) of a Word is stored at the lowest memory address of that Word.

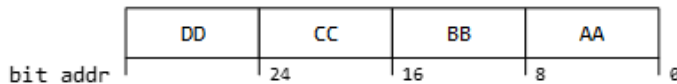


- The bit order inside a Byte, is not effected by endianness and stays the same.
- E.g. 32 bit Word AA BB CC DDh
- When red in that representation, the leftmost number is the most significant --> MSB = AAh
- The rightmost number is the least significant --> LSB = DDh

- Mem storage for LE:

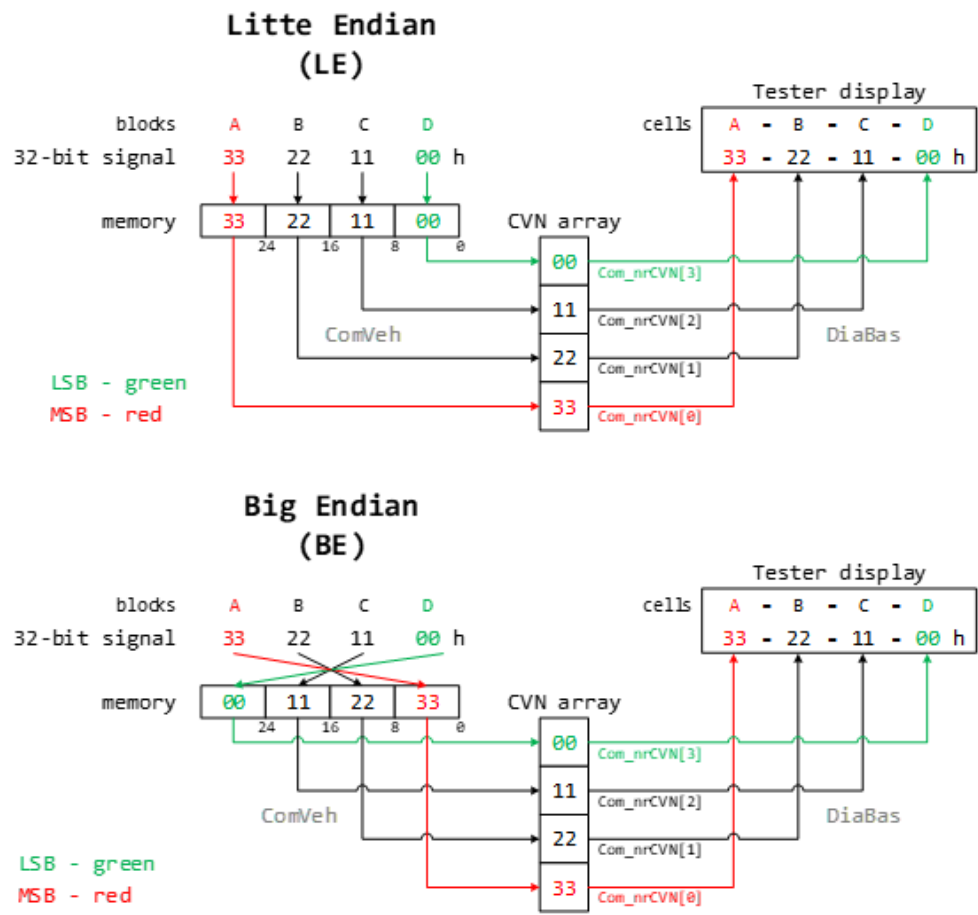


- Mem storage for BE:



- A CVN number is a 32 bit number divided in the 4 8-bit blocks A,B,C,D.
- Those blocks are displayed in the Tester Software in the format A - B - C - D.
- In that nomenclature, Block A is the MSB of the CVN and Block D is the LSB.
- The CVN is provided to the DiaBas service via a Byte array with 4 Element: Com\_nrCVN[0] - Com\_nrCVN[3]
- Array element 0 gets mapped to the display cell A and element 3 gets mapped to the display cell D.
- When the CVN is sent via 4 separate 8-bit signals on the bus, endianness in the ECU is irrelevant and each byte can be mapped separately to the array elements according to their naming.
- When the CVN is sent via 1 32-bit signal, the 32-bit memory representation of the signal needs to be divided into 4 8-bit blocks and mapped to the array elements according to the endianness of the system.
- Based on the Tester Display in the format A - B - C - D, Com\_nrCVN[0] must hold the MSB of the 32-bit bus signal and Com\_nrCVN[3] must hold the LSB of the 32-bit bus signal.
- E.g. CVN = 33 22 11 00h and is transmitted via a 32-bit bus signal holding value 33 22 11 00h. CVN needs to be displayed in the Tester in the Form 33 - 22 - 11 - 00. The mapping can be shown easily in a diagramm:





- MSB path is marked red, LSB path is marked green.
  - IFX CPUs are LE.
  - JDP CPUs are BE.

Summary Section (Summary of Notes)

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