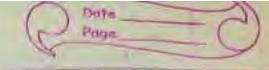
Questions: TCH.2 1) Why transistour called Bripolar? 2) Write shout note on unbiosed transistor 3) Explain Biased N-P-N toransiston. 4) Explain Biased P-N-P transistor 5) Explain input and output charecteristic of common base configuration, and e derive current orelation for common Imp.6) Derive current relation for common emitter configuration. >B 7) Draw and explain input output charecteristic for Common emilter OB Explain Base curve and collector curve for BJT. 8) Define of and B, and derive relation bet and B for transistor 9) Write shorte note on common collector configuration. 10) Define terms O'Load line 3 Bias point or operating point (8 point) 11) What is load line and explain it in detail and also explain what is a point (bias point / operating point) 12) Explain factors affecting stability of & point . => VBE, ICBO, B. => T. 13) Define stability factors, s, s', s". 14) Explain fixedbrebias circuit. in detail. 15) Explain emitter feedback biase circuit also explain stabilization of a point init. 16) Explain collector feedback bias circuit with stabilization of a point explain 17) decribe the Voltage dévider bias circuit.



18:)	Explain thermal run away and trasistor
	power disipation.
19	Transistor as a switch.
20	Define all trasistor switching time
	with diagram.
21.	Testing of die tousistoon using multimeter.
22	Explain all logic gates (distrig) with
	symbol and thout truth table.
23	Impliment AND and OR date using
	diode:
24)	Porove that NAND is universal gate.
25	Perove that NOR is 11.
26.)	Define fan in , fan out, Mise
	margin.
27)	Impliment NOR gate Using RTL.
	Impliment NAND gate using DTL.
2,000	Impliment NAND gate using RTL and Impliment NAND gate using RTL and
-47	NOR using OTL.
200)	White touth table for three input of
30)	AND NOR NAND , X-OR, X-NOR.
21)	AND, NOR, NAND, X-OR, X-NOR. What is propetation Dely?
019	What is properties in
- 7	
	- 5° = ΔIC 5" = ΔIC 5" = ΔIC
	AICRO I AVBE I APAC
	I I CBO and VBE

LICBO and Beconstant