

**GUJARAT TECHNOLOGICAL UNIVERSITY**  
**BE –SEMESTER 1&2(NEW SYLLABUS)EXAMINATION- WINTER 2018**

**Subject Code: 3110016****Date: 08-01-2019****Subject Name: basic electronics****Time: 10:30 am to 01:00 pm****Total Marks: 70****Instructions:**

1. Attempt all questions.
2. Make suitable assumptions wherever necessary.
3. Figures to the right indicate full marks.

		Marks
<b>Q.1</b>	(a) Explain V-I characteristic of tunnel diode.	<b>03</b>
	(b) What is zener breakdown? What is avalanche breakdown? Compare both the type of breakdown.	<b>04</b>
	(c) Write a short note: V-I characteristic of P-N junction diode.	<b>07</b>
<b>Q.2</b>	(a) Design and explain basic NAND gate using DTL logic.	<b>03</b>
	(b) Explain following gate using their truth table, logic symbol and equation. Ex-NOR, NAND, NO	<b>04</b>
	(c) Draw and Explain bridge rectifier. Explain advantage and disadvantage of bridge rectifier over full wave rectifier.	<b>07</b>
<b>OR</b>		
<b>Q.3</b>	(c) Write a short note: Biased clipper circuit.	<b>07</b>
	(a) Derive the relation between current gain $\alpha$ and $\beta$	<b>03</b>
	(b) What is DC load line? Explain with necessary diagram.	<b>04</b>
	(c) Draw and explain input and output characteristic of transistor in common emitter configuration.	<b>07</b>
<b>OR</b>		
<b>Q.3</b>	(a) What is stability factor? Explain.	<b>03</b>
	(b) Give comparison between CE, CB and CC configuration of transistor.	<b>04</b>
	(c) What are the different method for biasing the transistor. Explain any two method with necessary circuit diagram.	<b>07</b>
<b>Q.4</b>	(a) Why biasing circuits are required?	<b>03</b>
	(b) Explain why NAND and NOR gate are called universal gate?	<b>04</b>
	(c) Explain application of transistor as a switch.	<b>07</b>
<b>OR</b>		
<b>Q.4</b>	(a) List out the salient feature of emitter follower.	<b>03</b>
	(b) Explain various properties of CB amplifier.	<b>04</b>
	(c) Draw and explain the transistor a.c. equivalent circuit.	<b>07</b>
<b>Q.5</b>	(a) Give comparison of BJT and JFET.	<b>03</b>
	(b) Draw and explain the self bias circuit of FET.	<b>04</b>
	(c) Draw and explain various characteristic of JFET	<b>07</b>
<b>OR</b>		
<b>Q.5</b>	(a) What are the advantage of N-Channel MOSFET over P-Channel MOSFET.	<b>03</b>
	(b) Explain the application of FET as a buffer amplifier.	<b>04</b>
	(c) Write a short note: E-Type MOSFET	<b>07</b>