DESCRIPTION

The 555 monolithic timing circuit is a highly stable controller capable of producing accurate time delays, or oscillation. In the time delay mode of operation, the time is precisely controlled by one external resistor and capacitor. For a stable operation as an oscillator, the free running frequency and the duty cycle are both accurately controlled with two external resistors and one capacitor. The circuit may be triggered and reset on falling waveforms, and the output structure can source or sink up to 200 mA.

FEATURES

- Turn-off time less than 2 μs
- Max. operating frequency greater than 500 kHz
- Timing from microseconds to hours
- Operates in both astable and monostable modes
- High output current
- Adjustable duty cycle
- TTL compatible
- Temperature stability of 0.005% per °C

APPLICATIONS

- Precision timing
- Pulse generation
- Sequential timing
- Time delay generation
- Pulse width modulation

PIN CONFIGURATION

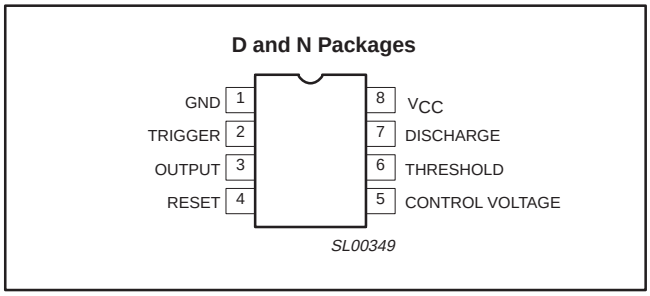

Figure 1. Pin configuration

BLOCK DIAGRAM

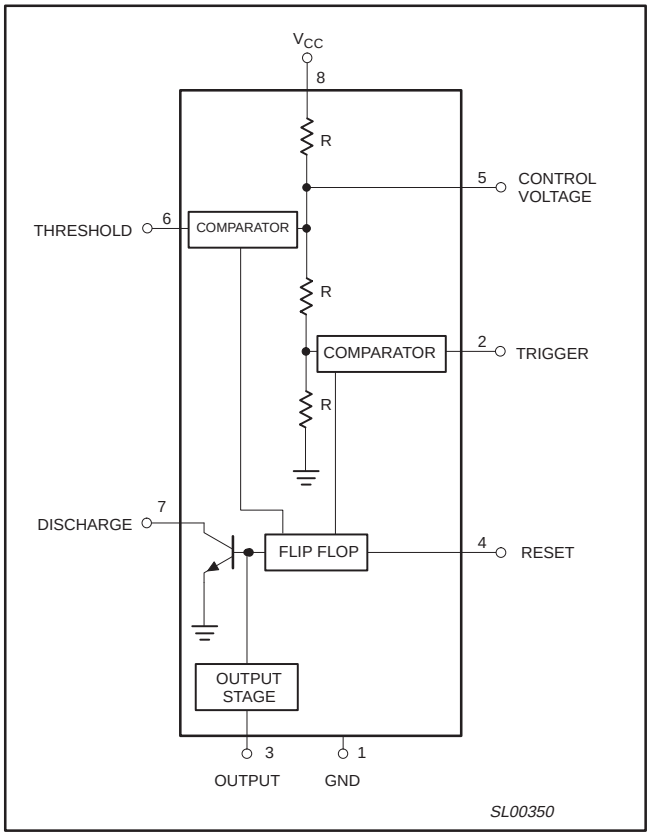

Figure 2. Block Diagram

ORDERING INFORMATION

DESCRIPTION	TEMPERATURE RANGE	ORDER CODE	DWG #
8-Pin Plastic Small Outline (SO) Package	0 to +70 °C	NE555D	SOT96-1
8-Pin Plastic Dual In-Line Package (DIP)	0 to +70 °C	NE555N	SOT97-1
8-Pin Plastic Small Outline (SO) Package	–40 °C to +85 °C	SA555D	SOT96-1
8-Pin Plastic Dual In-Line Package (DIP)	–40 °C to +85 °C	SA555N	SOT97-1
8-Pin Plastic Dual In-Line Package (DIP)	–55 °C to +125 °C	SE555CN	SOT97-1
8-Pin Plastic Dual In-Line Package (DIP)	–55 °C to +125 °C	SE555N	SOT97-1

EQUIVALENT SCHEMATIC

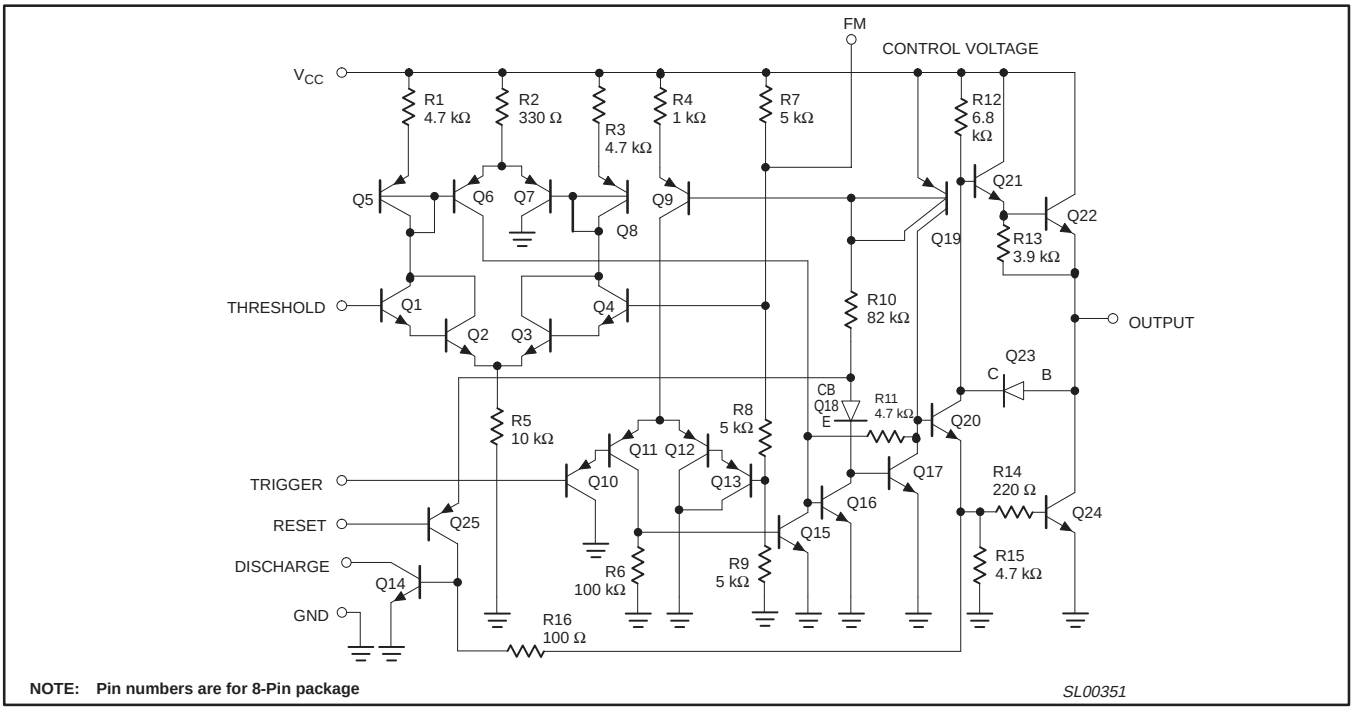

Figure 3. Equivalent schematic

ABSOLUTE MAXIMUM RATINGS

SYMBOL	PARAMETER	RATING	UNIT
V_{CC}	Supply voltage SE555 NE555, SE555C, SA555	 +18 +16	 V V
P_D	Maximum allowable power dissipation[1]	600	mW
T_{amb}	Operating ambient temperature range NE555 SA555 SE555, SE555C	 0 to +70 –40 to +85 –55 to +125	 °C °C °C
T_{stg}	Storage temperature range	–65 to +150	°C
T_{SOLD}	Lead soldering temperature (10 sec max)	+230	°C

NOTE:

1. The junction temperature must be kept below 125 °C for the D package and below 150°C for the N package.
   At ambient temperatures above 25 °C, where this limit would be derated by the following factors:
   D package 160 °C/W
   N package 100 °C/W

DC AND AC ELECTRICAL CHARACTERISTICS

T_{amb} = 25 °C, V_{CC} = +5 V to +15 V unless otherwise specified.

SYMBOL	PARAMETER	TEST CONDITIONS	SE555			NE555/SA555/SE555C			UNIT
			Min	Typ	Max	Min	Typ	Max	
V_{CC}	Supply voltage		4.5		18	4.5		16	V
I_{CC}	Supply current (low state)[1]	V_{CC} = 5 V, R_L = ∞ V_{CC} = 15 V, R_L = ∞		3 10	5 12		3 10	6 15	mA mA
t_M $\Delta t_M/\Delta T$ $\Delta t_M/\Delta V_S$	Timing error (monostable) Initial accuracy[2] Drift with temperature Drift with supply voltage	R_A = 2 kΩ to 100 kΩ C=0.1 μF		 0.5 30 0.05	 2.0 100 0.2		 1.0 50 0.1	 3.0 150 0.5	 % ppm/°C %/V
t_A $\Delta t_A/\Delta T$ $\Delta t_A/\Delta V_S$	Timing error (astable) Initial accuracy[2] Drift with temperature Drift with supply voltage	R_A, R_B = 1 kΩ to 100 kΩ C = 0.1 μF V_{CC} = 15 V		 4 0.15	 6 500 0.6		 5 0.3	 13 500 1	 % ppm/°C %/V
V_C	Control voltage level	V_{CC} = 15 V V_{CC} = 5 V	9.6 2.9	10.0 3.33	10.4 3.8	9.0 2.6	10.0 3.33	11.0 4.0	V V
V_{TH}	Threshold voltage	V_{CC} = 15 V V_{CC} = 5 V	9.4 2.7	10.0 3.33	10.6 4.0	8.8 2.4	10.0 3.33	11.2 4.2	V V
I_{TH}	Threshold current[3]			0.1	0.25		0.1	0.25	μA
V_{TRIG}	Trigger voltage	V_{CC} = 15 V V_{CC} = 5 V	4.8 1.45	5.0 1.67	5.2 1.9	4.5 1.1	5.0 1.67	5.6 2.2	V V
I_{TRIG}	Trigger current	V_{TRIG} = 0 V		0.5	0.9		0.5	2.0	μA
V_{RESET}	Reset voltage[4]	V_{CC} = 15 V, V_{TH} = 10.5 V	0.3		1.0	0.3		1.0	V
I_{RESET}	Reset current Reset current	V_{RESET} = 0.4 V V_{RESET} = 0 V		0.1 0.4	0.4 1.0		0.1 0.4	0.4 1.5	mA mA
V_{OL}	LOW-level output voltage	V_{CC} = 15 V I_{SINK} = 10 mA I_{SINK} = 50 mA I_{SINK} = 100 mA I_{SINK} = 200 mA		 0.1 0.4 2.0 2.5	 0.15 0.5 2.2 		 0.1 0.4 2.0 2.5	 0.25 0.75 2.5 	 V V V V
		V_{CC} = 5 V I_{SINK} = 8 mA I_{SINK} = 5 mA		 0.1 0.05	 0.25 0.2		 0.3 0.25	 0.4 0.35	 V V
V_{OH}	HIGH-level output voltage	V_{CC} = 15 V I_{SOURCE} = 200 mA I_{SOURCE} = 100 mA	 13.0	 12.5 13.3	 	 12.75	 12.5 13.3	 	 V V
		V_{CC} = 5 V I_{SOURCE} = 100 mA	 3.0	 3.3	 	 2.75	 3.3	 	 V
t_{OFF}	Turn-off time[5]	V_{RESET} = V_{CC}		0.5	2.0		0.5	2.0	μs
t_R	Rise time of output			100	200		100	300	ns
t_F	Fall time of output			100	200		100	300	ns
	Discharge leakage current			20	100		20	100	nA

NOTES:

1. Supply current when output high typically 1 mA less.
2. Tested at V_{CC} = 5 V and V_{CC} = 15 V.
3. This will determine the max value of R_A+R_B, for 15 V operation, the max total R = 10 MΩ, and for 5 V operation, the max. total R = 3.4 MΩ.
4. Specified with trigger input HIGH.
5. Time measured from a positive-going input pulse from 0 to 0.8×V_{CC} into the threshold to the drop from HIGH to LOW of the output. Trigger is tied to threshold.

TYPICAL PERFORMANCE CHARACTERISTICS

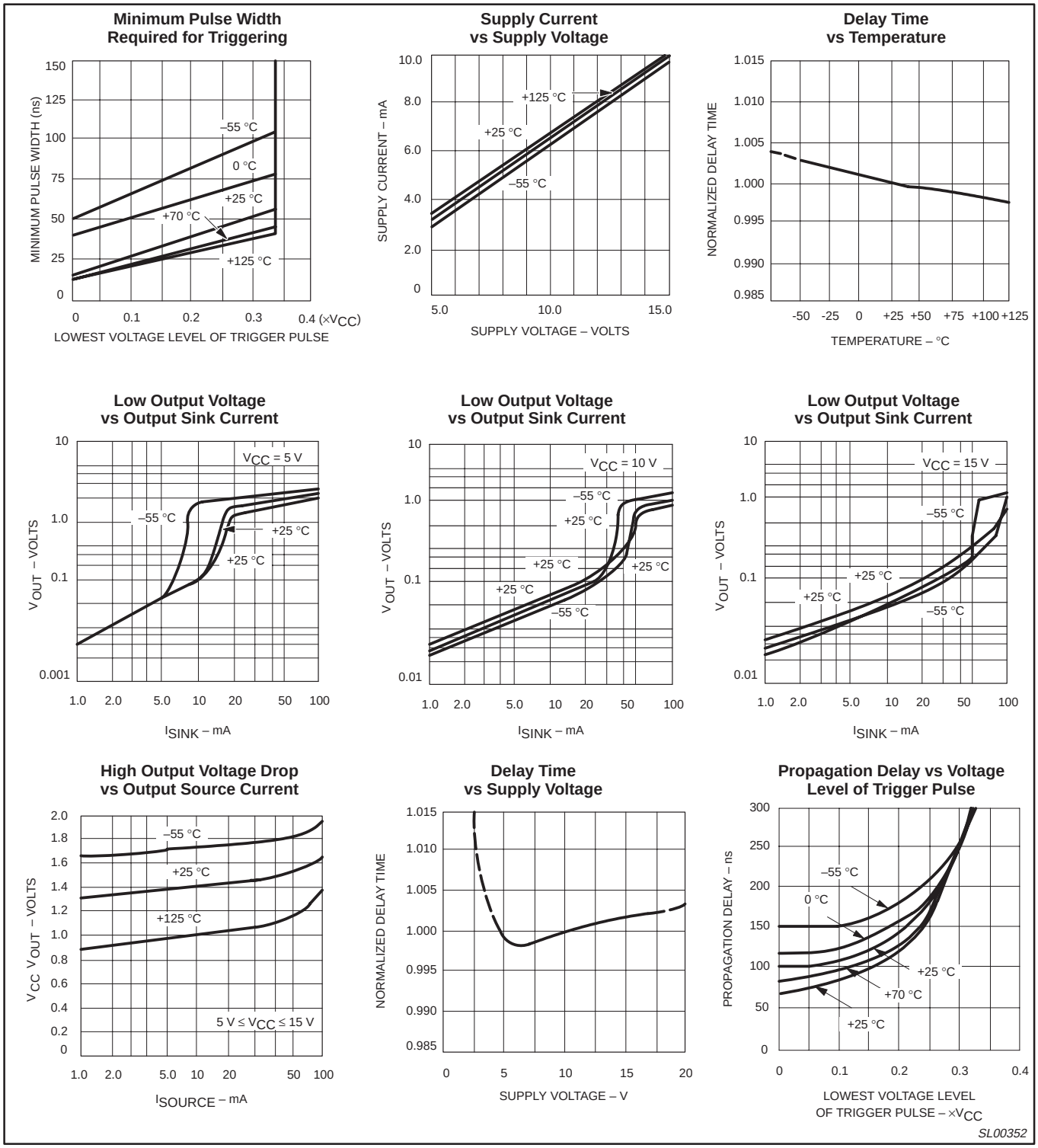

Figure 4. Typical Performance Characteristics