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Objectives

How to make different comparators using different logic gates ICs.

Required equipments.

Battery
Logic gates
Bread Board
Digital equipment board
Wires

Used Softwares

Multisim / Logic.ly

Experiments

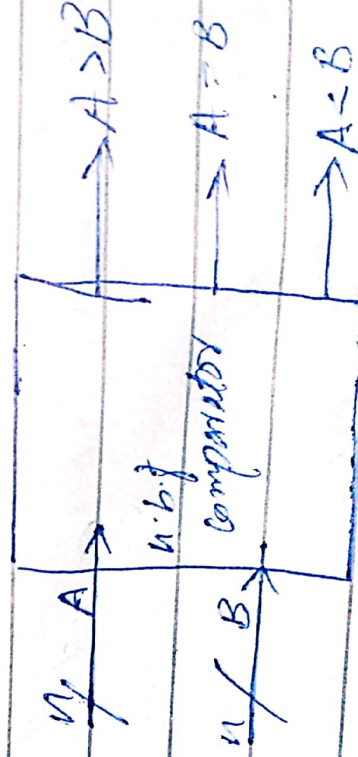
- Comparators
- 6254-bit comparators
- Boolean expression for comparators
- Use & applications of comparators

What is comparator?

A magnitude comparator is a combinational circuit that compares two digits or binary numbers and determines their relative magnitude in order to find out whether one number is equal, less than or greater than other digital numbers.

Comparator symbols are

$$A=B, A \rightarrow A, B > A$$



1-Bit magnitude comparator

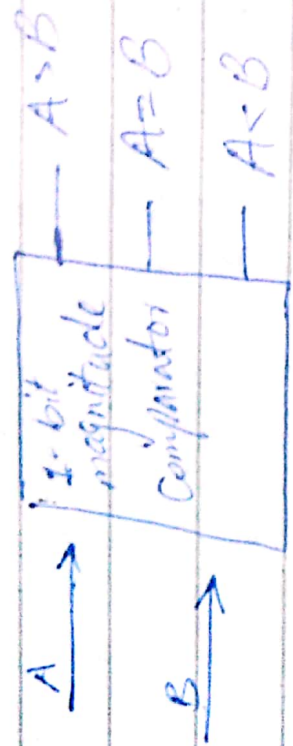
A comparator used to

compare two 1-bit binary numbers

It has two binary inputs

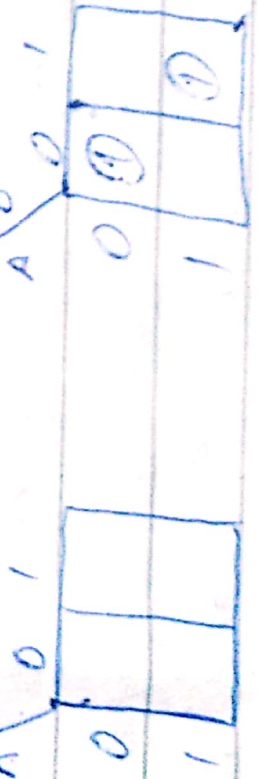
A_1B and three input, greater

than equal and less than



A	B	A = B	A < B	A > B
0	0	1	0	0
0	1	0	1	0
1	0	0	0	1
1	1	1	0	0

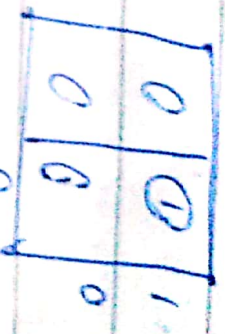
K-Map for $A < B$, $A = B$, $A > B$



$Y_1 = A < B; Y_1 = \bar{A}B$

$Y_2 = A = B$

$Y_3 = \bar{A}\bar{B} + AB$



$Y_3 = A \oplus B; Y_3 = AB$

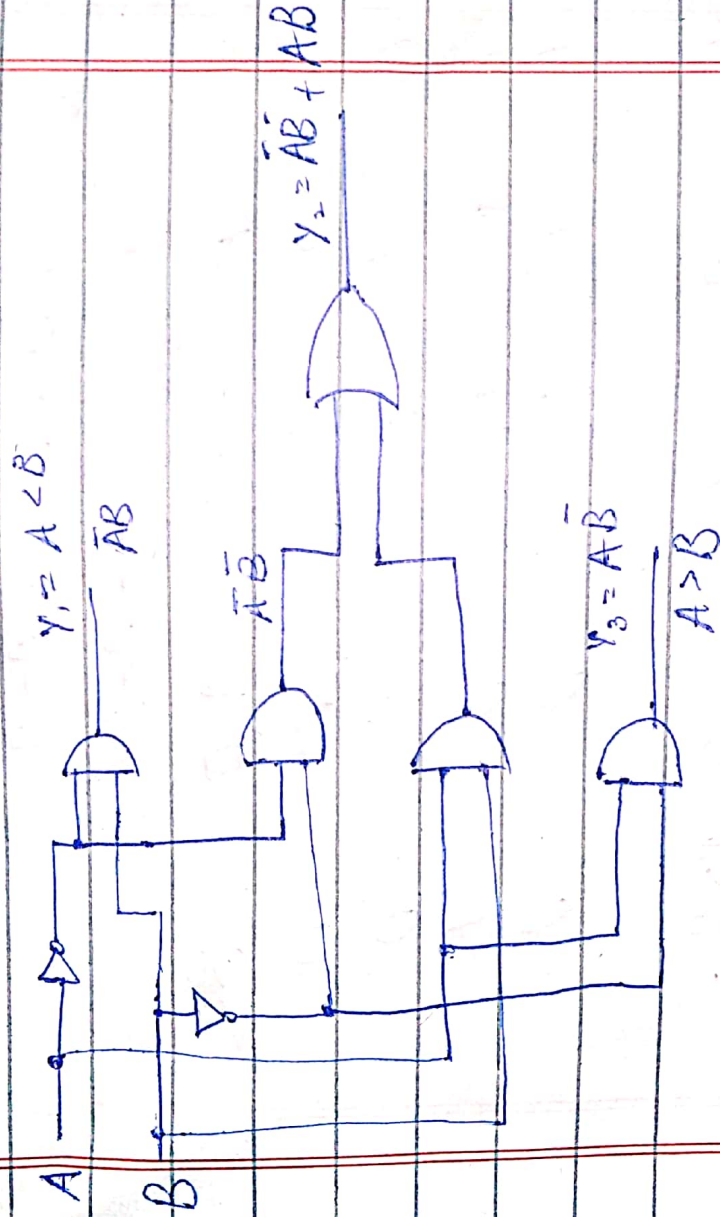
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$$Y_1 (A < B) = A'B$$

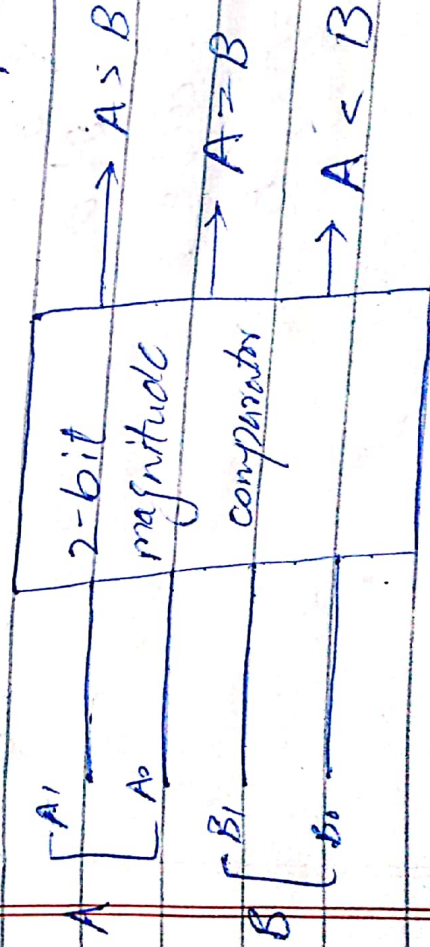
$$Y_2 (A = B) = A'B + AB = (A \oplus B)'$$

$$Y_3 (A > B) = AB'$$



2-Bit magnitude comparator

A comparator used to compare two bit number. It has 4-binary inputs and 3 binary outputs



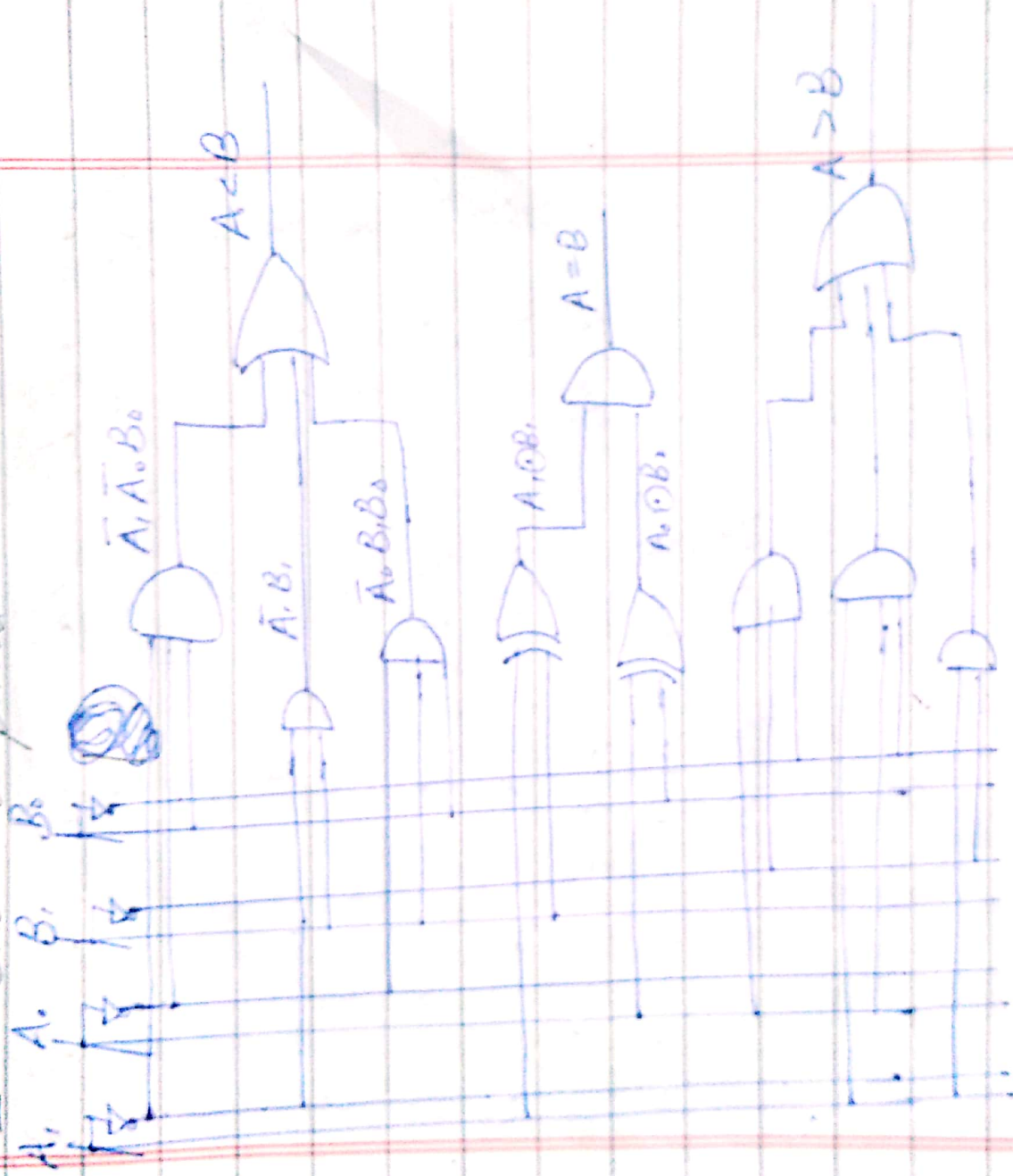
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2 - Bit comparator



4-bit magnitude comparator

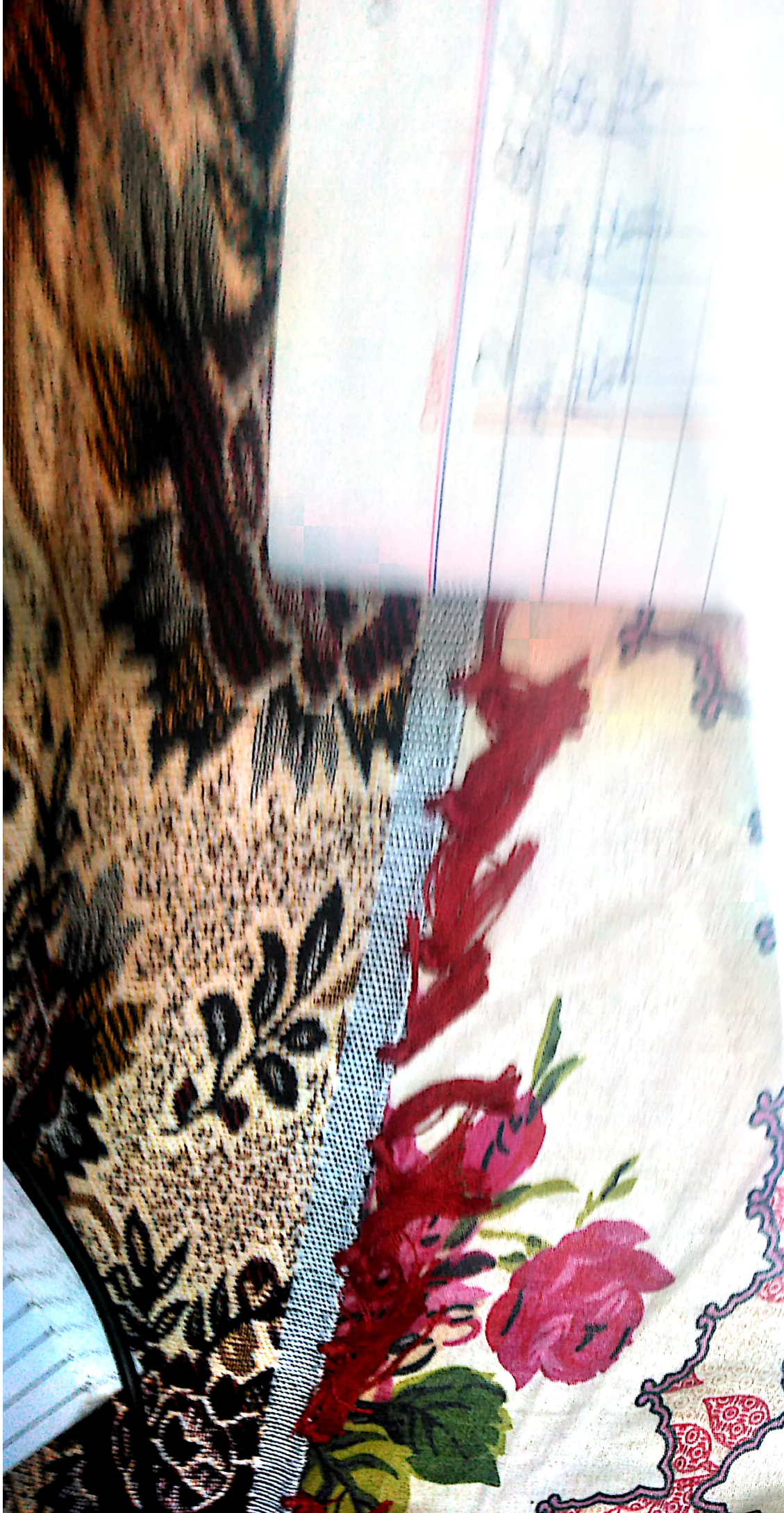
A 4-bit comparator used to compare two 4-bit words. The two 4-bit numbers are word

A: A₃A₂A₁A₀ & word B: B₃B₂B₁B₀

This circuit has 8 words and

3 binary outputs

A > B, A = B & A < B



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$A_3 B_3$	$A_2 B_2$	$A_1 B_1$	$A_0 B_0$	$A > B$	$A < B$	$A = B$
$A_3 > B_3$	x	x	x	1	0	0
$A_3 < B_3$	x	x	x	0	1	0
$A_3 = B_3$	$A_2 > B_2$	x	x	1	0	0
$A_3 = B_3$	$A_2 < B_2$	x	x	0	1	0
$A_3 = B_3$	$A_2 = B_2$	$A_1 > B_1$	x	1	0	0
$A_3 = B_3$	$A_2 = B_2$	$A_1 < B_1$	x	0	1	0
$A_3 = B_3$	$A_2 = B_2$	$A_1 = B_1$	$A_0 > B_0$	1	0	0
		$A_0 < B_0$	$A_0 = B_0$	0	1	1
		$A_0 = B_0$	$A_0 = B_0$			

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Inputs

Outputs

 $A, A_0, B, B_0, A > B, A = B, A < B$

0 0 0 0 0 0 1 0

0 0 0 1 0 0 1 1

0 0 1 0 0 0 0 1

0 0 1 1 0 0 0 1

0 1 0 0 1 0 0 0

0 1 0 1 0 0 1 0

0 1 1 0 0 0 1 1

0 1 1 1 0 0 1 1

1 0 0 0 1 0 0 0

1 0 0 0 1 0 0 0

1 0 1 0 0 1 0 0

1 0 1 1 0 0 0 1

1 1 0 0 1 0 0 0

1 1 0 1 1 0 0 0

1 1 1 0 1 0 0 0

1 1 1 1 0 1 0 0

K-Map for $A < B$

A, A_0	B, B_0	00	01	11	10
00	0	1	1	1	1
01	0	0	0	1	1
11	0	0	0	0	0
10	0	0	0	0	0

K-map for $A=B_1$

$A_1 A_0$ \ $B_1 B_0$	00	01	11	10
00	1	0	0	0
01	0	1	0	0
11	0	0	1	0
10	0	0	0	1

$$Y_2 = \bar{A}_1 \bar{A}_0 \bar{B}_1 \bar{B}_0 + \bar{A}_1 A_0 \bar{B}_1 B_0 + A_1 A_0 B_1 B_0 + A_1 \bar{A}_0 B_1 \bar{B}_0$$

After simplification $Y_2 = (A_1 \odot B_1)(A_0 \odot B_0)$ K-Map for $A > B_0$

$A_1 A_0$ \ $B_1 B_0$	00	01	11	10
00	0	0	0	0
01	1	0	0	0
11	1	1	1	1
10	1	1	1	0

$$Y_3 = A_0 \bar{B}_1 B_0 + A_1 \bar{B}_1 + A_1 A_0 B_0$$

Conclusion

After performing above experiment we have understood that the working and designing of comparators.