WCP52: Weekly Status Report
Week of Oct. 10, 2014.

Chris Pavlina

1. Hours spent:
   (a) Meeting: 1
   (b) EDA software preparation: 3
2. Accomplishments
   (a) Finalized first draft of specifications
   (b) Supplied KiCad build to team
3. Problems and Suggestions
   (a) A few of the PRS items need to be corrected still
4. Plans
   (a) Finish brainstorming "premortem analysis"
   (b) Begin designing test procedures

Harrison Owens

1. Accomplishments
   (a) Discussed Project requirement specification draft with Faculty Advisor and made Appropriate changes
   (b) Started brainstorming our "Pre Mortem" test or events that could go wrong while building the project
      i. Problems in software testing
      ii. Problems in Supply chain
      iii. Problems with budget
2. Problems
   (a) Found that we need to be more specific about our requirements as they will be graded
   (b) There were a couple of important requirements that were missing such as the safety protocols
3. Plans
   (a) As a group continue to practice making circuits using KiCad
   (b) Research methods on how to implement each part of the block diagram

Ken Zach

1. Hours spant working on project:
   (a) Meeting: 1.5 hours
   (b) KiCad: 1.5 hours
2. Problems/Solutions
   (a) None
3. Plans
   (a) start considering overall system architecture
   (b) do some research on how things can be accomplished (especially calculating phase)
   (c) look over project requirements before the final submission

Kaidi Xu

We team members had a meeting this Monday at senior design lab. During this meeting, we improved our Project Requirements Specification. We added some requirements and struck several requirements from the suggestions that Kyle give us last week. And our leader also made the KiCad let us to download.

We have a meet today with Professor Kyle, and show him our Project Requirements Specification. And here are something needs us to notice and change.

1. We need to change something about the standard Bode Plot spec.
   (a) describe what is meant by standard bode plot or get rid of the word standard.
   (b) or a plot of gain with respect to frequency.
2. SHALL go to 150MHz, and SHOULD go to 200MHz and keep amplitude spec up to 100 MHz
3. 3.2.5 change 80 to 60, and SHALL do 30dB instead of 40dB and should do 60dB.
4. should have a more precise phase accuracy(3 degrees)

Here is something we need to think about that might kill our project at the last, and know how we will avoid these problems.

- supply chain issues
- budget issues
- run out of time for software/testing
- general test-ability issues
- prototype breaking

From next week our next step is to figure out overall system architecture. We need to go high level than the system block diagram on the project write up. It needs to contain functional blocks and do not forget about power distribution.- figure out what is the best way to calculate phase.-figure out what might be the most difficult pieces and begin designing in such a way that make those difficult pieces easiest.