

# Gain and Phase Analyzer

## Interim Report

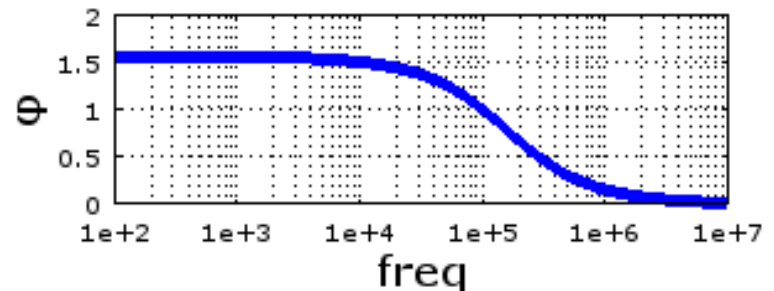
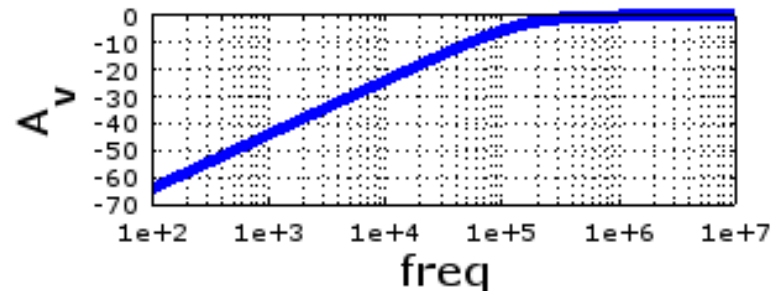
2014, December 5th

# Gain and Phase Analyzer

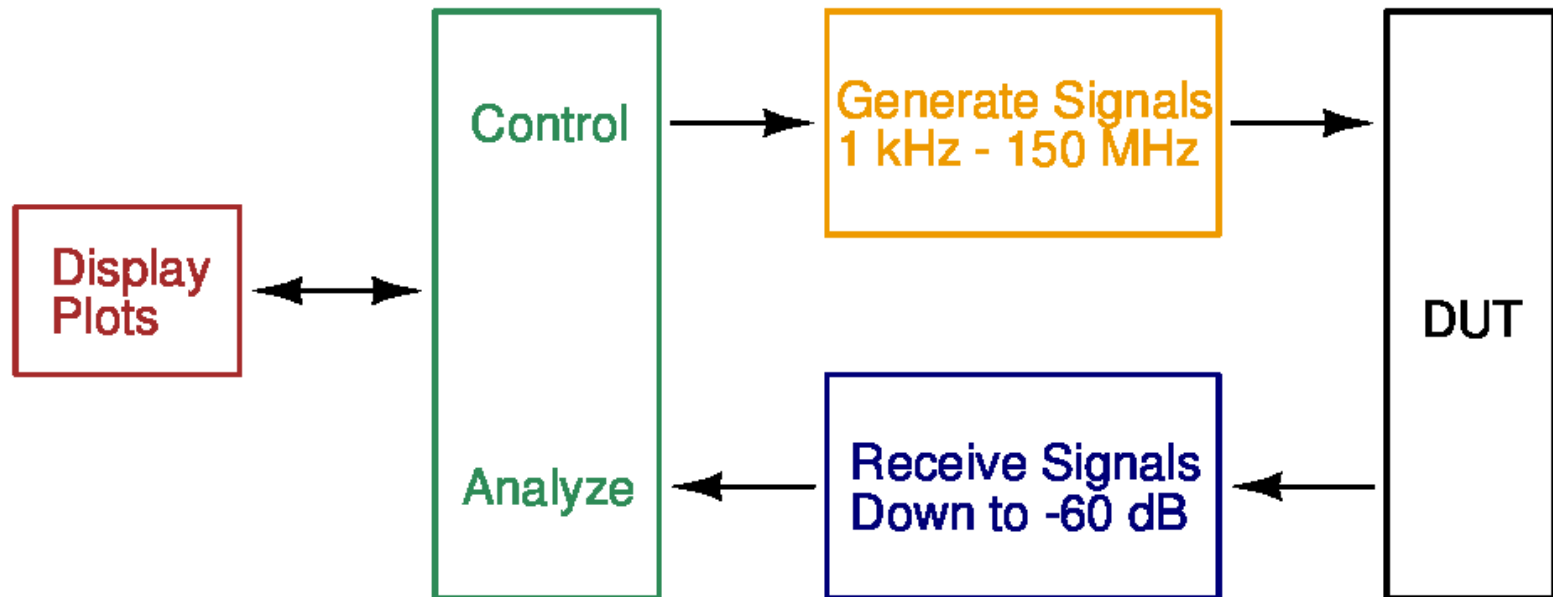
- Requirements
- Design
- Prototyping
- Schedule
- Budget
- Next Steps

# Purpose

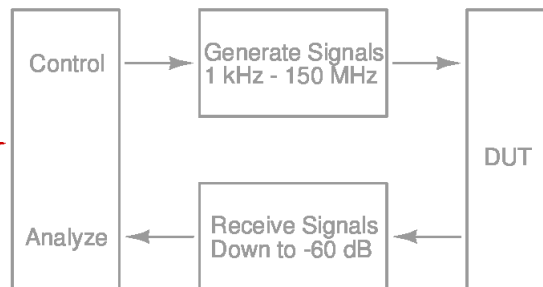
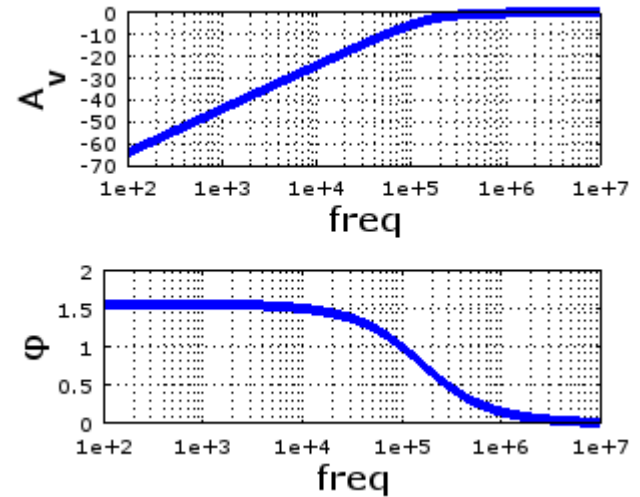
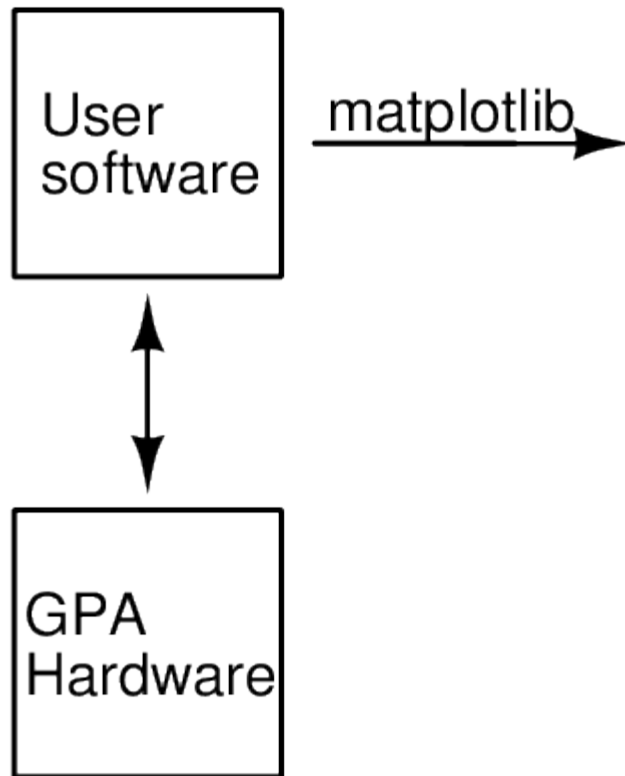
- Test the frequency behavior of filters, amplifiers
- Generate Bode plot with amplitude and phase



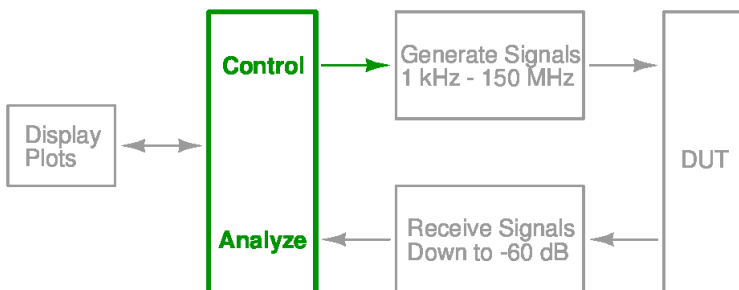
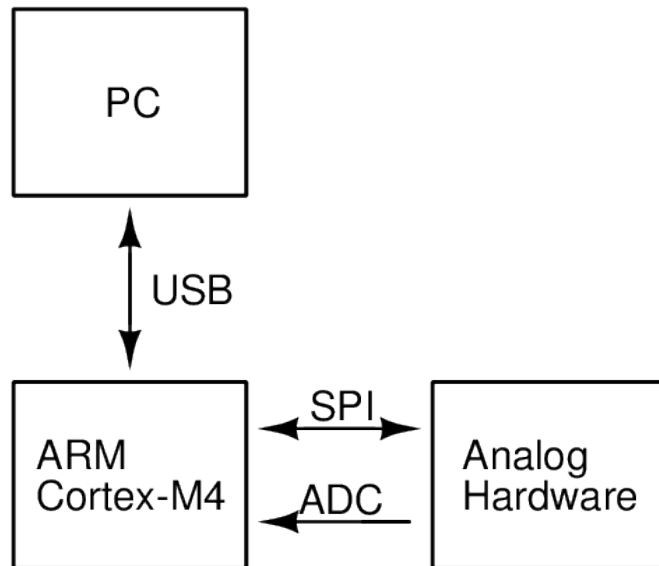
# Requirements



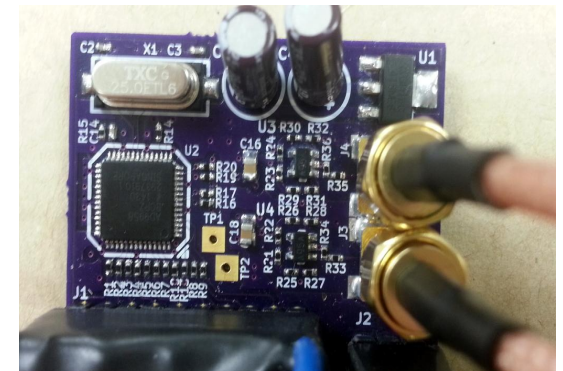
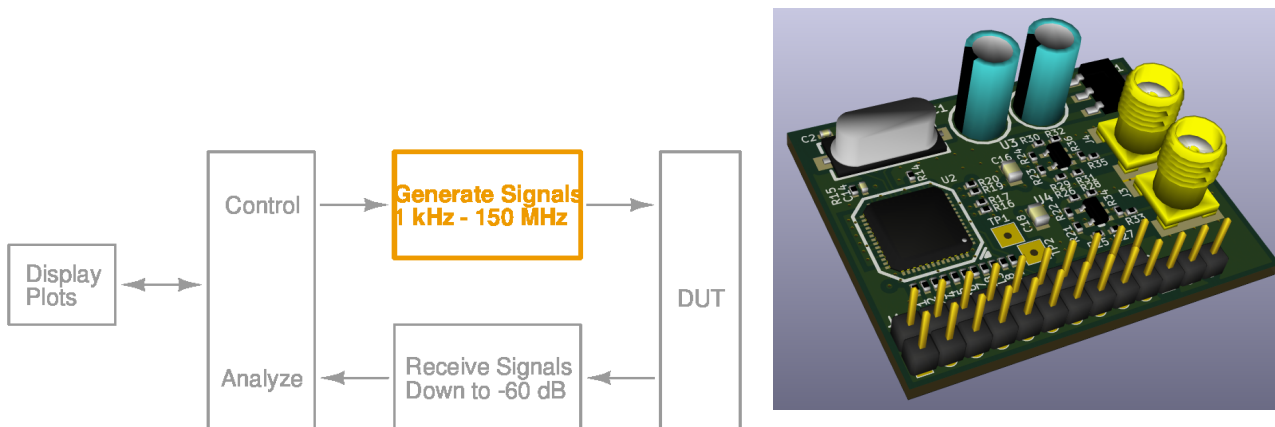
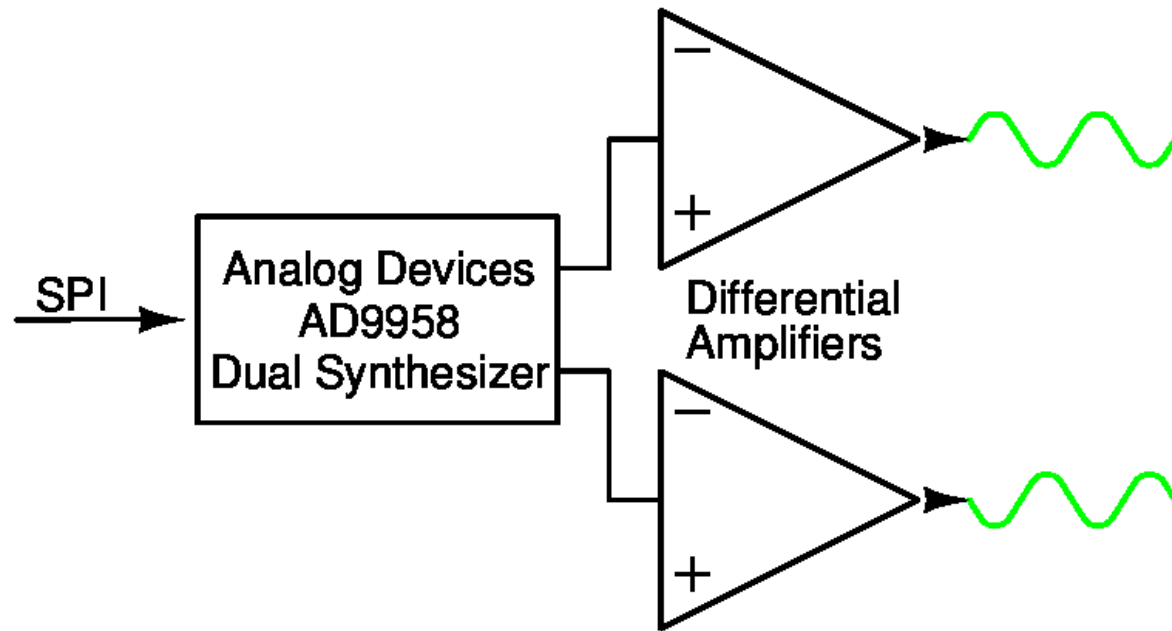
# Design — Software



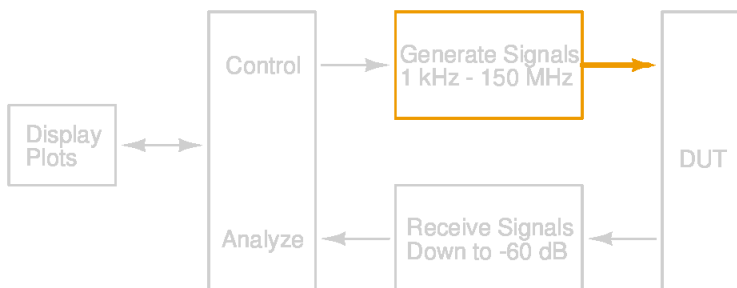
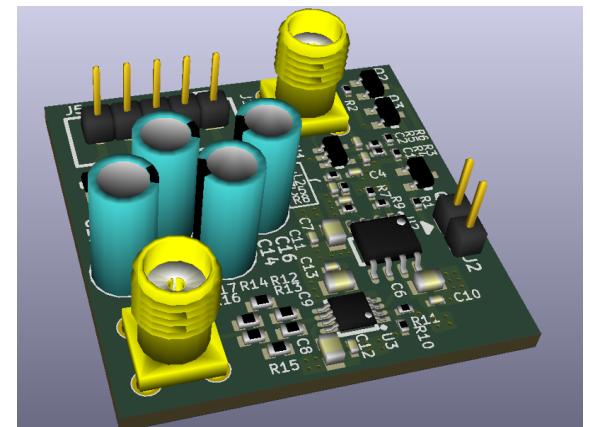
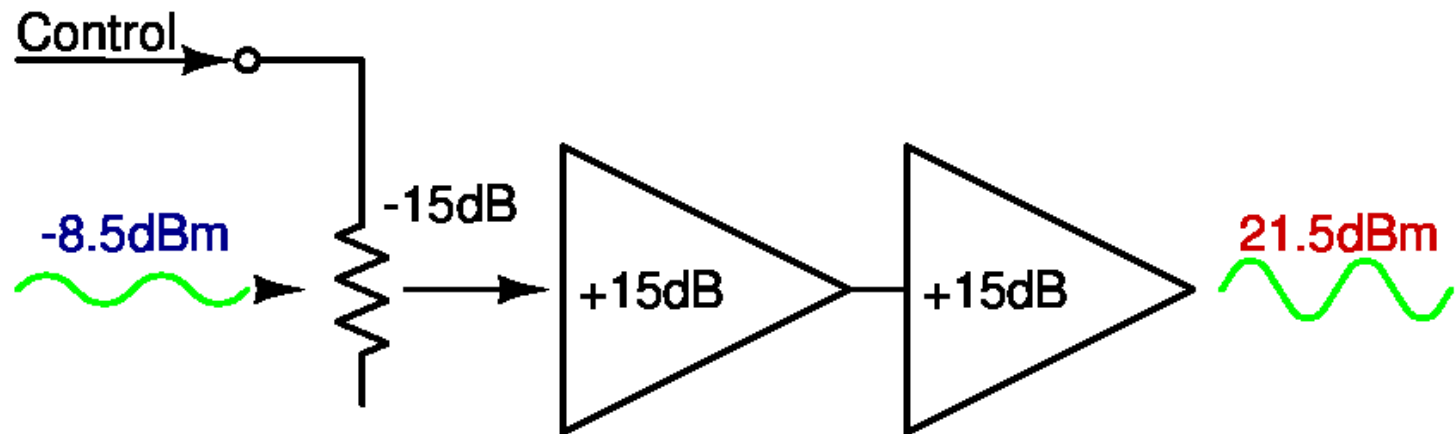
# Design — Microcontroller



# Design — Signal Generation

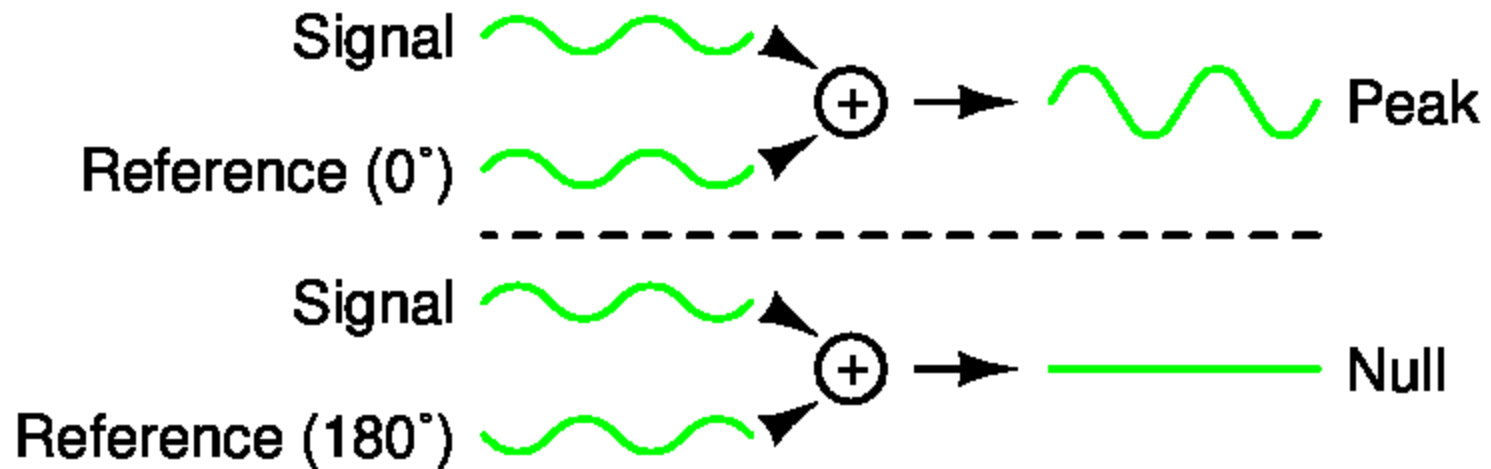


# Design — Signal Output

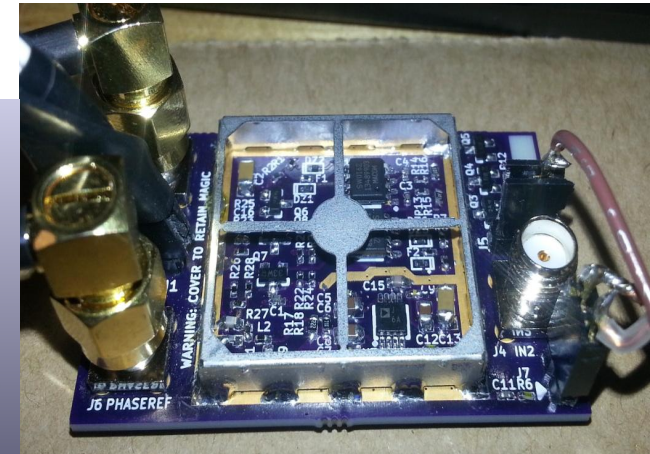
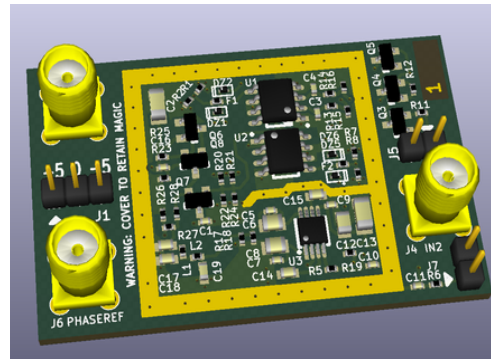
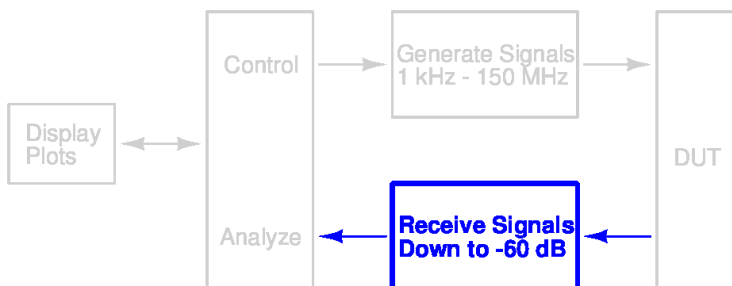
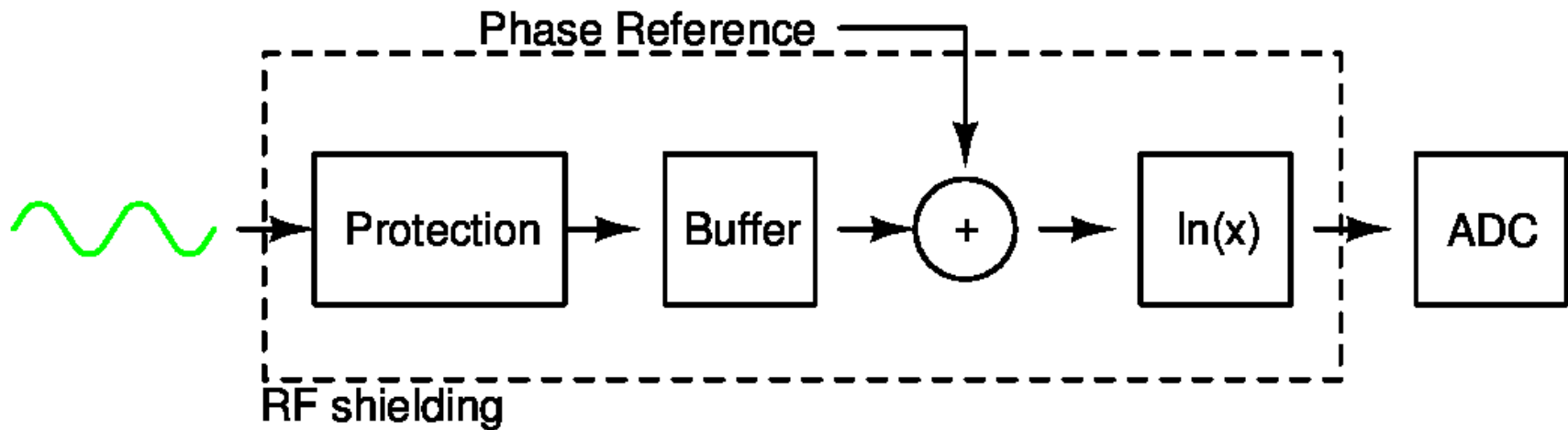




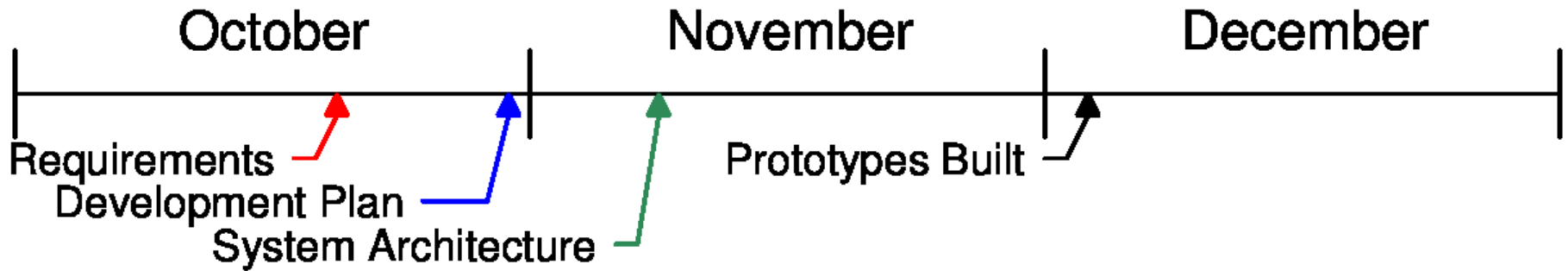
# Design — Phase Detection



# Design — Detection



# Timeline



# Schedule

Item	Start Date	End Date	Percent Complete
PRS	2014-10-03	2014-10-17	100%
PDP	2014-10-17	2014-10-31	100%
Architecture	2014-10-31	2014-11-14	100%
Interim Report	2014-11-14	2014-12-05	100%
Proto. built	2014-11-14	2014-12-05	100%
Proto. firmware	2014-11-10	2014-12-05	100%

# Budget

Item	Expended	Actual	Estimated to Completion	Estimated at Completion
Synthesizer	\$60	\$60	\$0	\$60
Input	\$70	\$70	\$0	\$70
Output amp.	\$66	\$66	\$0	\$66
Power supply	\$0	\$0	\$0	\$0
Final build	\$0		\$120	\$120
Enclosure	\$0		\$30	\$30
Misc/re-spins	\$0		\$100	\$100
<b>Total</b>	<b>\$196</b>	<b>\$196</b>	<b>\$250</b>	<b>\$446</b>

# Next Steps

## Hardware

- Combine prototypes into a final product

## Software

- Finish firmware with full analysis capability
- Write PC software that receives and plots data

# Conclusion

- Product can capture Bode plots
- Uses signal synthesizer, output amplifier, input frontend, ADC, and microcontroller
- These have been prototyped and tested