**Weekly Status Report**

**WCP 52**

**We have first meeting with Professor Kyle this Friday. First, we talked about this project and showed our team’s opinion about the project.**

**We showed the project proposal form to Kyle, mainly about our project requirement. With Professor Kyle’s suggest, we changed several requirements about project. First, change in dynamic range: “shall” resolve to 40dB (not 50), “should” to 60dB (not 80). Second, “Amplitude accuracy should be within 1dB, and phase accuracy within 1 degree, for frequencies less than 20MHz” we strike this requirement completely, because it is hard to achieve and unnecessary. Third, “The analyzer shall be capable of performing most analysis function directly, allowing the PC software to be simple” we could strike this requirement or change “shall” to “should”.**

**And we also have a problem need to solve, that is do we need use a microprocessor or an FPGA. Which one is better.**