

## 2.6 Another Style of Makefile

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When the objects of a makefile are created only by implicit rules, an alternative style of makefile is possible. In this style of makefile, you group **entries** by their prerequisites instead of by their targets. Here is what one looks like:

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当 makefile 的对象仅由隐式规则创建时，makefile 的替代样式是可能的。在这种风格的 makefile 中，您可以根据先决条件而不是目标对 **条目** 进行分组。这是一个样子：

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```
objects = main.o kbd.o command.o display.o \  
         insert.o search.o files.o utils.o  
  
edit : $(objects)  
      cc -o edit $(objects)  
  
$(objects) : defs.h  
kbd.o command.o files.o : command.h  
display.o insert.o search.o files.o : buffer.h
```

Here defs.h is given as a prerequisite of all the object files; command.h and buffer.h are prerequisites of the specific object files listed for them.

Whether this is better is a matter of taste: it is more compact, but some people dislike it because they find it clearer to put all the information about each target in one place.

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这里 defs.h 是所有目标文件的先决条件；command.h 和 buffer.h 是为其列出的特定目标文件的先决条件。

这是否更好是一个品味问题：它更紧凑，但有些人不喜欢它，因为他们发现将有关每个目标的所有信息放在一个地方更清晰。

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