

```
1
2
3  Assembly 'Language' {
4
5      [For Beginners Workshop]
6
7
8
9      < Here is where your presentation begins >
10
11
12  }
13
14
```

1 Step < /1 > {



< Compilation, architectures processeur,
mémoire, assembleur >

4
5
6 }
7

8 Step < /2 > {



< Hello_World.asm >

11
12
13 }
14

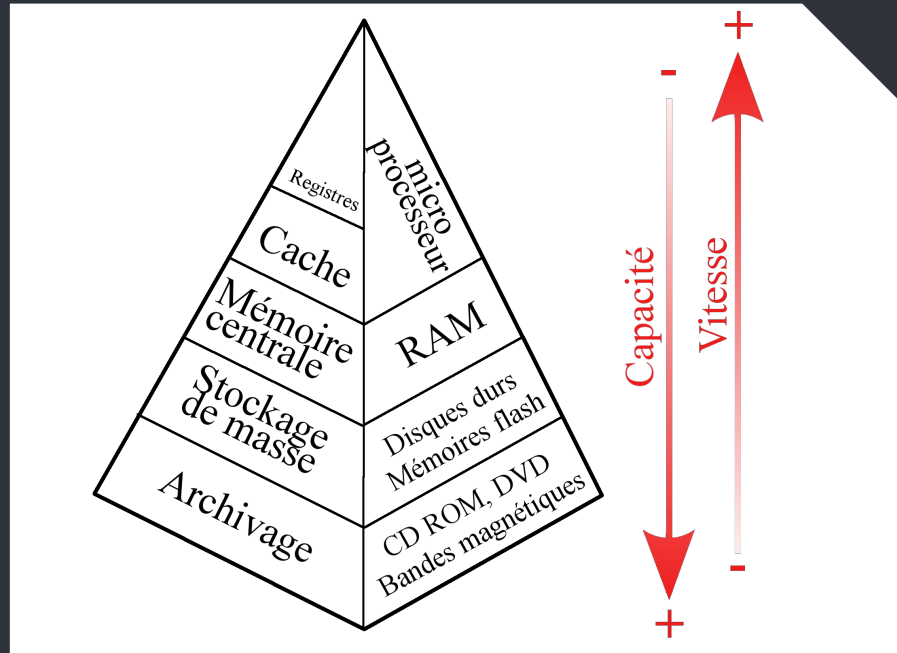
01 {

[Step 0x01]

< Compilation, architectures
processeur, mémoire, assembleur >

}

Types de 'Mémoires';



Architectures 'Processeur';

RISC

- ARM

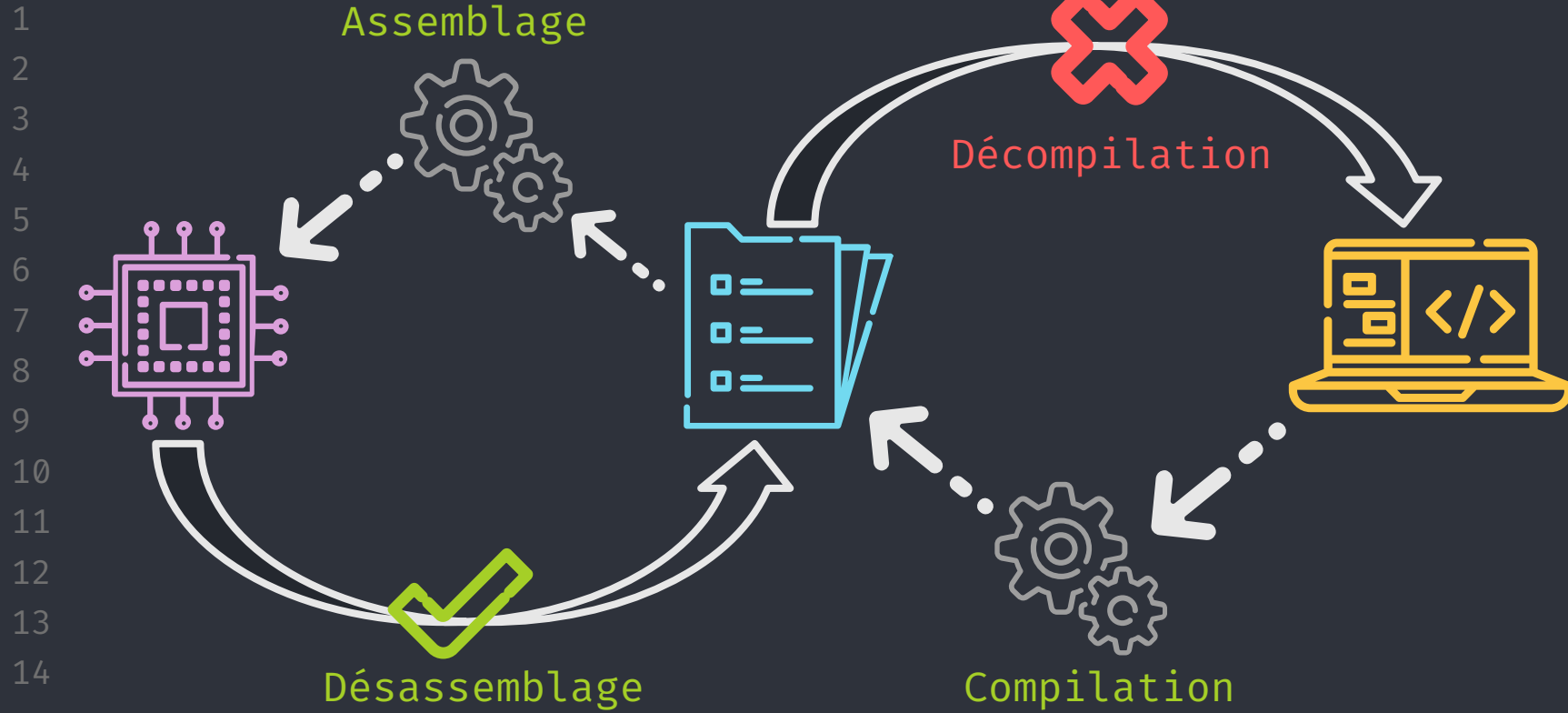
- ARM 32 bits
- ARM64

- MIPS

CISC

- Intel

- x86 (i386)
- x86_64 (amd64)



Language 'Assembleur';

$42_{10} \iff 0 \times 2A \iff 52_8 \iff 0010 \ 1010_2$

48 31 ff	\iff	xor %rdi, %rdi
48 c7 c0 3c 00 00 00	\iff	mov \$0x3c, %rax
0f 05	\iff	syscall

Syntaxe {

- INTEL

mov out, in

1: mov al, 1

2: mov DWORD PTR[-4 +ebp], eax

- AT&T

mov in, out

1: movb \$1, %al

2: mov %eax, -4(%ebp)

}


```
1 Introduction; {
```

```
2
```

```
3
```

```
4     Mov
```

```
5     Lea
```

```
6     Inc / dec
```

```
7     Push / pop
```

```
8     Add / sub
```

```
9     Syscall
```

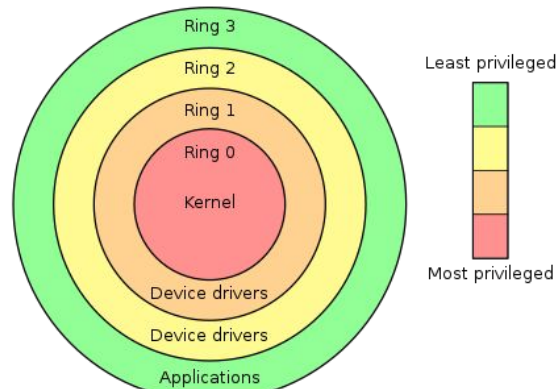
```
10    Jump / je / jne / ...
```

```
11    Call / ret
```

```
12
```

```
13 }
```

```
14
```



Registres{

Généraux	Pointeurs	Segments
AX	SP	CS
BX	BP	DS
CX	SI	SS
DX	DI	ES

IP	FLAGS
----	-------

}

Registres{

