

Name: Waris Damkham ID: 6388014 Sec: 1

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Q1. Output from “matmul2.cu”

[illegible]

Q2. Source code and output from “matmul_shared.cu”

Source code:

```
/home/u6388014/matmul_shared.cu - 10.34.73.11 - Editor - WinSCP
#include<stdio.h>

#define Width 32 // size of Width x Width matrix
#define TILE_WIDTH 16

__global__ void matrixMul(float* A, float* B, float* C, int width)
{
    __shared__ float As[TILE_WIDTH][TILE_WIDTH];
    __shared__ float Bs[TILE_WIDTH][TILE_WIDTH];
    int row = blockIdx.y * TILE_WIDTH + threadIdx.y;
    int col = blockIdx.x * TILE_WIDTH + threadIdx.x;
    float c_val = 0.0f; for(int i = 0; i < width/TILE_WIDTH; i++)
    {
        As[threadIdx.y][threadIdx.x] = A[row * width + (i * TILE_WIDTH + threadIdx.x)];
        Bs[threadIdx.y][threadIdx.x] = B[(i * TILE_WIDTH + threadIdx.y) * width + col];
        __syncthreads();
        for(int k = 0; k < TILE_WIDTH; k++)
            c_val += As[threadIdx.y][k] * Bs[k][threadIdx.x]; __syncthreads();
    }
    C[row * width + col] = c_val;
}

int main (int argc, char *argv[] ) {
    const int n = 16;
    int i,j;
    int size = Width * Width * sizeof(float);
    float M[Width][Width], N[Width][Width], P[Width][Width];
    float *Md, *Nd, *Pd;

    for (i=0; i < Width; i++) {
        for (j=0; j < Width; j++) {
            M[i][j] = 1; N[i][j] = 2;
        }
    }

    cudaMalloc( (void**)&Md, size);
    cudaMalloc( (void**)&Nd, size);
    cudaMalloc( (void**)&Pd, size);

    cudaMemcpy( Md, M, size, cudaMemcpyHostToDevice);
    cudaMemcpy( Nd, N, size, cudaMemcpyHostToDevice);

    // Setup the execution configuration
    dim3 dimBlock(n, n);
    dim3 dimGrid(Width/n, Width/n);

    // Launch the device computation threads!
    matrixMul<<<dimGrid, dimBlock>>>(Md, Nd, Pd, Width);

    // Read P from the device
    cudaMemcpy(P, Pd, size, cudaMemcpyDeviceToHost);

    // Free device matrices
    cudaFree(Md); cudaFree(Nd); cudaFree(Pd);

    for (i=0; i < Width; i++) {
        for (j=0; j < Width; j++) {
            printf("%.2f ", P[i][j]);
        }
        printf("\n");
    }
}
```

```
tee98901@elucuser ~]$ ./matmahard
```

```
[u6388014@cluster ~]$ nvcc --version
nvcc: NVIDIA (R) Cuda compiler driver
Copyright (c) 2005-2009 NVIDIA Corporation
Built on Fri_Feb_19_19:12:59_PST_2010
Cuda compilation tools, release 3.0, V0.2.1221
```

```
Freq 0 = 103
Freq 1 = 103
Freq 2 = 103
Freq 3 = 103
Freq 4 = 103
Freq 5 = 103
Freq 6 = 103
Freq 7 = 103
Freq 8 = 103
Freq 9 = 103
#elements = 1024
```

Q4.2 Real output from running “hist.cu”

```
[u6388014@cluster ~]$ ./hist
Freq 0 = 103
Freq 1 = 103
Freq 2 = 103
Freq 3 = 103
Freq 4 = 102
Freq 5 = 102
Freq 6 = 102
Freq 7 = 102
Freq 8 = 102
Freq 9 = 102
#elements = 1024
```