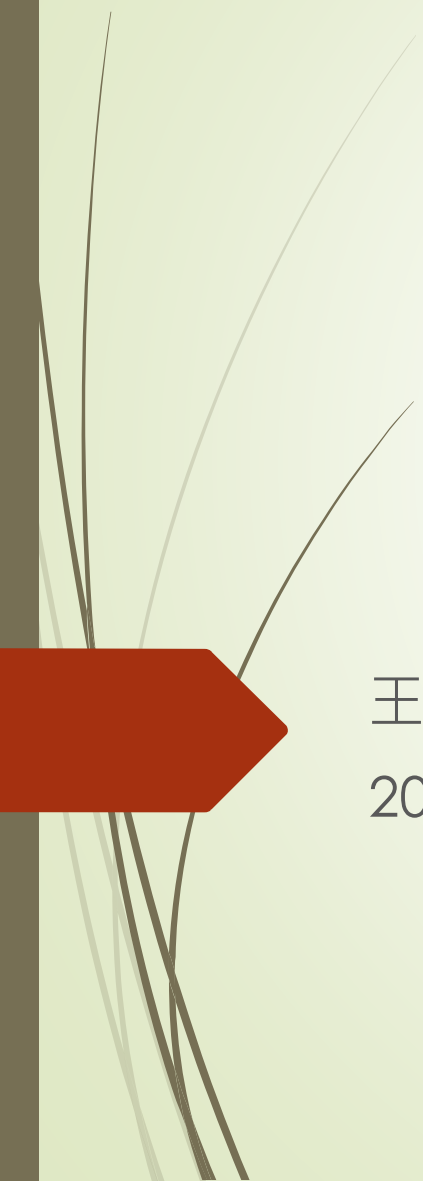


----- LAB 1 -----

MIPS Assembly Programming



王宣仁

2017/10/25

實驗內容

- ➡ 實驗目的
- ➡ MIPS Assembly Programming
- ➡ MARS (MIPS Assembler and Runtime Simulator)
- ➡ 課堂實作
- ➡ 課後作業
- ➡ 計分方式
- ➡ 附錄

實驗目的

- ➡ 熟悉 MIPS assembly programming
- ➡ 認識 MIPS編譯工具MARS
- ➡ 實作 MIPS assembly programming

MIPS Assembly Programming

- ➔ 程式由data section與text section構成，data section 包含global data而text section包含組語指令。

	Data section
<code>.data</code>	
<code>input : .asciiz "pls enter a number : "</code>	
<code>output : .asciiz "\nearest prime number is: "</code>	
	Text section
<code>.text</code>	
<code>.globl main</code>	
<code>main :</code>	
<code>#read the first number</code>	
<code>li \$v0, 4</code>	<code># load syscall code (4 : print string)</code>
<code>la \$a0, input</code>	<code># load address of string to be printed into \$a0</code>
<code>syscall</code>	<code># print str</code>
	<code>.</code>
	<code>.</code>
	<code>.</code>
<code>down:</code>	
<code>add \$t1, \$zero, \$t2</code>	
<code>loop_ob :</code>	
<code>add \$t3, \$zero, \$t0 #reset \$t3</code>	
<code>sub \$t1, \$t1, \$t0 # \$t0 down</code>	
<code>j loop_i</code>	

範例程式解說 – 最大公因數

```
.data
msgb : .asciiz "The GCD is : "
inputa : .asciiz "The first number= "
inputb : .asciiz "The second number= "
.text
.globl main
main :

    #輸入兩自然數求最大公因數
    #讀第一個數到t1
    li $v0, 4                # load syscall code (4 : print string)
    la $a0, inputa           # load address of string to be printed into $a0
    syscall                  # print str
    li $v0, 5                # load syscall code (5 : read int)
    syscall                  # read int
    add $t1, $zero, $v0

    #讀第二個數到t2
    li $v0, 4
    la $a0, inputb
    syscall
    li $v0, 5
    syscall
    add $t2, $zero, $v0
```

#比對 t1 t2 大小，若t1大則做減法，若t1較小則與t2做對調

```
comp :    slt $t0, $t1, $t2
          beq $t0, $zero, subb
          add $t3, $t1, $zero
          add $t1, $t2, $zero
          add $t2, $t3, $zero
subb :    sub $t1, $t1, $t2
          bne $t1, $zero, comp
```

#顯示最大公因數

```
li $v0, 4
la $a0, msgb
syscall
add $a0,$zero $t2
li $v0, 1
syscall
li $v0, 10
syscall
```

MARS (MIPS Assembler and Runtime Simulator)

- MARS is MIPS assembler and runtime simulator
- GUI with point-and-click control and integrated editor
- Easily editable register and memory values, similar to a spreadsheet
- Display values in hexadecimal or decimal
- Command line mode for instructors to test and evaluate many programs easily
- MARS website <http://courses.missouristate.edu/kenvollmar/mars/>

MARS介面

Toolbar

The screenshot displays the MARS 4.5 interface. At the top, a toolbar contains various icons for file operations, editing, and execution, with a red box highlighting it. Below the toolbar is the code editor, which contains the following assembly code:

```
near_prime_num.s  
1 .data
```

A red box highlights the code editor area, with the label "Code" in red text. To the right of the code editor is a window titled "Registers" showing a table of registers and their values. The table has columns for Name, Number, and Value. The registers listed are \$zero, \$at, \$v0, \$v1, \$a0, \$a1, \$a2, \$a3, \$t0, \$t1, \$t2, \$t3, \$t4, \$t5, \$t6, \$t7, \$t8, \$t9, \$s0, \$s1, \$s2, \$s3, \$s4, \$s5, \$s6, \$s7, \$s8, \$s9, \$f0, \$f1, \$f2, \$f3, \$f4, \$f5, \$f6, \$f7, \$f8, \$f9, \$f10, \$f11, \$f12, \$f13, \$f14, \$f15, \$f16, \$f17, \$f18, \$f19, \$f20, \$f21, \$f22, \$f23, \$f24, \$f25, \$f26, \$f27, \$f28, \$f29, \$f30, \$f31, \$ra, \$pc, \$hi, and \$lo. The values are mostly 0x00000000, except for \$ra which is 0x00000000 and \$pc which is 0x00000000.

At the bottom of the interface is a window titled "Mars Messages" showing the execution output:

```
pls enter a number : 159  
nearest prime number is: 163  
nearest prime number is: 157  
-- program is finished running --
```

A red box highlights the "Mars Messages" window, with the label "執行資訊" in red text. A "Clear" button is located at the bottom left of the "Mars Messages" window.

At the bottom right of the interface is a window titled "暫存器訊息" (Register Information) which is currently empty.

組譯與執行

1. Open code 3. Run the current file

The screenshot shows the MARS MIPS assembler simulator. Red arrows and text indicate the workflow: 1. Open code (arrow to File menu), 2. Assemble the current file (arrow to the Assemble icon in the toolbar), and 3. Run the current file (arrow to the Run icon in the toolbar). The main editor shows the assembly code for 'near_prime_num.s'. The bottom panel displays the 'Mars Messages' window with the output of the program. On the right, a 'Registers' window shows the state of MIPS registers.

2. Assemble the current file

4. 輸入指令並閱讀資訊

Registers	Coproc 1	Coproc 0
Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00100000
\$v0	2	0x0000000a
\$v1	3	0x00000000
\$a0	4	0x00000094
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x00000001
\$t1	9	0x00000094
\$t2	10	0x0000009f
\$t3	11	0x00000094
\$t4	12	0x00000000
\$t5	13	0x00000002
\$t6	14	0x00000000
\$t7	15	0x00000000
\$s0	16	0x00000000
\$s1	17	0x00000094
\$s2	18	0x00000000
\$s3	19	0x00000094
\$s4	20	0x00000000
\$s5	21	0x00000000
\$s6	22	0x00000000
\$s7	23	0x00000000
\$s8	24	0x00000000
\$s9	25	0x00000000
\$k0	26	0x00000000
\$k1	27	0x00000000
\$gp	28	0x10000000
\$sp	29	0x7fffffc
\$fp	30	0x00000000
\$ra	31	0x00000000
pc		0x004000a0
hi		0x00000000
lo		0x00000000

pls enter a number : 159
nearest prime number is: 163
nearest prime number is: 157
-- program is finished running --

啟用 Windows
移至 [設定] 以啟用 Windows

逐步執行

1. 選擇execute tab
2. Assemble and run one step at a time

C:\Users\Wang\Google Drive\106-1課程\CO\LAB\lab1\LAB1_code\GCD20170821\GCD20170821\MARS 4.5

File Edit Run Settings Tools Help

Run speed at max (no interaction)

4. 觀察暫存器

3. Observe code

5. 觀察訊息

啟用 Windows
移至 [設定] 以啟用 Windows *

Bkpt	Address	Code	Basic	Source
	0x00400000	0x24020004	addiu \$2,\$0,0x00000004	9: li \$v0, 4 # load syscall code (4 : print string)
	0x00400004	0x3c011001	lui \$1,0x00001001	10: la \$a0, input # load address of string to be printed into \$a0
	0x00400008	0x3424000e	ori \$4,\$1,0x0000000e	
	0x0040000c	0x0000000c	syscall	11: syscall # print str
	0x00400010	0x24020005	addiu \$2,\$0,0x00000005	13: li \$v0, 5 # load syscall code (5 : read int)
	0x00400014	0x0000000c	syscall	14: syscall # read int
	0x00400018	0x00024820	add \$9,\$0,\$2	15: add \$t1, \$zero, \$v0
	0x0040001c	0x24020004	addiu \$2,\$0,0x00000004	23: li \$v0, 4 # load syscall code (4 : print string)
	0x00400020	0x3c011001	lui \$1,0x00001001	24: la \$a0, input # load address of string to be printed into \$a0
	0x00400024	0x34240022	ori \$4,\$1,0x00000022	
	0x00400028	0x0000000c	syscall	25: syscall # print str
	0x0040002c	0x24020005	addiu \$2,\$0,0x00000005	27: li \$v0, 5 # load syscall code (5 : read int)
	0x00400030	0x0000000c	syscall	28: syscall # read int
	0x00400034	0x00025020	add \$10,\$0,\$2	29: add \$t2, \$zero, \$v0
	0x00400038	0x012e402e	sll \$8,\$9,\$10	38: sll \$t0, \$t1, \$t2
	0x0040003c	0x11000004	beq \$8,\$0,0x00000004	39: beq \$t0, \$zero, subb

Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x10010000
\$v0	2	0x00000024
\$v1	3	0x00000000
\$a0	4	0x10010022
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x00000000
\$t1	9	0x00000034
\$t2	10	0x00000024
\$t3	11	0x00000000
\$t4	12	0x00000000
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0x00000000
\$t8	16	0x00000000
\$t9	17	0x00000000
\$k0	18	0x00000000
\$k1	19	0x00000000
\$k2	20	0x00000000
\$k3	21	0x00000000
\$k4	22	0x00000000
\$k5	23	0x00000000
\$k6	24	0x00000000
\$k7	25	0x00000000
\$k8	26	0x00000000
\$k9	27	0x00000000
\$gp	28	0x10000000
\$sp	29	0x7fffffc0
\$fp	30	0x00000000
\$ra	31	0x00000000
\$pc		0x00400038
\$hi		0x00000000
\$lo		0x00000000

Messages

The first number= 52
The second number= 42

查詢組語指令

點擊help

C:\Users\Wang\Google Drive\106-1課程\CO\LAB\lab1\near_prime_num.s - MARS 4.5

File Edit Run Settings Tools Help

Run speed at max (no interaction)

基本指令

System Call

MARS 4.5 Help

MIPS MARS License Bugs/Comments Acknowledgements Instruction Set Song

Operand Key for Example Instructions

label, target	any textual label
\$t1, \$t2, \$t3	any integer register
\$f2, \$f4, \$f6	even-numbered floating point register

Basic Instructions

Instruction	Description
abs.s \$t2,\$f4	Floating point absolute value double precision: Set \$t2 to absolute value of \$f4, double precision
abs.s \$f0,\$f1	Floating point absolute value single precision: Set \$f0 to absolute value of \$f1, single precision
add \$t1,\$t2,\$t3	Addition with overflow: set \$t1 to (\$t2 plus \$t3)
add.d \$f2,\$f4,\$f6	Floating point addition double precision: Set \$f2 to double-precision floating point value of \$f4 plus \$f6
add.s \$f0,\$f1,\$f3	Floating point addition single precision: Set \$f0 to single-precision floating point value of \$f1 plus \$f3
addi \$t1,\$t2,-100	Addition immediate with overflow: set \$t1 to (\$t2 plus signed 16-bit immediate)
addiu \$t1,\$t2,-100	Addition immediate unsigned without overflow: set \$t1 to (\$t2 plus signed 16-bit immediate), no overflow
addu \$t1,\$t2,\$t3	Addition unsigned without overflow: set \$t1 to (\$t2 plus \$t3), no overflow
andi \$t1,\$t2,100	Bitwise AND: Set \$t1 to bitwise AND of \$t2 and 100
andi \$t1,\$t2,100	Bitwise AND immediate: Set \$t1 to bitwise AND of \$t2 and zero-extended 16-bit immediate
bcif \$t1,label	Branch if specified FP condition flag false (BCIF, not BCLF): If Coprocessor 1 condition flag specified by immediate is false (zero) then branch to statement at label's address
bcif label	Branch if FP condition flag 0 false (BCIF, not BCLF): If Coprocessor 1 condition flag 0 is false (zero) then branch to statement at label's address
bcit \$t1,label	Branch if specified FP condition flag true (BCIT, not BCLT): If Coprocessor 1 condition flag specified by immediate is true (one) then branch to statement at label's address
bcit label	Branch if FP condition flag 0 true (BCIT, not BCLT): If Coprocessor 1 condition flag 0 is true (one) then branch to statement at label's address
beq \$t1,\$t2,label	Branch if equal: Branch to statement at label's address if \$t1 and \$t2 are equal
bgez \$t1,label	Branch if greater than or equal to zero: Branch to statement at label's address if \$t1 is greater than or equal to zero
bgez \$t1,label	Branch if greater then or equal to zero and link: If \$t1 is greater than or equal to zero, then set \$ra to the Program Counter and branch to statement at label's address
bgtz \$t1,label	Branch if greater than zero: Branch to statement at label's address if \$t1 is greater than zero
blez \$t1,label	Branch if less than or equal to zero: Branch to statement at label's address if \$t1 is less than or equal to zero
bltz \$t1,label	Branch if less than zero: Branch to statement at label's address if \$t1 is less than zero
bltz \$t1,label	Branch if less than zero and link: If \$t1 is less than or equal to zero, then set \$ra to the Program Counter and branch to statement at label's address
bne \$t1,\$t2,label	Branch if not equal: Branch to statement at label's address if \$t1 and \$t2 are not equal
break	Break execution: Terminate program execution with exception
break 100	Break execution with code: Terminate program execution with specified exception code
c.eq.d \$f2,\$f4	Compare equal double precision: If \$f2 is equal to \$f4 (double-precision), set Coprocessor 1 condition flag 0 true else set it false
c.eq.d \$f2,\$f4	Compare equal double precision: If \$f2 is equal to \$f4 (double-precision), set Coprocessor 1 condition flag specified by immediate to true else set it to false
c.eq.s \$f0,\$f1	Compare equal single precision: If \$f0 is equal to \$f1, set Coprocessor 1 condition flag 0 true else set it false
c.eq.s \$f0,\$f1	Compare equal single precision: If \$f0 is equal to \$f1, set Coprocessor 1 condition flag specified by immediate to true else set it to false
c.le.d \$f2,\$f4	Compare less or equal double precision: If \$f2 is less than or equal to \$f4 (double-precision), set Coprocessor 1 condition flag 0 true else set it false
c.le.d \$f2,\$f4	Compare less or equal double precision: If \$f2 is less than or equal to \$f4 (double-precision), set Coprocessor 1 condition flag specified by immediate true else set it false
c.le.s \$f0,\$f1	Compare less or equal single precision: If \$f0 is less than or equal to \$f1, set Coprocessor 1 condition flag 0 true else set it false
c.le.s \$f0,\$f1	Compare less or equal single precision: If \$f0 is less than or equal to \$f1, set Coprocessor 1 condition flag specified by immediate to true else set it to false
c.lt.d \$f2,\$f4	Compare less than double precision: If \$f2 is less than \$f4 (double-precision), set Coprocessor 1 condition flag 0 true else set it false

Mars Messages Run I/O

nearest prime number is:
-- program is finished run

pls enter a number: 70

nearest prime number is:
nearest prime number is:
-- program is finished run

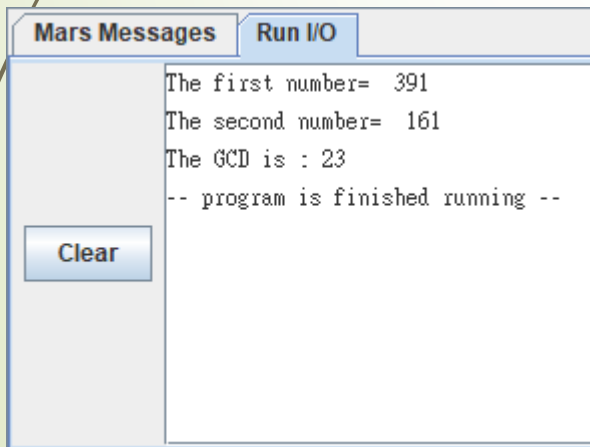
Close

啟用 Windows
移至 [設定] 以啟用 Windows。

課堂實作

- 題目一：執行範例程式並輸入兩自然數找出最大公因數
- 題目二：參考範例程式與PPT輸入兩自然數做減法
- 期 限：Lab1課堂結束前

題目一示範

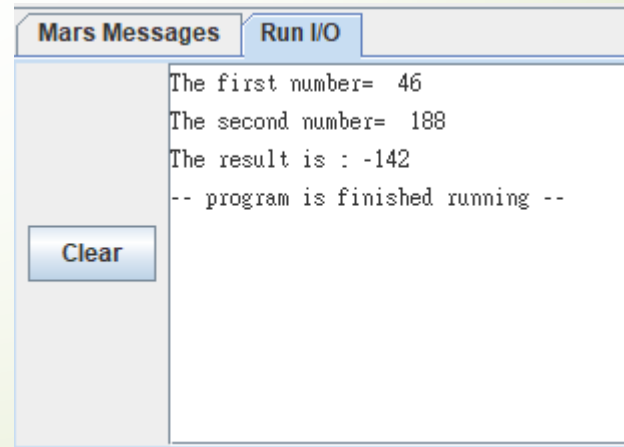


The screenshot shows a window titled "Mars Messages" with a "Run I/O" button. The text area contains the following output:

```
The first number= 391  
The second number= 161  
The GCD is : 23  
-- program is finished running --
```

Below the text area is a "Clear" button.

題目二示範



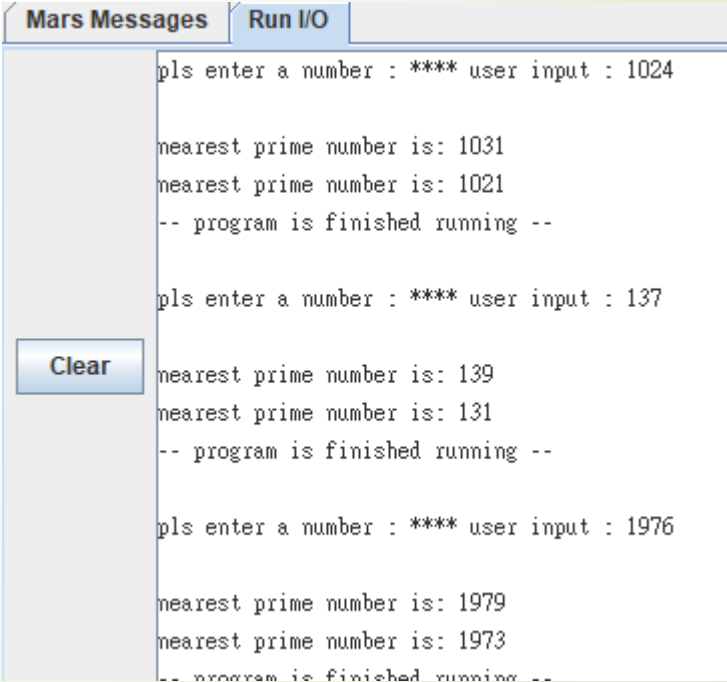
The screenshot shows a window titled "Mars Messages" with a "Run I/O" button. The text area contains the following output:

```
The first number= 46  
The second number= 188  
The result is : -142  
-- program is finished running --
```

Below the text area is a "Clear" button.

課後作業

- ➡ 題目：輸入 $X(X \in N, X > 3)$ ，找出最接近的兩質數 A 與 $B(X > A, X < B)$ ，並能給予文字提示如附圖執行1024、137與1976。
- ➡ 期限：2017/11/01
- ➡ 繳交：上傳程式碼至ECOURSE



```
Mars Messages Run I/O
pls enter a number : **** user input : 1024
nearest prime number is: 1031
nearest prime number is: 1021
-- program is finished running --

pls enter a number : **** user input : 137
nearest prime number is: 139
nearest prime number is: 131
-- program is finished running --

pls enter a number : **** user input : 1976
nearest prime number is: 1979
nearest prime number is: 1973
-- program is finished running --
```

Lab1計分方式

- 課堂練習：50%
- 課後作業：50%
 - 功 能 正 常：35%
 - 降低執行指令數：1~5 名 15%
6~20名 5%

附錄

Basic Instructions	Extended (pseudo) Instructions	Directives	Syscalls	Exceptions	Macros
abs.d \$f2,\$f4	Floating point absolute value double precision : Set \$f2 to absolute value of \$f4, double pre				
abs.s \$f0,\$f1	Floating point absolute value single precision : Set \$f0 to absolute value of \$f1, single pre				
add \$t1,\$t2,\$t3	Addition with overflow : set \$t1 to (\$t2 plus \$t3)				
add.d \$f2,\$f4,\$f6	Floating point addition double precision : Set \$f2 to double-precision floating point value c				
add.s \$f0,\$f1,\$f3	Floating point addition single precision : Set \$f0 to single-precision floating point value c				
addi \$t1,\$t2,-100	Addition immediate with overflow : set \$t1 to (\$t2 plus signed 16-bit immediate)				
addiu \$t1,\$t2,-100	Addition immediate unsigned without overflow : set \$t1 to (\$t2 plus signed 16-bit immediate),				
addu \$t1,\$t2,\$t3	Addition unsigned without overflow : set \$t1 to (\$t2 plus \$t3), no overflow				
and \$t1,\$t2,\$t3	Bitwise AND : Set \$t1 to bitwise AND of \$t2 and \$t3				
andi \$t1,\$t2,100	Bitwise AND immediate : Set \$t1 to bitwise AND of \$t2 and zero-extended 16-bit immediate				
bc1f 1,label	Branch if specified FP condition flag false (BC1F, not BCLF) : If Coprocessor 1 condition fla				
bc1f label	Branch if FP condition flag 0 false (BC1F, not BCLF) : If Coprocessor 1 condition flag 0 is f				
bc1t 1,label	Branch if specified FP condition flag true (BC1T, not BCLT) : If Coprocessor 1 condition flag				
bc1t label	Branch if FP condition flag 0 true (BC1T, not BCLT) : If Coprocessor 1 condition flag 0 is t				
beq \$t1,\$t2,label	Branch if equal : Branch to statement at label's address if \$t1 and \$t2 are equal				
bgez \$t1,label	Branch if greater than or equal to zero : Branch to statement at label's address if \$t1 is g				
bgezal \$t1,label	Branch if greater then or equal to zero and link : If \$t1 is greater than or equal to zero, t				
bgtz \$t1,label	Branch if greater than zero : Branch to statement at label's address if \$t1 is greater than z				
blez \$t1,label	Branch if less than or equal to zero : Branch to statement at label's address if \$t1 is less				
bltz \$t1,label	Branch if less than zero : Branch to statement at label's address if \$t1 is less than zero				
bltzal \$t1,label	Branch if less than zero and link : If \$t1 is less than or equal to zero, then set \$ra to the				
bne \$t1,\$t2,label	Branch if not equal : Branch to statement at label's address if \$t1 and \$t2 are not equal				

<code>or \$t1,\$t2,\$t3</code>	Bitwise OR : Set \$t1 to bitwise OR of \$t2 and \$t3
<code>ori \$t1,\$t2,100</code>	Bitwise OR immediate : Set \$t1 to bitwise OR of \$t2 and zero-extended 16-bit immediate
<code>round.w.d \$f1,\$f2</code>	Round double precision to word : Set \$f1 to 32-bit integer round of double-precision floating point value
<code>round.w.s \$f0,\$f1</code>	Round single precision to word : Set \$f0 to 32-bit integer round of single-precision floating point value
<code>sb \$t1,-100(\$t2)</code>	Store byte : Store the low-order 8 bits of \$t1 into the effective memory byte address
<code>sc \$t1,-100(\$t2)</code>	Store conditional : Paired with Load Linked (ll) to perform atomic read-modify-write
<code>sdcl \$f2,-100(\$t2)</code>	Store double word from Coprocessor 1 (FPU) : Store 64 bit value in \$f2 to effective memory address
<code>sh \$t1,-100(\$t2)</code>	Store halfword : Store the low-order 16 bits of \$t1 into the effective memory address
<code>sll \$t1,\$t2,10</code>	Shift left logical : Set \$t1 to result of shifting \$t2 left by number of bits
<code>sllv \$t1,\$t2,\$t3</code>	Shift left logical variable : Set \$t1 to result of shifting \$t2 left by number of bits in \$t3
<code>slt \$t1,\$t2,\$t3</code>	Set less than : If \$t2 is less than \$t3, then set \$t1 to 1 else set \$t1 to 0
<code>slti \$t1,\$t2,-100</code>	Set less than immediate : If \$t2 is less than sign-extended 16-bit immediate, then set \$t1 to 1 else set \$t1 to 0
<code>sltiu \$t1,\$t2,-100</code>	Set less than immediate unsigned : If \$t2 is less than sign-extended 16-bit immediate, then set \$t1 to 1 else set \$t1 to 0
<code>sltu \$t1,\$t2,\$t3</code>	Set less than unsigned : If \$t2 is less than \$t3 using unsigned comparison, then set \$t1 to 1 else set \$t1 to 0
<code>sqrtd \$f2,\$f4</code>	Square root double precision : Set \$f2 to double-precision floating point square root of \$f4
<code>sqrts \$f0,\$f1</code>	Square root single precision : Set \$f0 to single-precision floating point square root of \$f1
<code>sra \$t1,\$t2,10</code>	Shift right arithmetic : Set \$t1 to result of sign-extended shifting \$t2 right by number of bits
<code>srav \$t1,\$t2,\$t3</code>	Shift right arithmetic variable : Set \$t1 to result of sign-extended shifting \$t2 right by number of bits in \$t3
<code>srl \$t1,\$t2,10</code>	Shift right logical : Set \$t1 to result of shifting \$t2 right by number of bits
<code>srlv \$t1,\$t2,\$t3</code>	Shift right logical variable : Set \$t1 to result of shifting \$t2 right by number of bits in \$t3
<code>sub \$t1,\$t2,\$t3</code>	Subtraction with overflow : set \$t1 to (\$t2 minus \$t3)
<code>sub.d \$f2,\$f4,\$f6</code>	Floating point subtraction double precision : Set \$f2 to double-precision floating point subtraction of \$f4 and \$f6

System Call

Basic Instructions	Extended (pseudo) Instructions	Directives	Syscalls	Exceptions	Macros
--------------------	--------------------------------	------------	----------	------------	--------

Table of Available Services

Service	Code in \$v0	Arguments	Result
print integer	1	\$a0 = integer to print	
print float	2	\$f12 = float to print	
print double	3	\$f12 = double to print	
print string	4	\$a0 = address of null-terminated string to print	
read integer	5		\$v0 contains integer read
read float	6		\$f0 contains float read
read double	7		\$f0 contains double read
read string	8	\$a0 = address of input buffer \$a1 = maximum number of characters to read	<i>See note below table</i>
sbrk (allocate heap memory)	9	\$a0 = number of bytes to allocate	\$v0 contains address of allocated memory
exit (terminate execution)	10		