

# QCE'24 Quantum Resource Estimation Educational Challenge Submission

## - Matrix Inversion by QSVT

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### Abstract

We investigate the physical resources required to solve systems of linear equations using the quantum singular value transform algorithm. We find that empirically, both the qubit counts and runtimes scale favorably (polylogarithmically) with the input size. We predict that a  $10^{10}$ -dimensional banded circulant matrix may be inverted using under 2 million qubits in about one hour. We furthermore identify bottlenecks in the scaling and highlight opportunities for further optimization.

## 1 Introduction

Since the breakthrough 2009 algorithm of Harrow, Hasidim, and Lloyd (HHL algorithm), it has been well-known that for sparse, well-conditioned matrices, quantum computers are capable of solving the system of linear equations (SLE) problem exponentially faster than the best possible classical algorithms [1]. Due to the complexities of practical implementation, the performance analyses of quantum algorithms for solving SLEs have been primarily restricted to the asymptotic regime. In this project, we aim to answer the question of what physical quantum resources are required to solve SLEs of sizes at which the problem becomes classically intractable. Specifically, we consider the use of the quantum singular value transformation (QSVT) which achieves the best asymptotic complexity of all known quantum SLE algorithms [2, 3].

All code used to generate the results in this report can be found at <https://github.com/Walden-Killick/QCE24-QRE-Challenge>.

## 2 Preliminary

### 2.1 Problem definition

Given a quantum state  $|b\rangle$  and an efficient classical description of a sparse matrix  $A$ , the quantum system of linear equations (QSLE) problem is the problem of preparing the state  $|x\rangle = A^{-1}|b\rangle$ . Broadly speaking, the primary challenges in any QSLE algorithm are (1) accessing  $A$  in a quantum circuit, and (2) performing (approximately) the transformation  $A \mapsto A^{-1}$ . The HHL algorithm [1] achieves these goals using a sparse Hamiltonian simulation subroutine [4] and quantum phase estimation [5], respectively. By contrast, QSVT utilizes the fundamentally different techniques of block encoding [2] and quantum signal processing [6] and achieves an exponentially improved dependence on the target precision as well as a lesser improvement with respect to the condition number. We briefly review these techniques in the following subsection.

### 2.2 Matrix inversion by QSVT

To access  $A$  in a quantum circuit, QSVT relies on quantum oracles which efficiently encode both the non-zero structure and non-zero elements of  $A$ . These are defined rigorously as follows.

**Definition 2.1** (Sparse access oracles [7]). *Let  $A$  be an  $N \times N$   $s$ -sparse<sup>1</sup> matrix. Let  $c(j, l)$  be a function which returns the row index of the  $l$ -th non-zero matrix element in the  $j$ -th column of  $A$ . The sparse access oracles  $O_c$  and  $O_A$  for  $A$  are the unitary operators defined as*

$$O_c |l\rangle |j\rangle = |l\rangle |c(j, l)\rangle$$

and

$$O_A |0\rangle |l\rangle |j\rangle = \left( A_{c(j, l), j} |0\rangle + \sqrt{1 - |A_{c(j, l), j}|^2} |1\rangle \right) |l\rangle |j\rangle.$$

We should note that in this formalism,  $O_A$  also implicitly encodes the non-zero structure of  $A$  by calling  $c(j, l)$ ; however, for certain sufficiently well-structured matrices such as those considered in this project, this is not prohibitive.

The following informal theorem states that one can construct a unitary matrix which directly contains (a down-scaled version of)  $A$  as a submatrix (block).

**Theorem 2.2** (Block-encoding sparse matrices [2, 7]). *Let  $A, O_c, O_A$  be as in Definition 2.1. Using one call to each of  $O_c$  and  $O_A$ , we can construct a larger unitary matrix  $U_A$  for which the upper left  $N \times N$  block is equal to  $A/s$ .*

The final ingredient in matrix inversion by QSVT is then to transform the singular values of  $A/s$  as  $x \mapsto 1/x$ . QSVT cannot perform this transformation exactly, but can perform almost-arbitrary polynomial transformations [8] and thus approximate  $1/x$  to arbitrary precision.

**Theorem 2.3** (Matrix inversion by QSVT [2]). *Let  $A$  be a sparse matrix with condition number  $\kappa$  and let  $U_A$  block-encode  $A$  as in Theorem 2.2. Using  $O(\kappa \log(\kappa/\epsilon))$  calls to  $U_A$  and  $U_A^\dagger$ , we can construct a unitary which block-encodes  $A^{-1}$  to within accuracy  $\epsilon$ .*

We refer the reader to [3] for a pedagogical introduction to block encodings and QSVT.

### 2.3 Banded circulant matrices

To construct polynomial-size oracles  $O_c$  and  $O_A$ ,  $A$  must be well-structured in some way. For this project, we consider *banded circulant* matrices primarily as they are amongst the simplest classes of matrices which can be block-encoded efficiently whilst still exhibiting nontrivial solutions.

<sup>1</sup>A matrix is  $s$ -sparse if it has at most  $s$  non-zero entries per column.

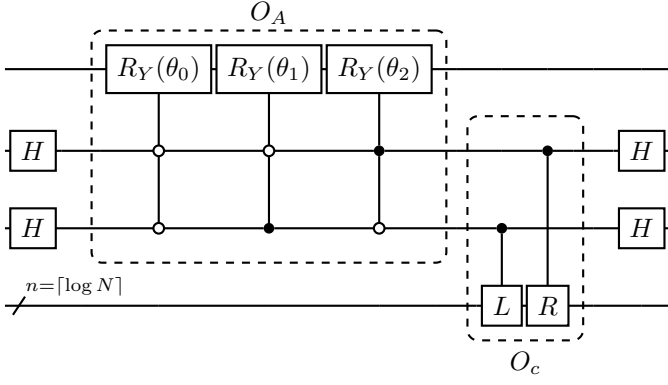


Figure 1: Quantum circuit for block-encoding an  $N \times N$  banded circulant matrix, adapted from [7]. For  $n = \lceil \log N \rceil$  qubits, the  $L$  and  $R$  gates each consist of  $n$  multi-controlled NOT gates with successively decreasing numbers of controls (see Figure 2).

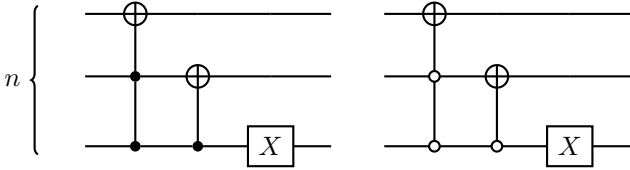


Figure 2: Quantum circuits for the  $L$  and  $R$  gates respectively, with  $n = 3$  qubits.

**Definition 2.4** (Circulant matrix). A circulant matrix is a matrix in which each row is equal to the previous row but with each element shifted one place to the right.

**Definition 2.5** (Banded circulant matrix). A circulant matrix is banded if only the first three entries of the second row are non-zero.

The following is an example of a banded circulant matrix with diagonal entries  $\alpha$ , sub-diagonal entries  $\beta$ , and super-diagonal entries  $\gamma$ :

$$\begin{bmatrix} \alpha & \gamma & 0 & \cdots & \beta \\ \beta & \alpha & \gamma & \cdots & 0 \\ 0 & \beta & \alpha & \cdots & 0 \\ \vdots & \vdots & \vdots & \ddots & \vdots \\ \gamma & 0 & 0 & \cdots & \alpha \end{bmatrix}.$$

Physically, a banded circulant matrix corresponds to the adjacency matrix of a cycle graph.

Reference [7] shows how to construct polylogarithmic-size sparse-access oracles for this class of matrices, for which we show a high-level sketch in Figure 1. In particular, the authors show how to construct  $O_A$  in  $O(1)$  time and  $O_c$  in  $O(\text{poly log}(N))$  time. Combining this with Theorems 2.2 and 2.3, we can see that using the aforementioned construction, we can perform matrix inversion by QSVT in time  $O(\kappa \text{poly log}(N) \log(\kappa/\epsilon))$ , retaining the exponential quantum speedup with respect to the matrix size. This furthermore outperforms the HHL algorithm with respect to both precision and the condition number, which has a complexity of  $O(\kappa^2 \log(N)/\epsilon)$  [1].

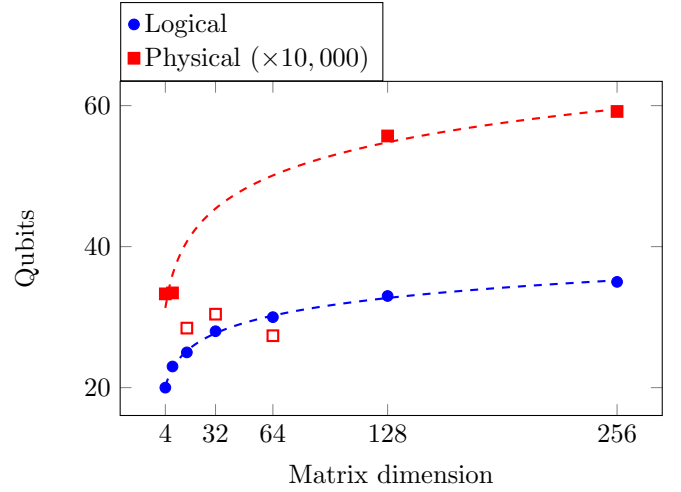


Figure 3: Resource estimates for the logical and physical qubit requirements for matrix inversion by QSVT.

## 3 Results

The resource estimates were generated by constructing QSVT circuits in Qiskit and subsequently loading them into the Microsoft Azure Quantum Resource Estimator. We are primarily concerned with observing how the resource estimates scale with the input size  $N$ , and thus arbitrarily assume condition number  $\kappa = 3$  and target accuracy  $\epsilon = 0.1$  for all circuits. The circuits and the scripts used to generate them can be found in the directory QASM\_CIRCUITS.

### 3.1 Qubit counts

Figure 3 shows how the logical and physical resource estimates scale with the input size. Whilst the logical qubit estimates conform very naturally with the expected logarithmic growth, the physical estimates scale much more erratically. We attribute this to the ‘lucky’ cancellation of non-Clifford gates when transpiling the  $L$  and  $R$  gates (see Figure 2), since the anomalously low results are caused by smaller  $T$  factory count estimates. In the interest of extrapolating to worst-case resource estimates, we mark as outliers the instances which outperform an instance of smaller size (indicated with hollow markers in Figure 3). Promisingly, despite the somewhat irregular results, the physical resource estimates nonetheless likewise conform to a logarithmic growth with  $N$ .

The logarithmic best-fit functions, computed using NumPy’s polyfit, are roughly:

$$\begin{aligned} y_{\text{logical}} &\approx 3.61 \log N + 15.2, \\ y_{\text{physical}} &\approx 67800 \log N + 219000, \end{aligned}$$

where  $\log$  denotes the natural logarithm.

Although highly impractical for small problem sizes due to the large constant factors, the data would seem to empirically imply that a realistically achievable number of physical qubits will be able to tackle problems of classically intractable sizes. For example, if the extrapolation of the physical qubit requirements remains accurate for large inputs, a  $10^{10}$ -dimensional banded circulant matrix could be inverted using under 2 million physical qubits.

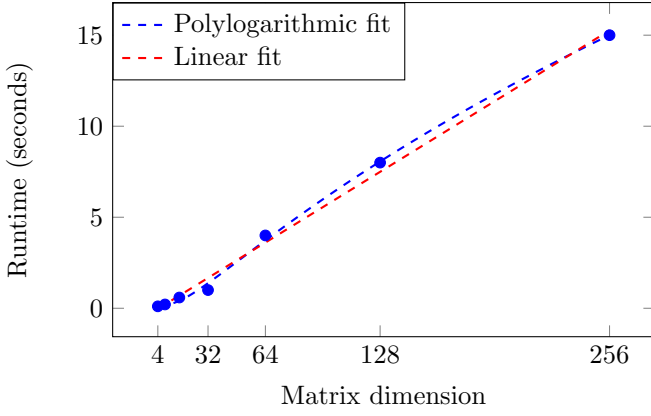


Figure 4: Resource estimates for the running time of matrix inversion by QSVT.

### 3.2 Runtimes

Figure 4 shows how the runtime estimates scale with the input size. In this case, the asymptotic growth of the runtime at first glance appears linear as opposed to the desired polylogarithmic growth. Although more data points could theoretically be used to verify the behavior, an additional challenge faced here is that the resource estimator reaches its maximum timeout of 20 minutes for matrices of size greater than 256.

To circumvent this issue, we first note that the only circuit elements that change size between instances are the  $L$  and  $R$  gates (see Figure 1) which ultimately consist of several multi-controlled NOT gates (see Figure 2). In the exploratory notebook QISKIT\_TRANSPIRATION.IPYNB, we have empirically found the runtime of Qiskit’s transpilation of multi-controlled gates to scale as  $O(n^2)$ , implying a ‘naive’ implementation such as the construction found in [9]. With the number of such gates present in the block-encoding circuit, we should then expect to see a runtime scaling of  $O(n^3) = O(\log^3 N)$ . Fitting the data to a degree-3 polylogarithmic function using SciPy’s `curve_fit` yields

$$y_{\text{runtime}} = 0.308 \log^3 N - 1.77 \log^2 N + 3.42 \log N - 2.03$$

which indeed conforms to the dataset with a smaller average error than the linear fit. To further justify fitting to this function, in the notebook BLOCK\_ENCODING\_RUNTIME.IPYNB we have evaluated the runtime of the block-encoding circuit only and observed clear polylogarithmic scaling, which implies that the same scaling holds for the full QSVT circuit.

If the extrapolation to higher dimensional matrices remains accurate, with this implementation a  $10^{10}$ -dimensional banded circulant matrix could be inverted on a quantum computer in about one hour.

## 4 Conclusion and discussion

Whilst of significant practical relevance, quantum algorithms for solving SLEs are still predominantly presented in a complexity-theoretic and oracular, and rarely a practical, setting. In this project, we have built explicit quantum circuits for solving SLEs by QSVT for a specific class of sparse matrices, and evaluated the end-to-end physical resource requirements using Microsoft’s Azure Quantum Resource Estimator.

Our findings show that both the logical and physical qubit count estimates adhere to the expected logarithmic growth, and that a realistically achievable number of physical qubits may suffice to demonstrate quantum advantage for matrix inversion.

The resource estimates for runtime seem to display a lesser degree of certainty in their asymptotic growth with matrix size, but we can deduce both theoretically and empirically that we should expect to see a growth which scales as  $O(\log^3 N)$ . Fitting the data as such, if the behavior remains consistent when extrapolated to large matrix sizes, we should likewise expect a reasonable amount of time to suffice to invert matrices of classically challenging sizes.

We furthermore highlight room for optimization in these estimates. When scaling to large matrices, the bottleneck is the many multi-controlled NOT gates present in  $O_c$  (see Figure 2). The software tools we have used appear to transpile such gates into circuits of depth  $O(n^2)$ , resulting in an overall complexity for  $O_c$  of  $O(\log^3 N)$ . The best way to transpile multi-controlled gates is an area of active research, with recent theoretical results achieving depths as low as  $O(\log^3 n)$  [10]. With the  $n \sim \log N$  gates present in  $L$  and  $R$ , this would imply an overall runtime which scales with input size as  $O(n \log^3 n) = O(\log N \log^3 \log N)$ . If and by how much these results reduce resource requirements in practice remains an open question and a potential direction for future work.

## References

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