ChipTune

- Author: Wallie Everest
- Description: Vintage 8-bit sound generator
- Language: Verilog

How it works

Chiptune implements an 8-bit Programmable Sound Generator. Input is from a serial UART interface. Output is PWM audio.

How to test

Explain how to test your project

IO

#	Input	Output
0	io_in[0] (CLOCK)	io_out[0] (PWM)
1	io_in[1] (IN_1)	io_out[1] (OUT_1)
2	io_in[2] (IN_2)	io_out[2] (OUT_2)
3	io_in[3] (MODE)	io_out[3] (OUT_3)
4	io_in[4] (BAUD_CLK)	io_out[4] (REF_CLK)
5	io_in[5] (TCK)	io_out[5] (RTCK)
6	io_in[6] (TMS)	io_out[6] (LED)
7	io_in[7] (TDI)	io_out[7] (TDO)