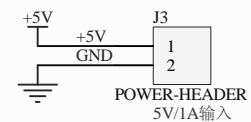
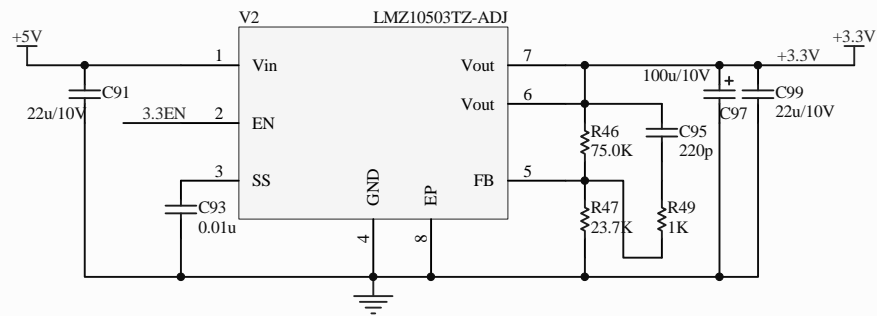
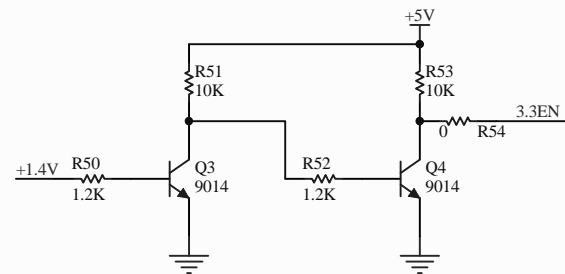
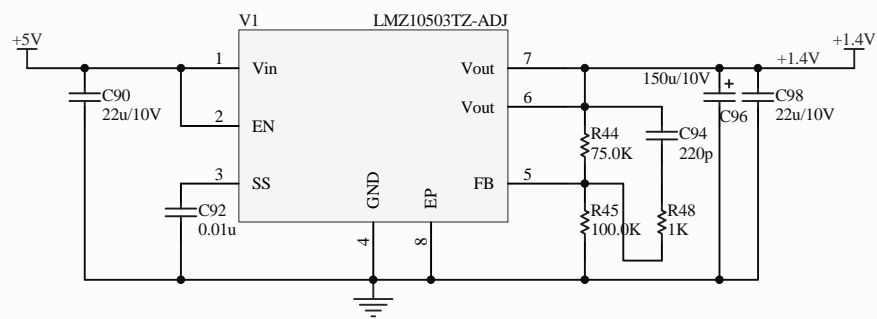


1. BEA20(缺管IPU=1): CPU mode=1, Little Endian;
2. BEA9(缺管IPU=1): BEA18(缺管PD=0); Boot mode=10, 8bit Flash;
3. BEA17-16(缺管PD=0): EMIFA clk=10; CPU6;
4. BEA15-14(缺管PD=0): EMIFB clk=10; CPU6;
5. BEA11(缺管PD=0): McISP1 enable(UTPA disable);
6. BEA9(缺管PD=0): for 6416 must be pull-up;

| Title | | | Revision | | |
|-------|----------------------------|--|----------|--|--|
| Size | Number | | | | |
| A2 | | | | | |
| Date: | 2016-8-8 | | Sheet of | | |
| File: | E:\HK-1557\0485_DSP_SchDoc | | Draw By: | | |



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|------------|------------------------------|-----------|
| Title | | |
| Size A4 | Number | Revision |
| Date: | 2016-9-8 | Sheet of |
| File: | E:\HK-1553To485\Power.SchDoc | Drawn By: |

