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MEM2-ES. ILIAS Directory and File Structure	Rev. 1.1	10.01.2017 Page 1 of 2

Folder	File	Contents
	_MEM2-ES_ILIAS.pdf	This file
	_MEM2-ES_Info.pdf	General information on this course
	_MEM2-ES_Project.pdf	Description of the project
Control Files		
	...	2 folders with Matlab and C files for both cascaded and simple closed loop motor control
Info		
	Strommessung_MP-Board.pdf	Probleme der Strommessung auf dem Laboraufbau
	Mix_C_and_Cpp.htm	Info on how to mix C and C++ code in a project
	Behavior_SMOperation.pdf	Sketch showing the implementation of a state machine framework
	Implementation_Definitions.pdf	Project implementation specifications.
	StateMachineExample.pdf	Example used to test the implementation of the state machine framework
	CodingGuidelines C++.pdf	Example coding guidelines of CS group
	Documentation_Requirements.pdf	The required documentation of the project
Input		
	Lec_Pointer.pdf	Lecture on pointer (C/C++/Java)
	Lec_StateMachine.pdf	Description of the state machine files supplied
	Lecture_QualityTest.pdf	Lecture on quality and test
	Lecture_SchedulabilityAnalysis.pdf	Lecture on schedulability analysis
	Lecture_SoftwareEngineeringProcess.pdf	Lecture on the software engineering process
	Modeling_Exercise.pdf	Exercise for practicing behavior modeling
	SGL_Behavior.pdf	Preparation file for the SGL on behavior modeling
	SGL_Clock_Synchronisation.pdf	Preparation file for the SGL on clock synchronization
	SGL_Requirements.pdf	Preparation file for the SGL on requirements
LabBoard Docs		
	Drehgeber_Info.pdf	Brief info on the mode of operation of an incremental encoder
	HWFunc.pdf	Description of the functions to be used to access the hardware
	IO-Schematic.pdf	Schematic of the IO board interfacing the hardware
	Lab_ME_Checklist.pdf	Checklist on handling the lab board in room U131
	Motor_Datenblatt.pdf	Data sheet of the DC motors and the integrated incremental encoder
	Rtlibsrc.h	Header file needed for the code generated by Simulink
	VxWorksClks.pdf	Info on the clocks used by VxWorks on this lab board
	Watchdog_Spec.pdf	Info on the mode of operation of the watchdog on this lab board

Folder	File	Contents
Lab Files	...	Files generally needed to use the lab exercise board
Master Files	...	Files implementing the chain master
PTP Files	...	Files implementing a PTP slave
Software Docs	...	Miscellaneous documents on software
State Machine Files	...	Files implementing the state machine framework
VHDL Files	...	Files implementing the FPGA functionality
WindRiver Docs	...	Miscellaneous documents on WindRiver software