

<EE 210 Final Project>

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<5/5/2025>
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Introduction

This project involved designing, simulating, and building a complete tone-control and karaoke audio circuit. The circuit takes a stereo audio input and processes it through five stages: mixer/karaoke selector, tone control, volume control, LED volume display, and output buffer. Each stage builds on concepts covered earlier in the EE 210 course, like op-amps, voltage dividers, and comparators. The final result is a working audio circuit that can adjust bass and treble, mute vocals, show volume levels using LEDs, and safely use headphones.

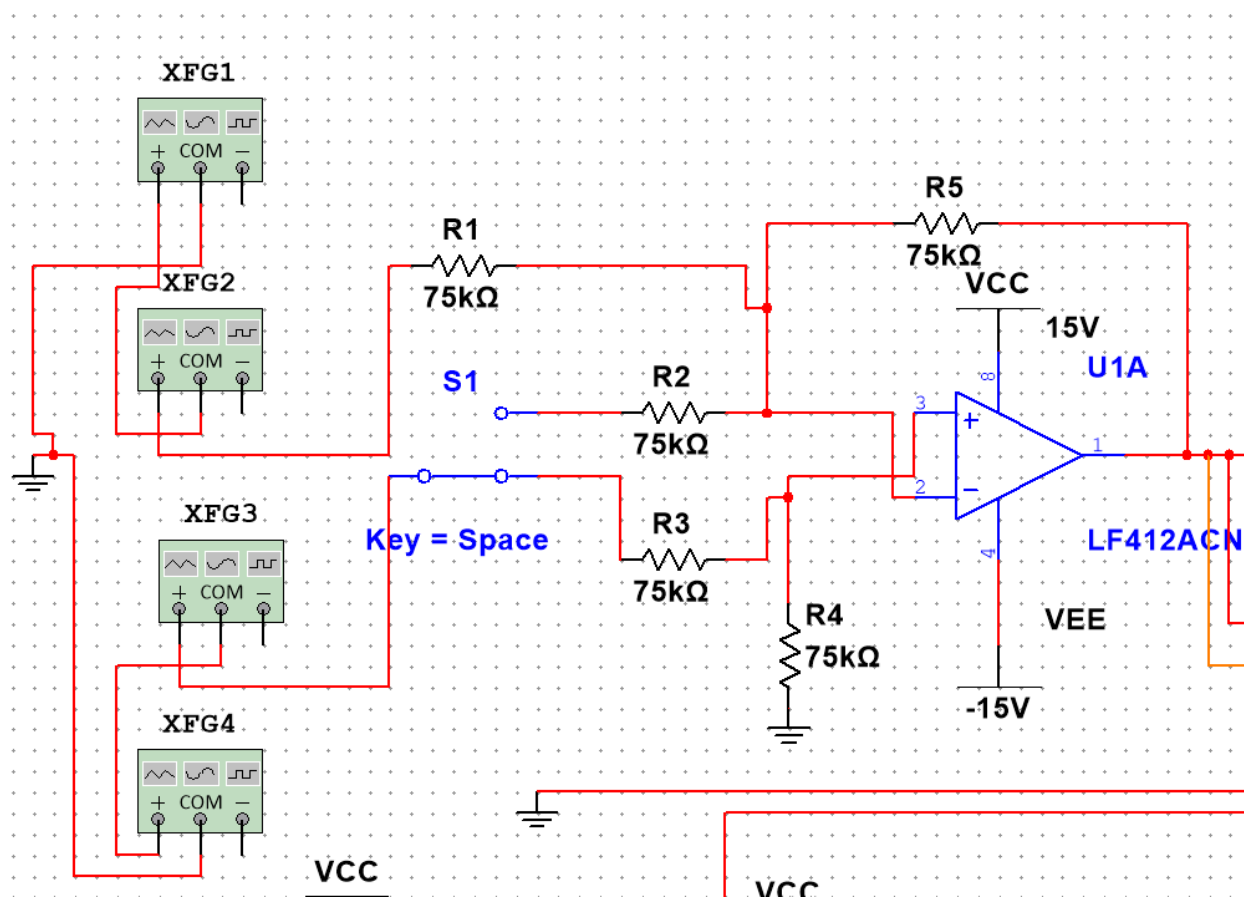
Design and Simulation

Block 1

Design Objective

Combine (mix) or subtract (karaoke) left and right audio channels using op-amp configurations.

Schematic



Block 1 schematic

Theory of Operation

This block uses a SPDT switch to select between two op-amp modes. In mixer mode, it acts as an inverting summing amplifier, outputting $-(L+R)$. In karaoke mode, it subtracts one channel from the other, outputting $(L-R)$, which suppresses vocals that are common to both channels. The circuit relies on matched resistors for balanced output.

Derivations/Calculations

Date _____
Page _____

Block 1

$$V_{out} = V_L - V_R$$

$$V_{out} = -(V_L + V_R)$$

$$\frac{R}{R + 100k\Omega} = \frac{1}{10}$$

$$10R = R + 100k$$

$$9R = 100k$$

$$R = \frac{100k}{9}$$

$$R = 11.111k\Omega$$

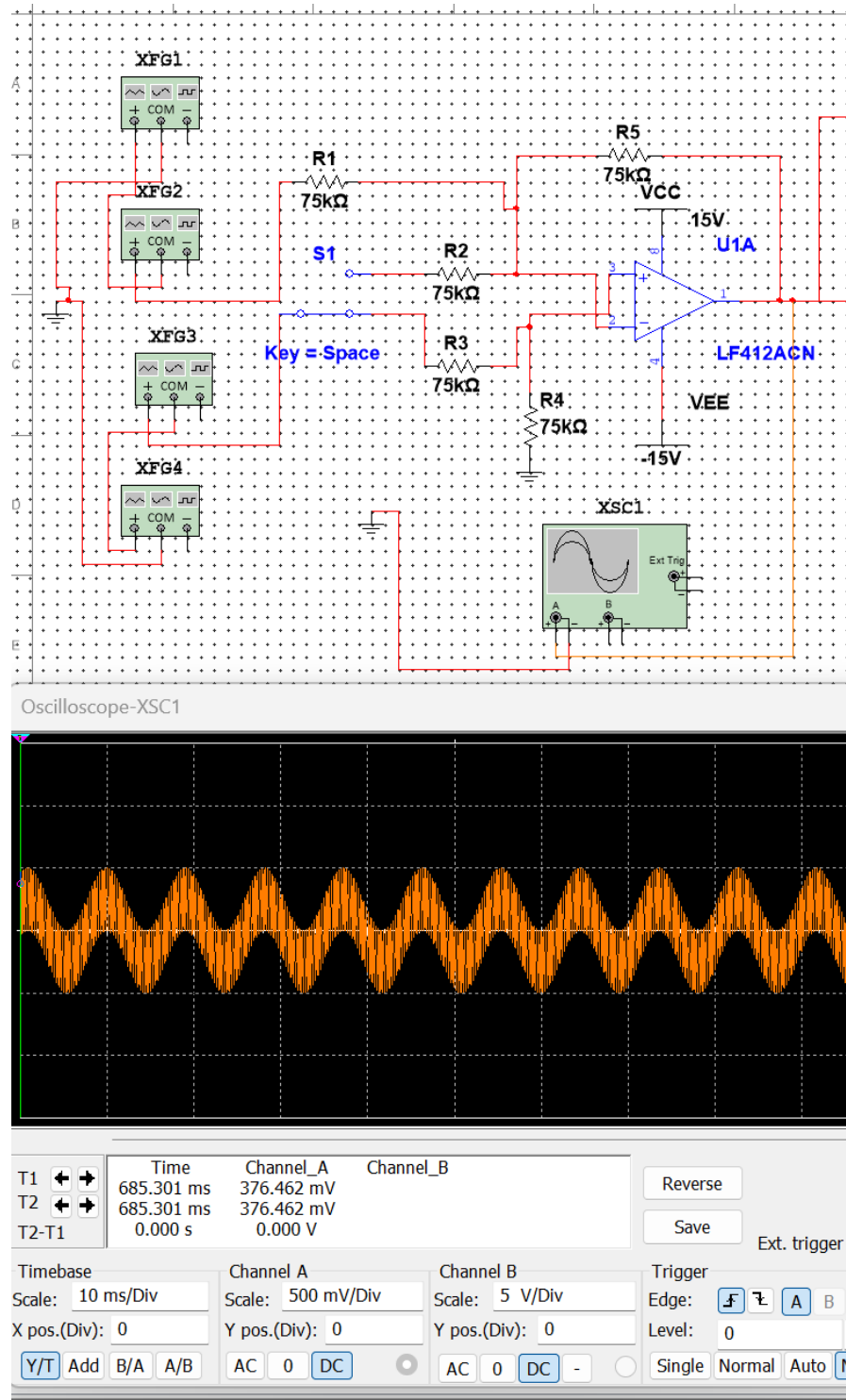
$$\frac{R + 100k}{R} = 10$$

$$1 + \frac{100k}{R} = 10$$

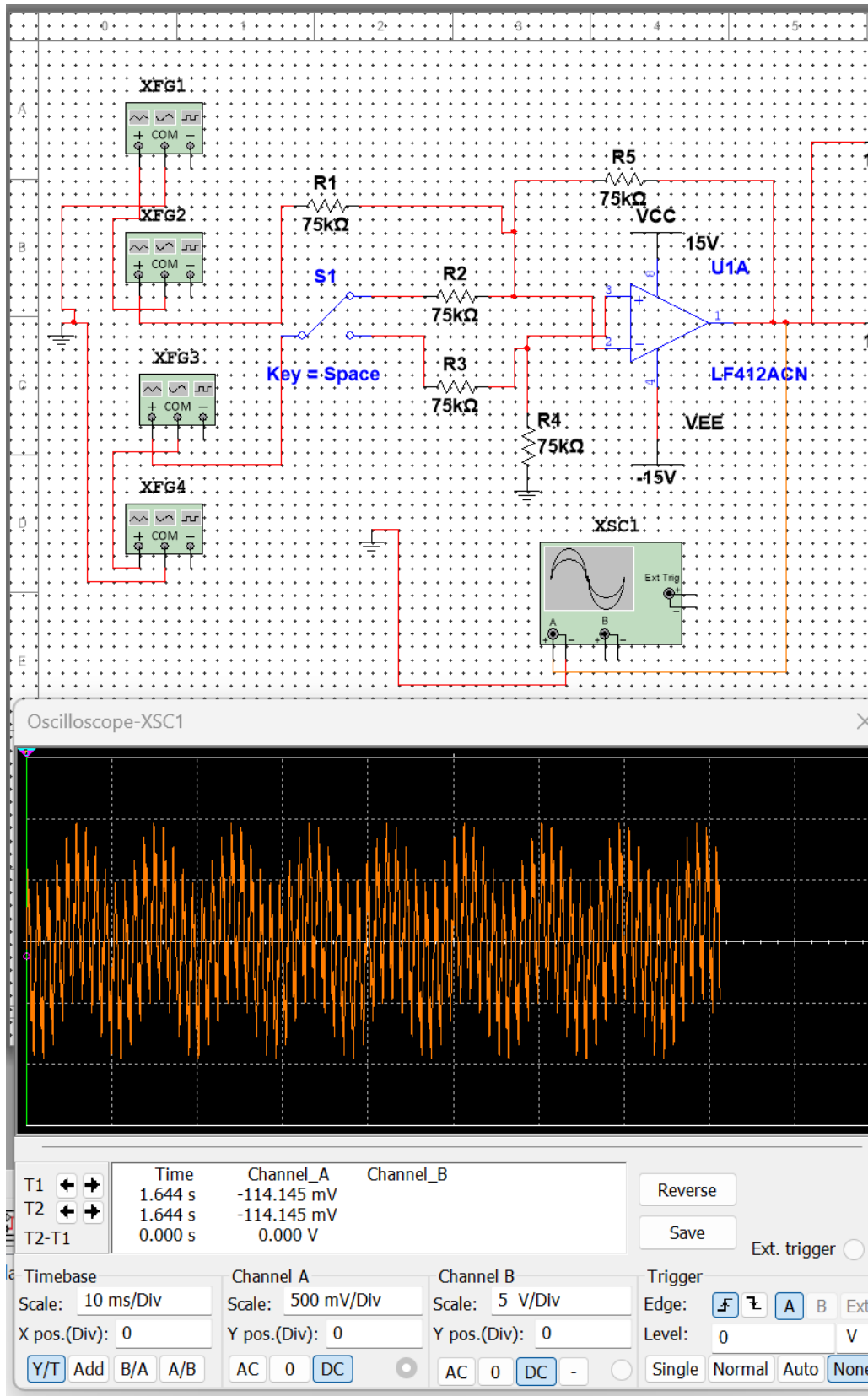
$$\frac{100k}{R} = 9$$

$$R = 11.111k\Omega$$

Simulation Results



Block 1- Mixer Mode



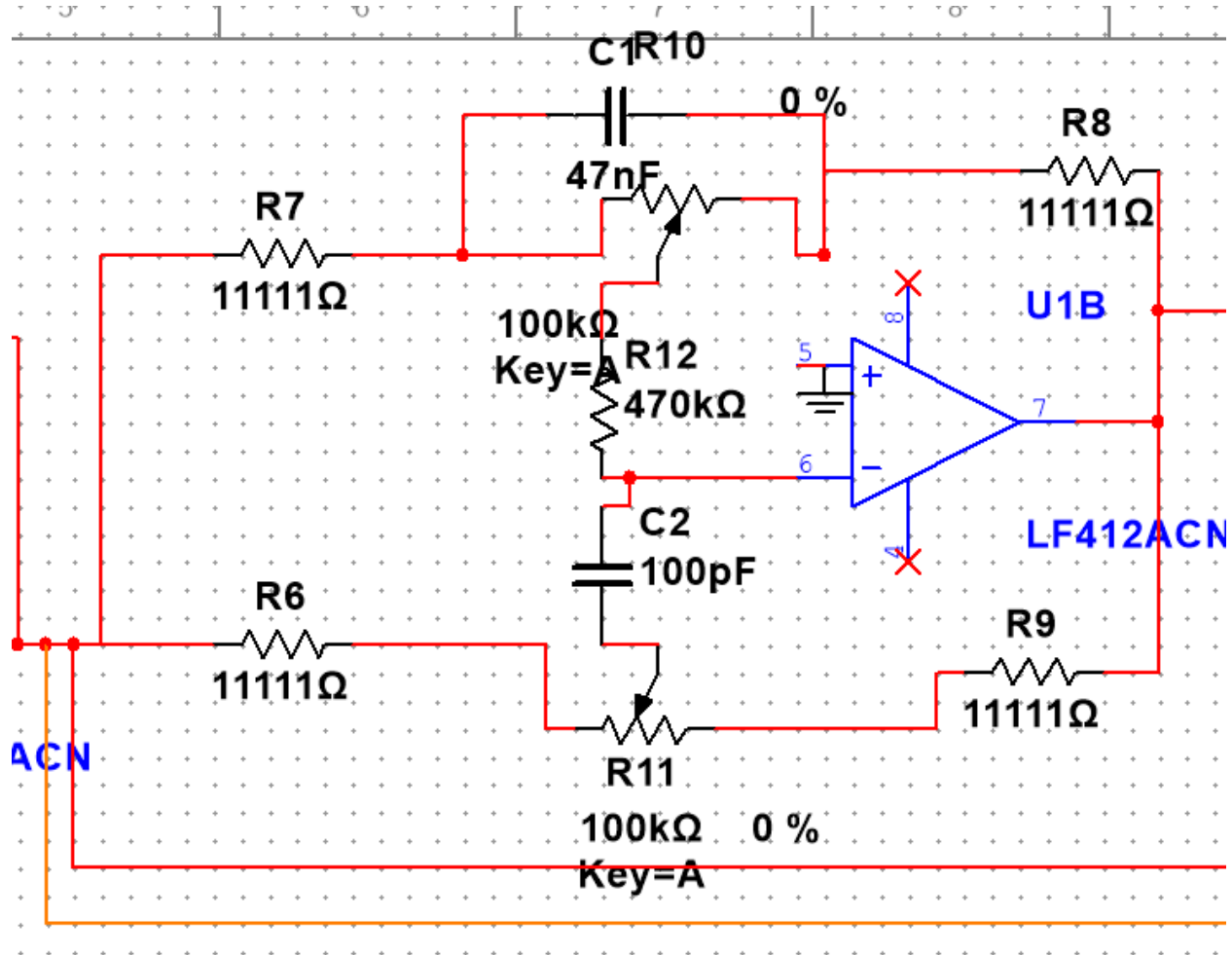
Block 1- Karaoke Mode

Block 2

Design Objective

Allow independent bass and treble control using a Baxandall tone control circuit.

Schematic

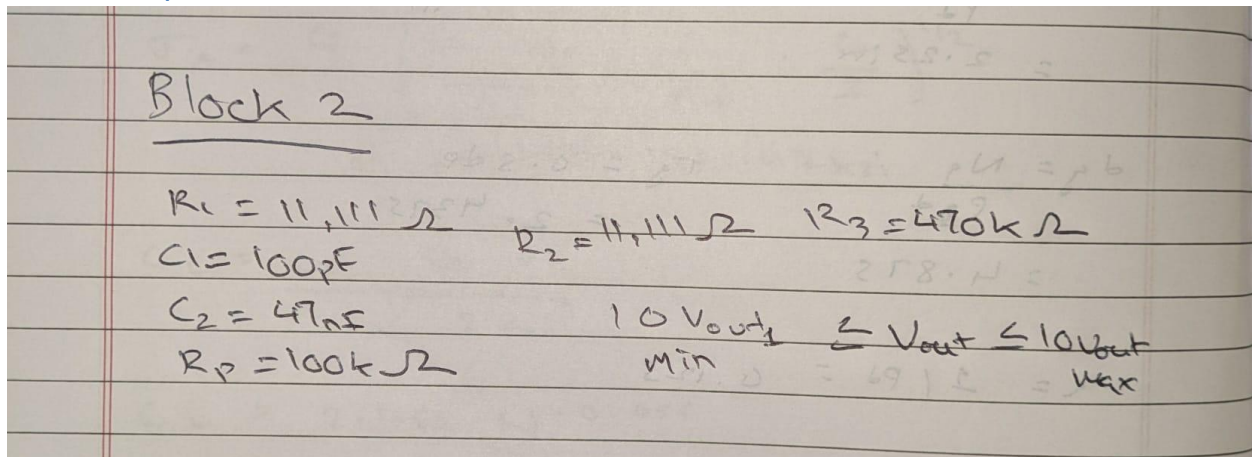


Block 2 schematic

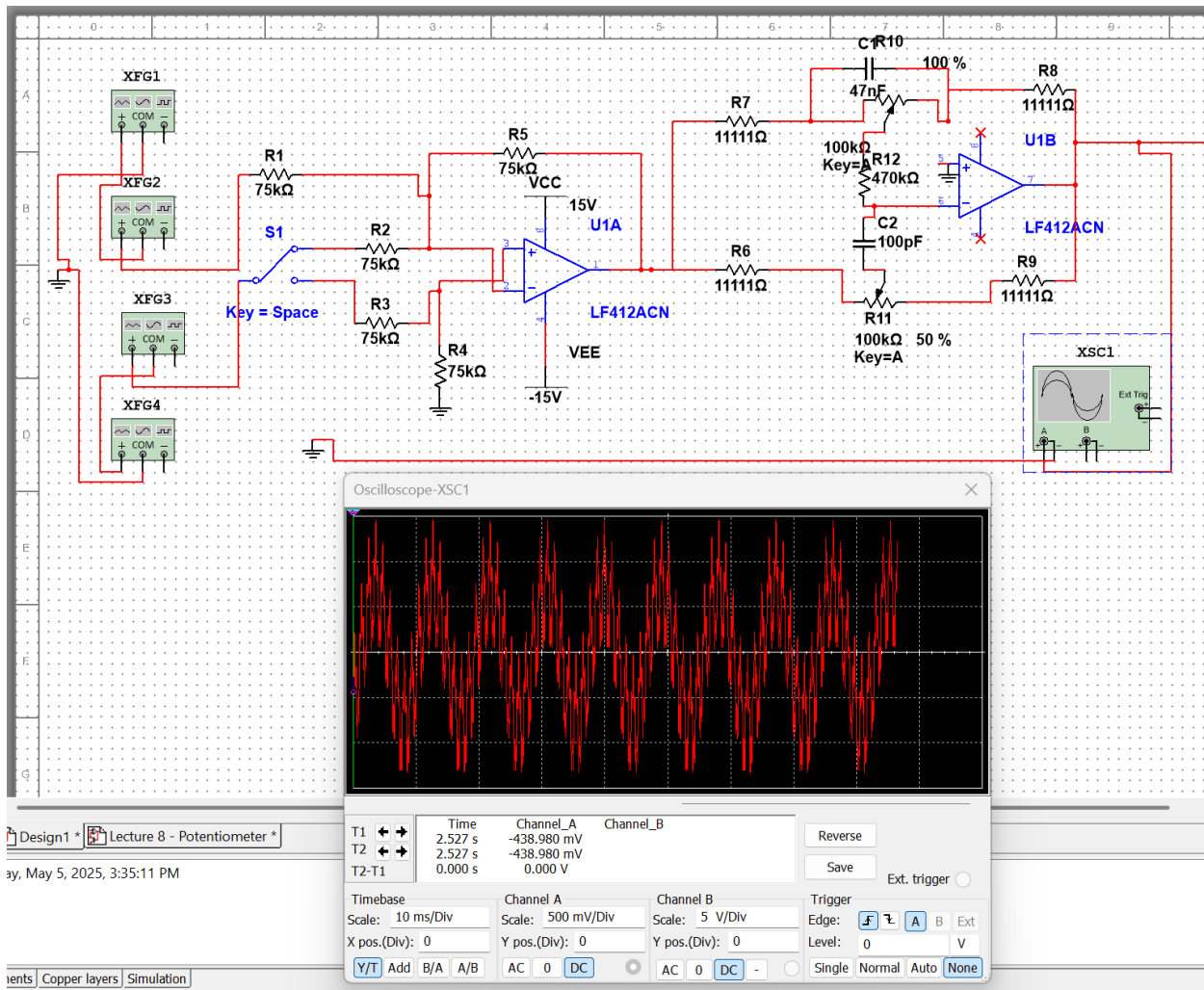
Theory of Operation

This stage adjusts frequency content using two op-amp-based feedback loops. The user can boost or cut bass and treble with potentiometers. The circuit shapes the frequency response depending on the position of these pots and the values of surrounding resistors and capacitors, achieving a gain range from roughly 0.1 to 10.

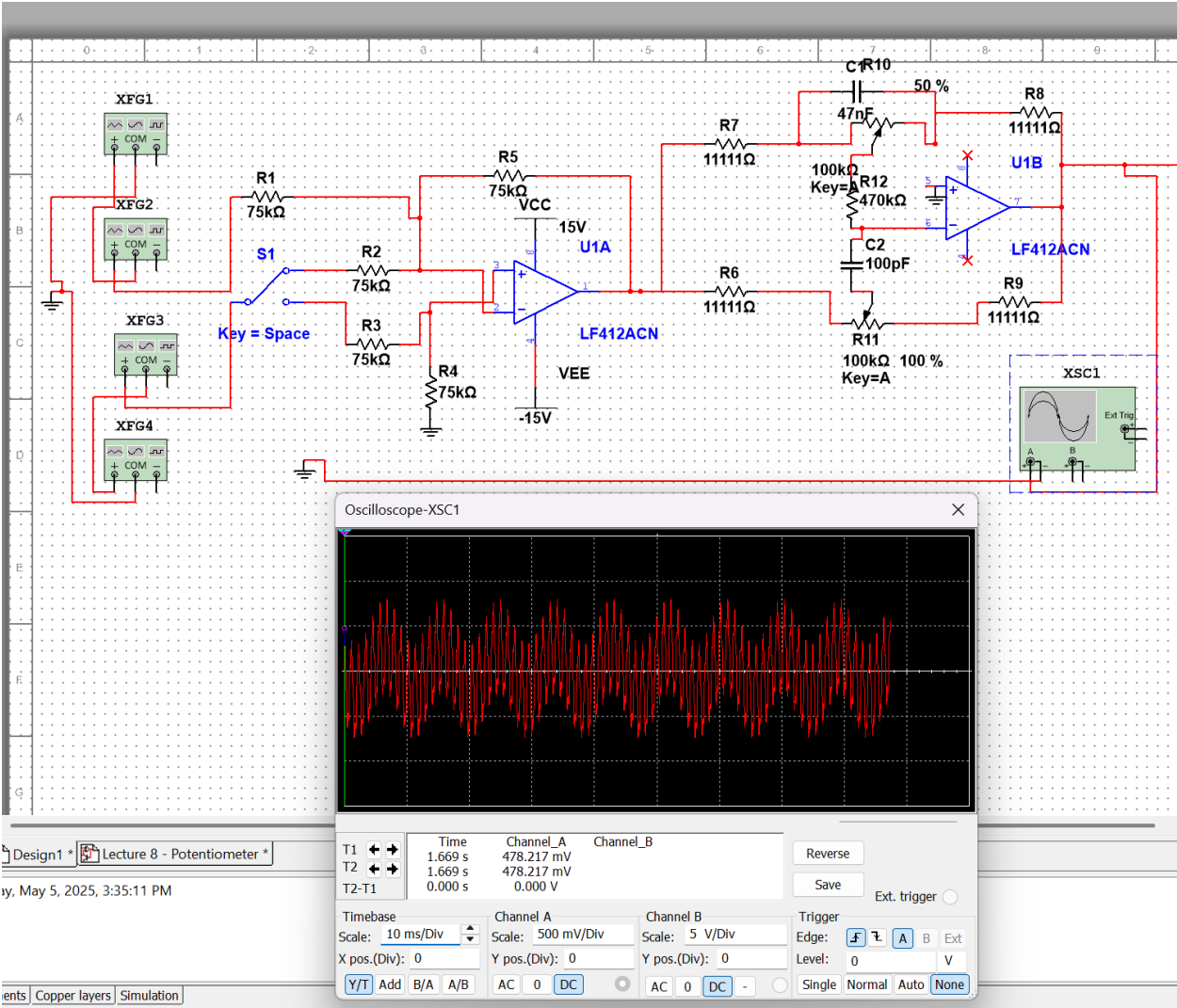
Derivations/Calculations



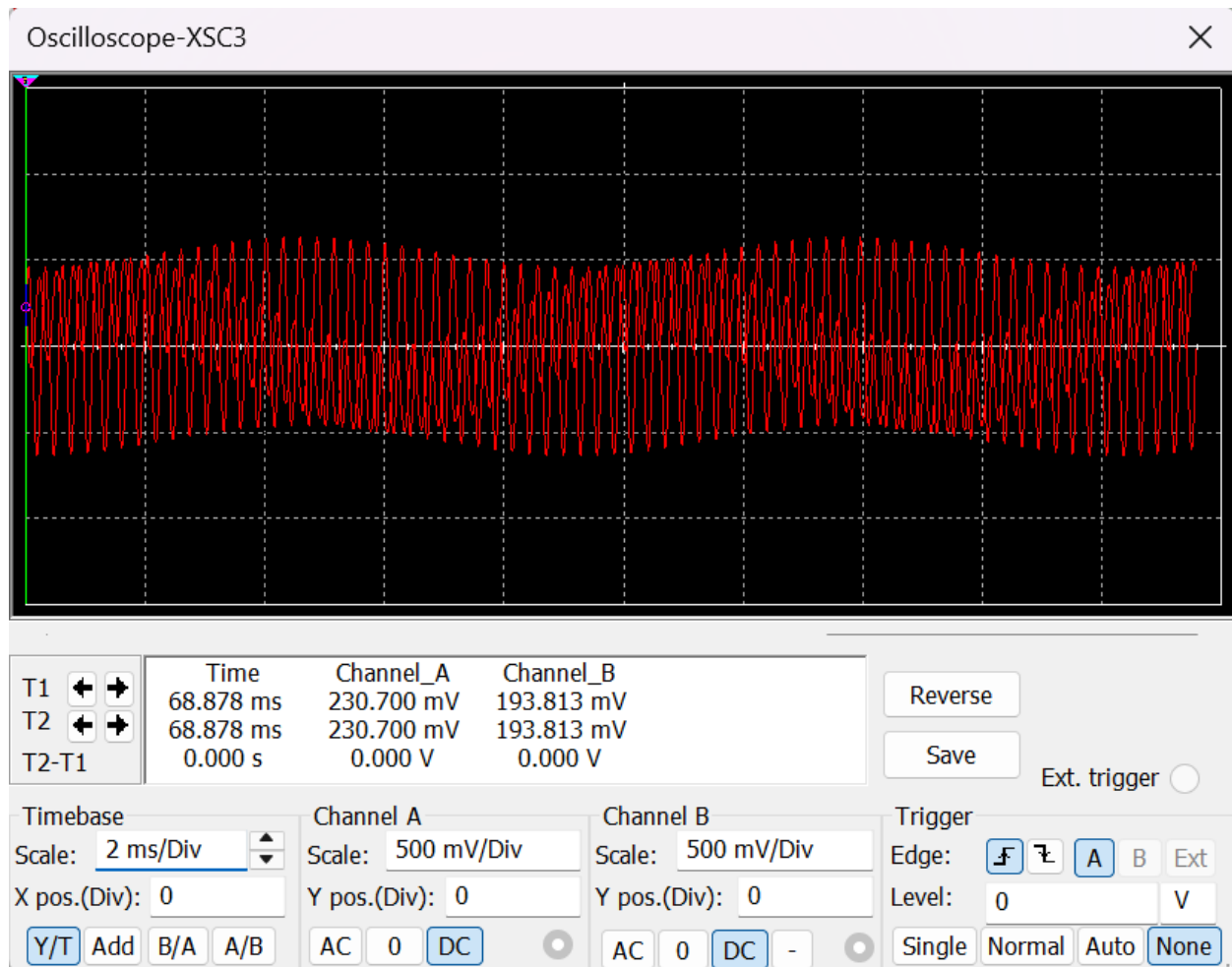
Simulation Results



Block 2- Bass Boost



Block 2- Treble boost

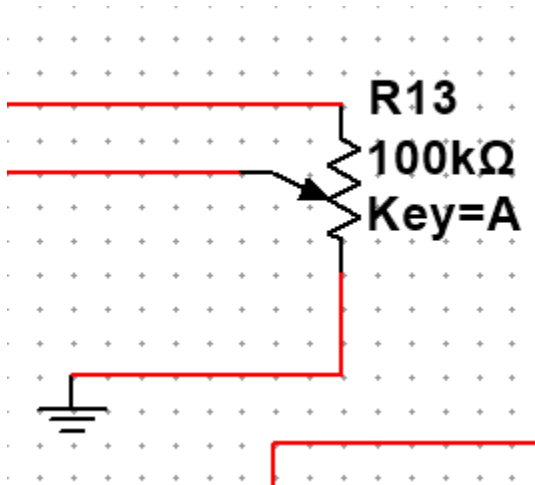


Block 2- Bass and Treble attenuation

Block 3

Design Objective

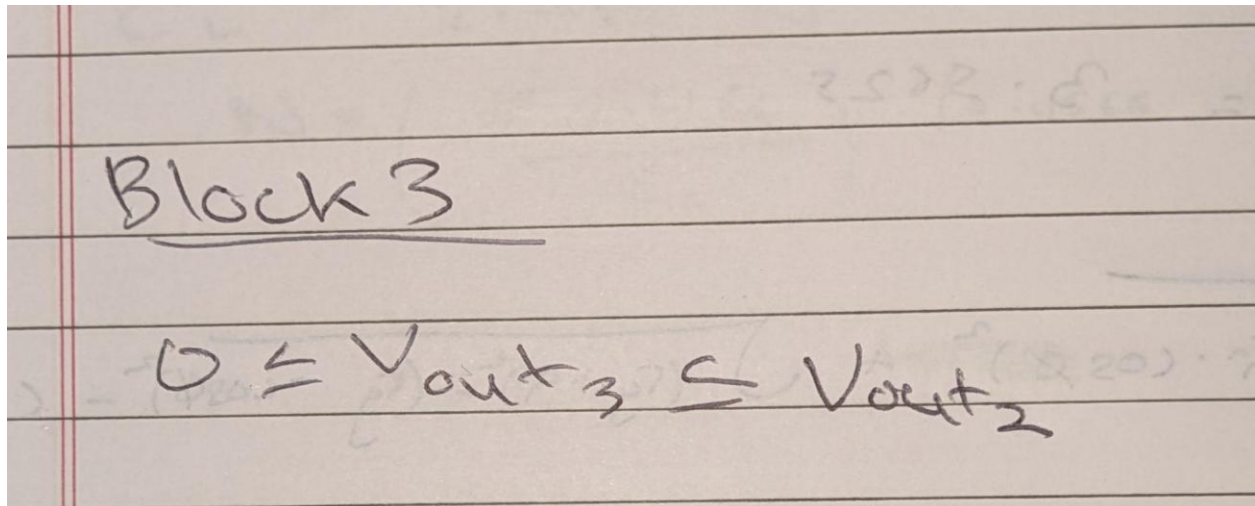
Provide overall volume adjustment of the audio signal.

Schematic

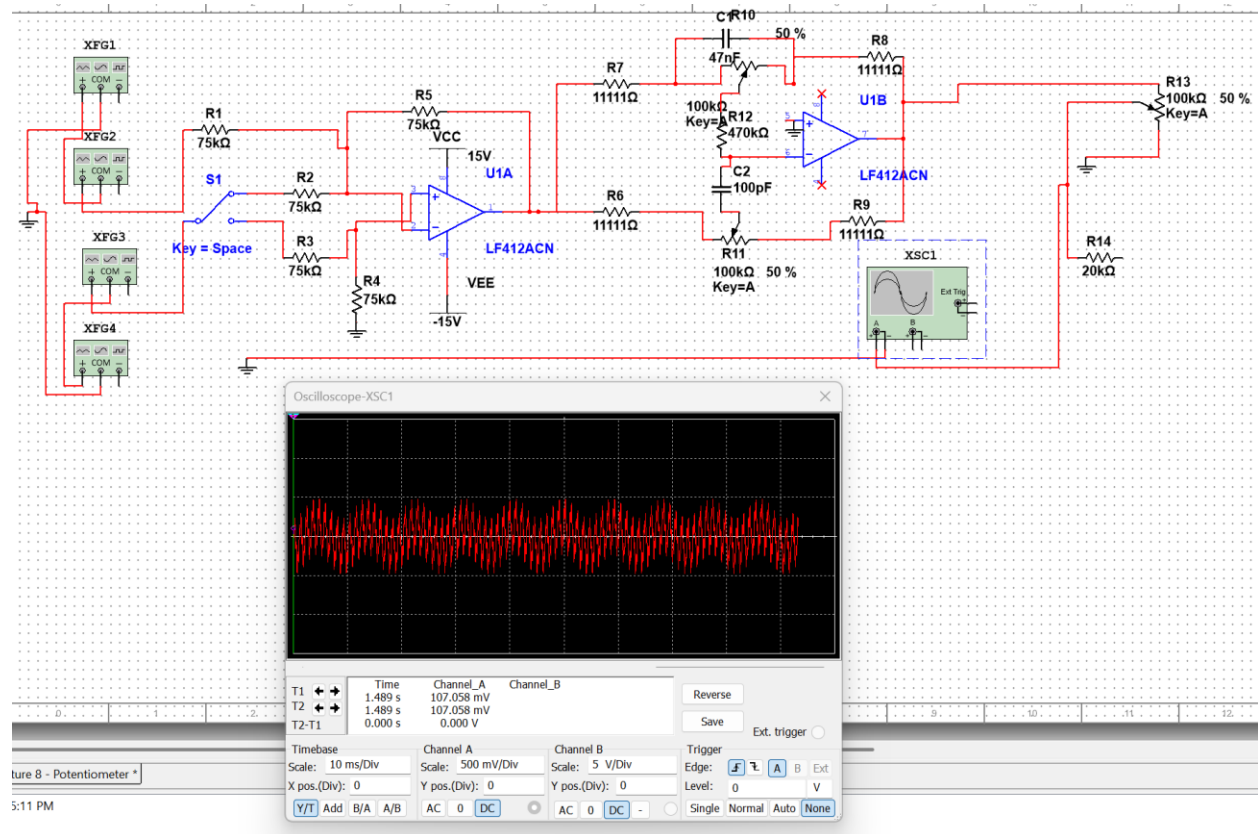
Block 3 schematic

Theory of Operation

This block is a simple voltage divider, typically implemented with a single potentiometer. By changing the wiper position, the user can scale the amplitude of the audio signal anywhere from 0 to full output, making it the simplest yet crucial part of user control.

Derivations/Calculations

Simulation Results

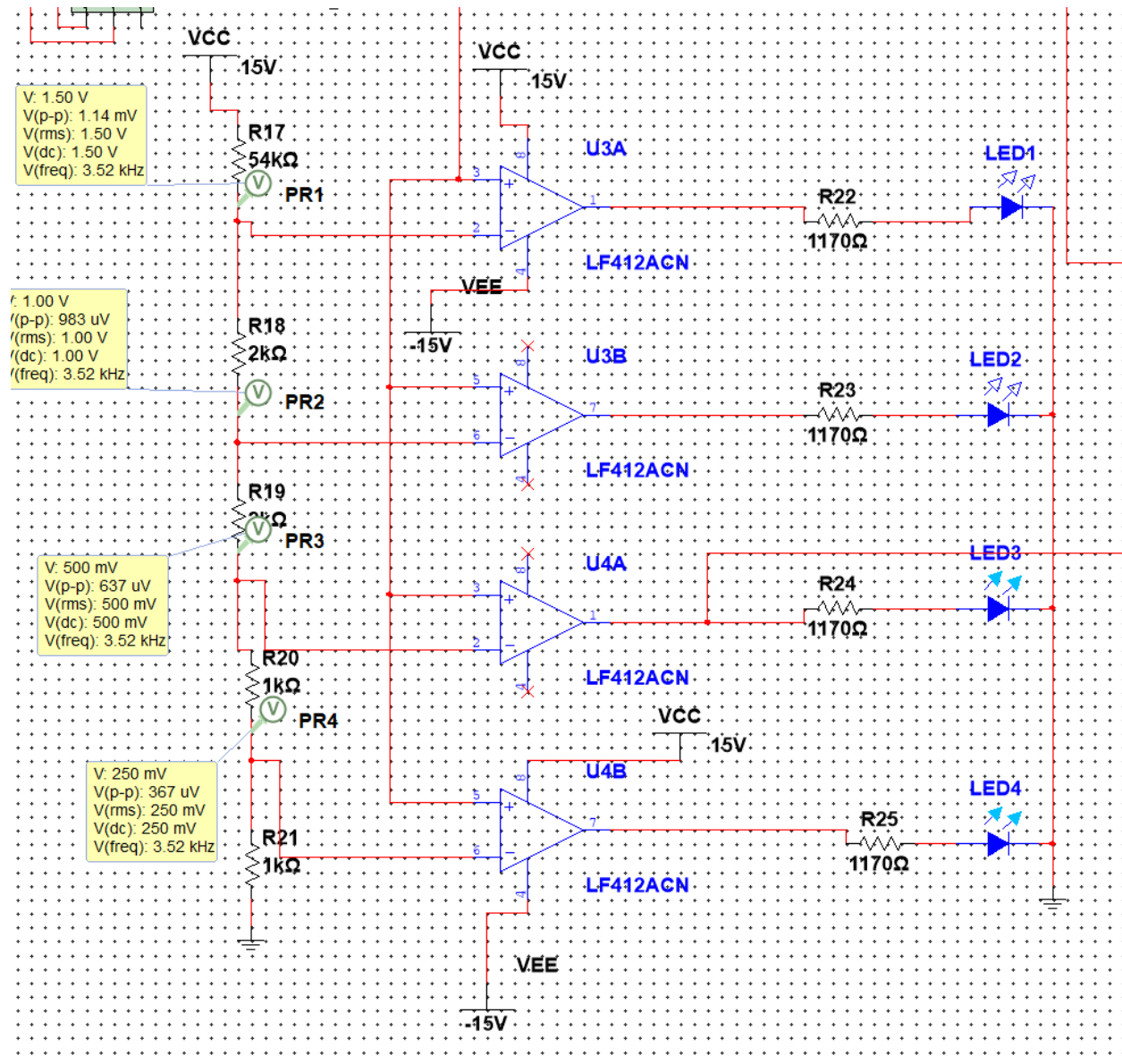


Block 4

Design Objective

Visually indicate the output volume level using a 4-LED bar.

Schematic



Block 4 schematic

Theory of Operation

This block uses comparator op-amps to compare the audio signal with fixed voltage references (0.25 V, 0.5 V, 1.0 V, 1.5 V). As the input signal exceeds each threshold, the corresponding LED lights up. This gives a rough but effective indication of audio intensity.

Derivations/Calculations

Block 4

$$15 - 1.5 = 13.5 \quad R_1 = 13.5 \text{ k}\Omega$$

$$1.5 - 1 = 0.5 \quad R_2 = 500 \Omega$$

$$1 - 0.5 = 0.5 \quad R_3 = 500 \Omega$$

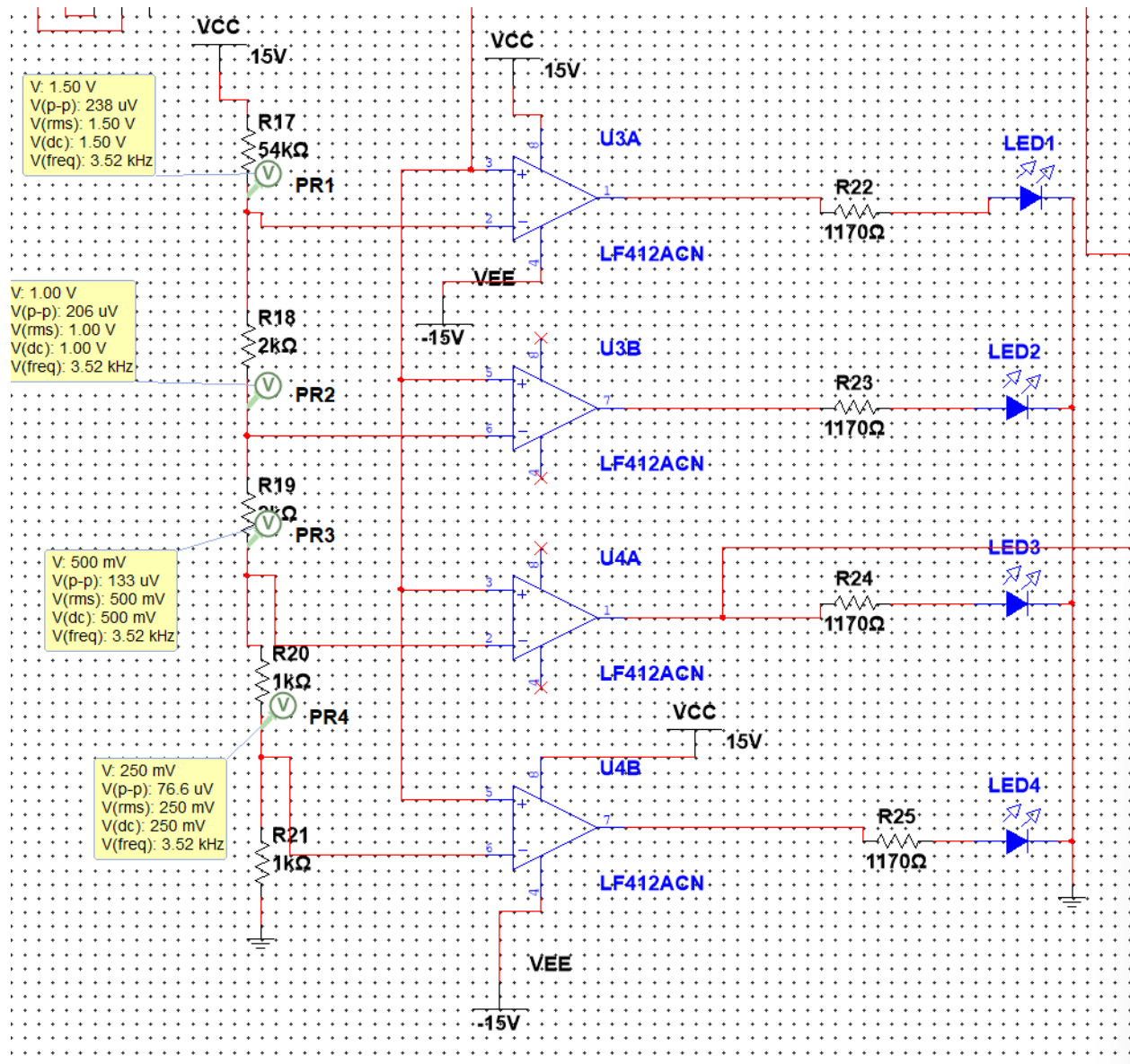
$$0.5 - 0.25 = 0.25 \quad R_4 = 250 \Omega$$

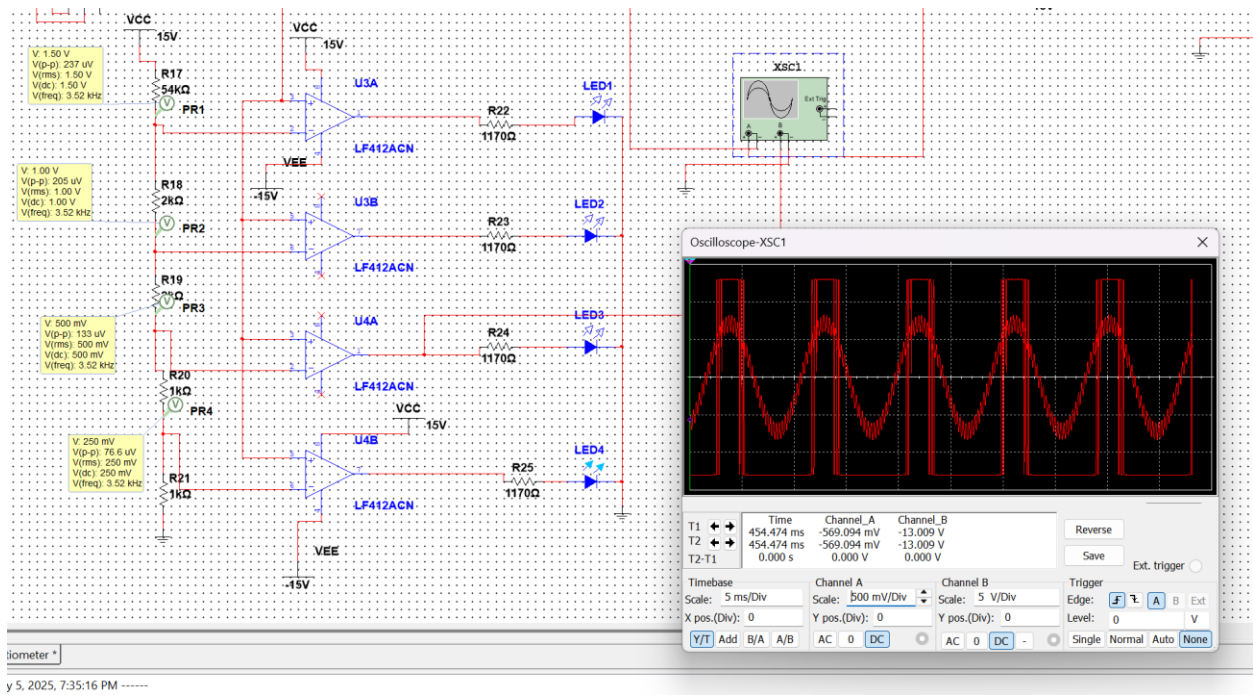
$$0.25 - 0 = 0.25 \quad R_5 = 250 \Omega$$

$$R_{in} = 1170 \Omega = 15 - 0.3.3$$

$$0.07$$

Simulation Results



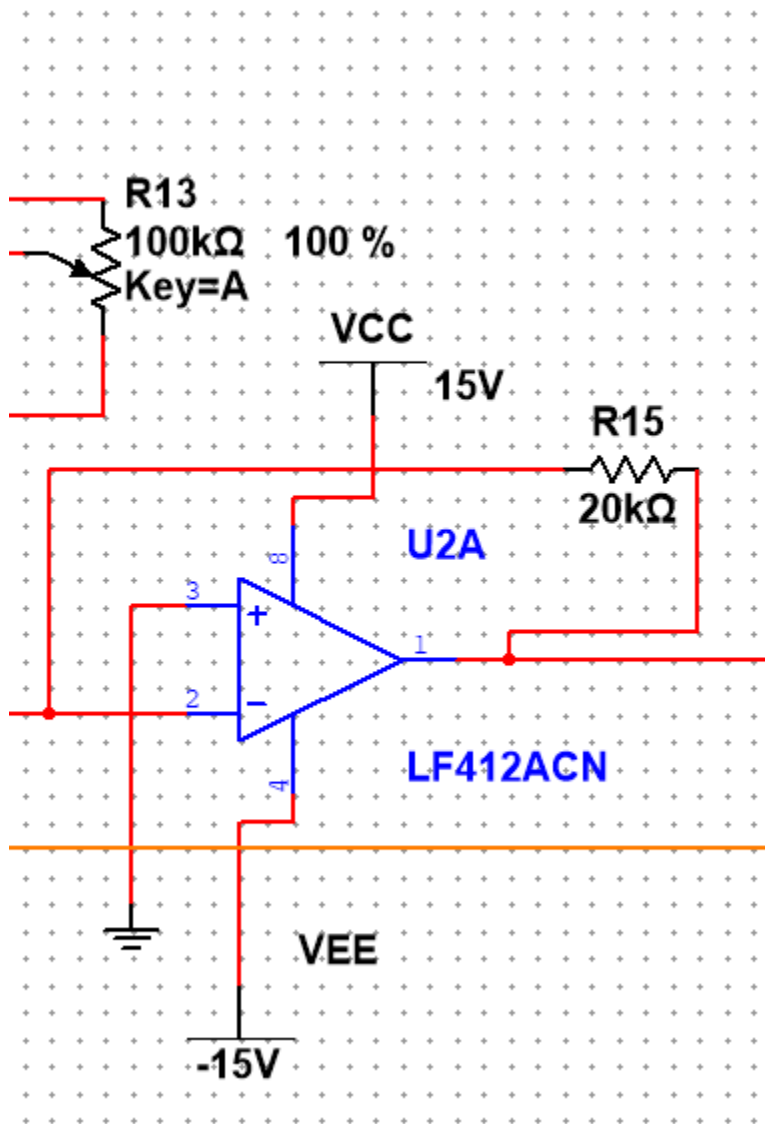


Block 5

Design Objective

Reduce and buffer the signal to headphone-safe levels (0.5–1 V).

Schematic



Block 5 schematic

Theory of Operation

This final stage uses an inverting amplifier with gain <1 . It attenuates the potentially high output of the earlier stages to prevent distortion or damage to headphones. High-value resistors help avoid loading the previous stage. The op-amp also acts as a buffer to preserve signal integrity at the output jack.

Derivations/Calculations

Block 5

$$V_{out} = -2V \quad V_{in} = 1V \quad V_{R_{max}} = 1V$$

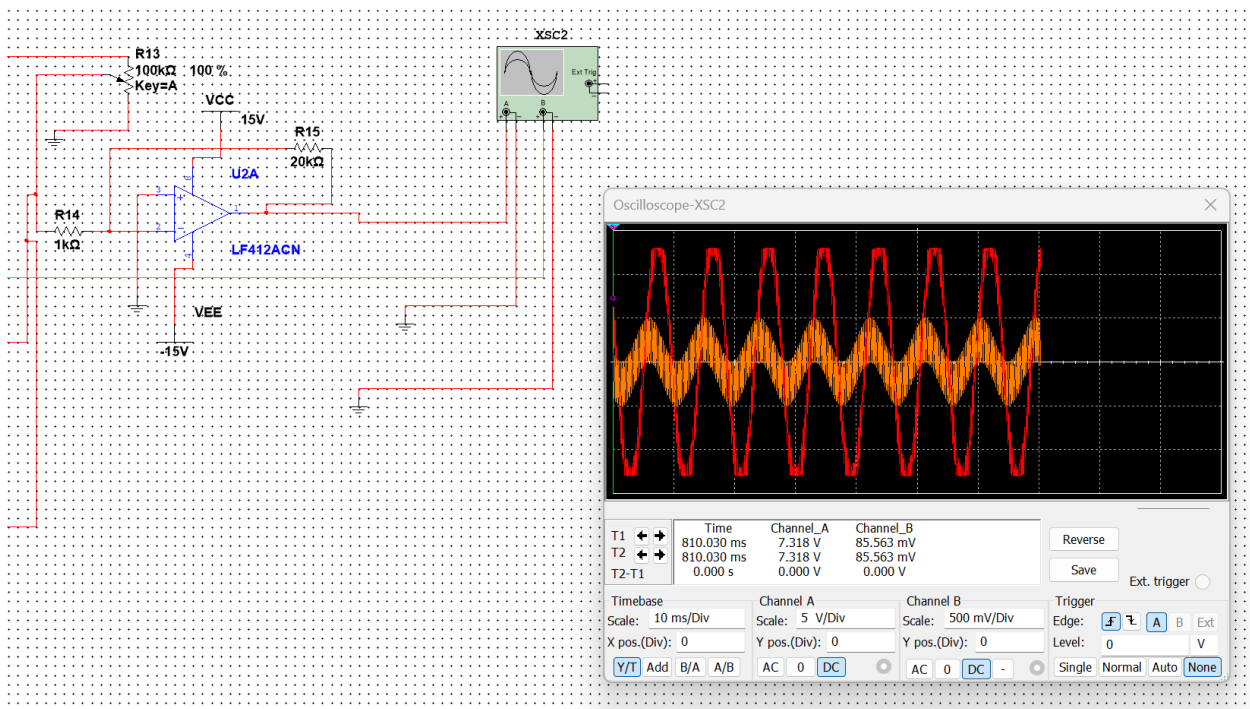
$$V_{out_{max}} = -2(10)$$

$$= -20$$

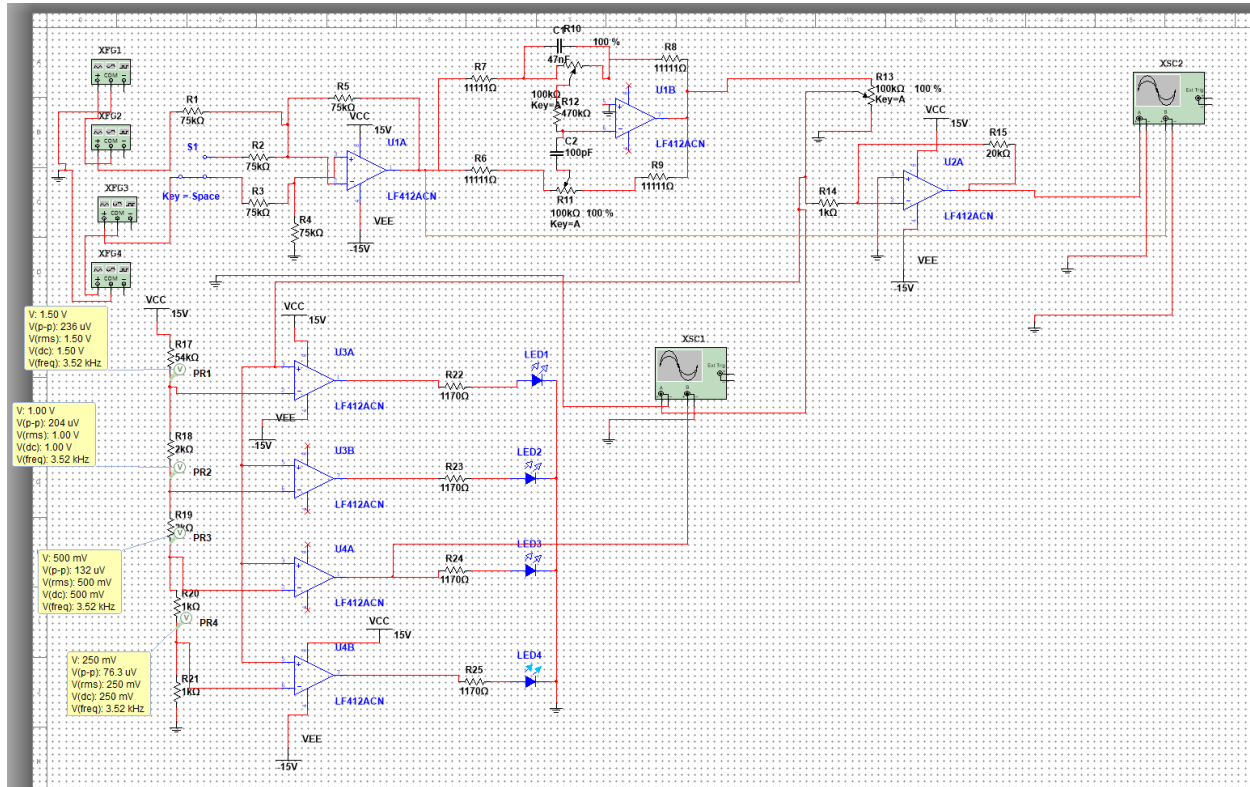
$$V_{out_{max}} = 1 = -20x = x = -1/20$$

$$V_{out} = \frac{1}{20} (V_{out3})$$

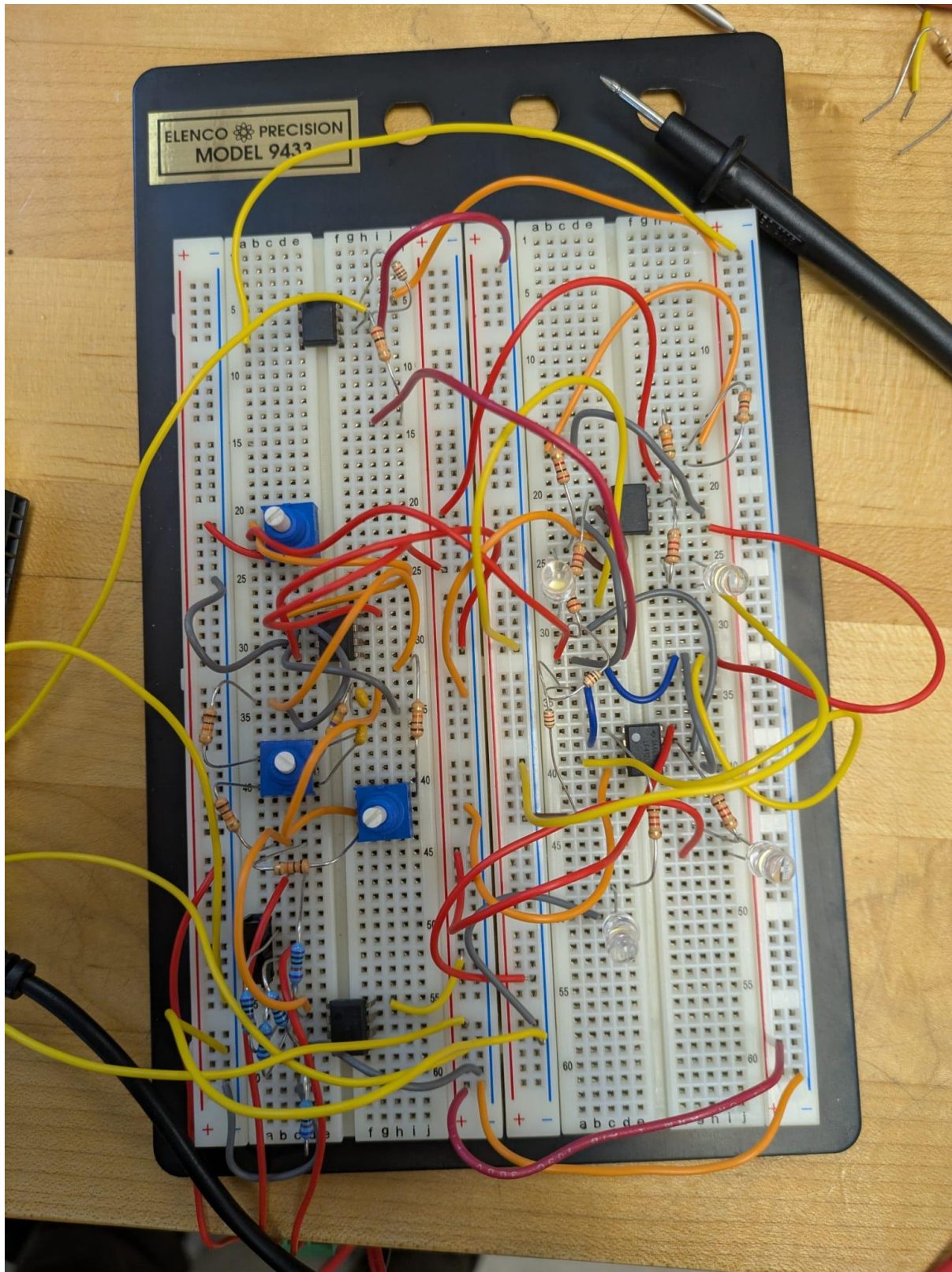
Simulation Results



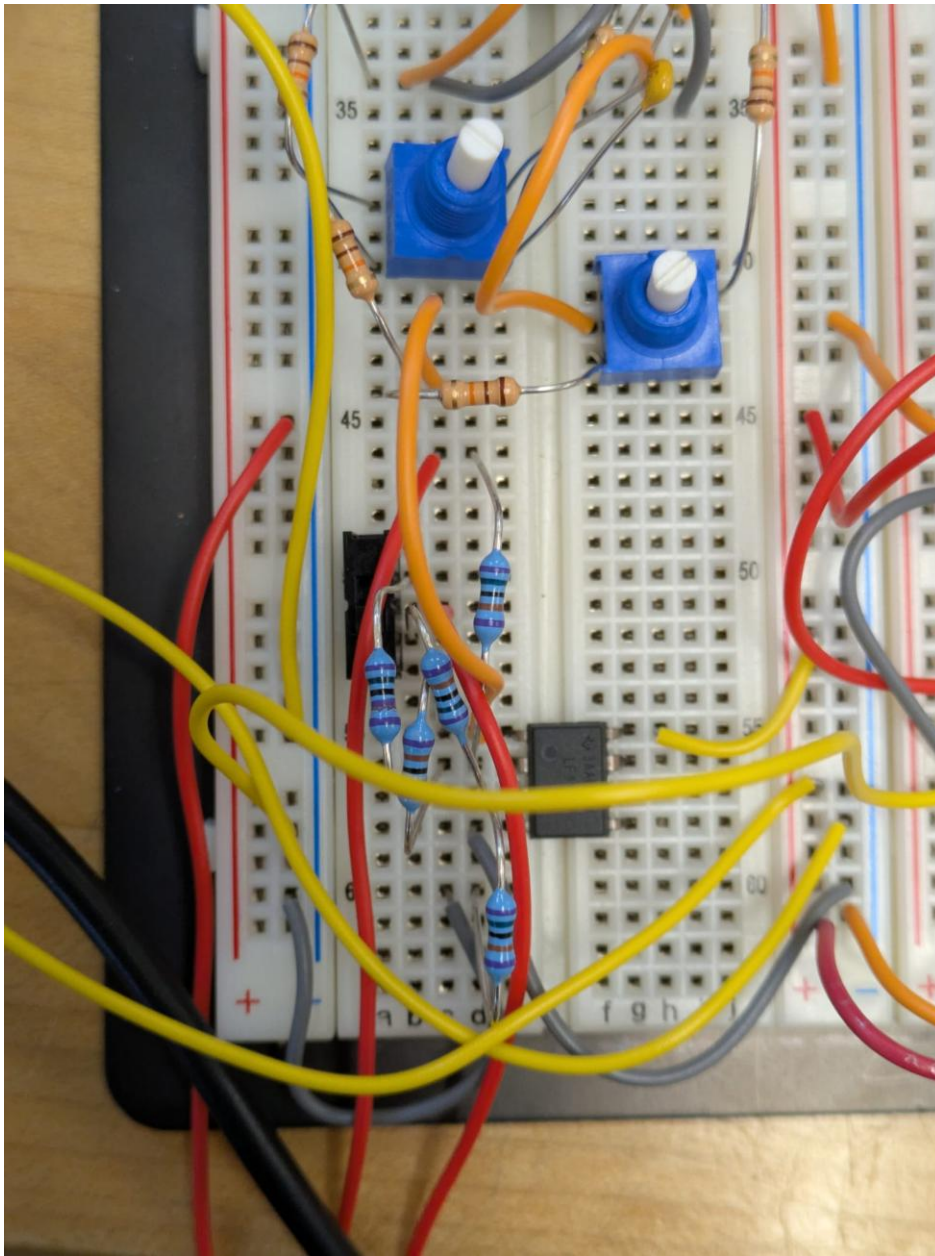
Complete Assembly and Breadboard Images



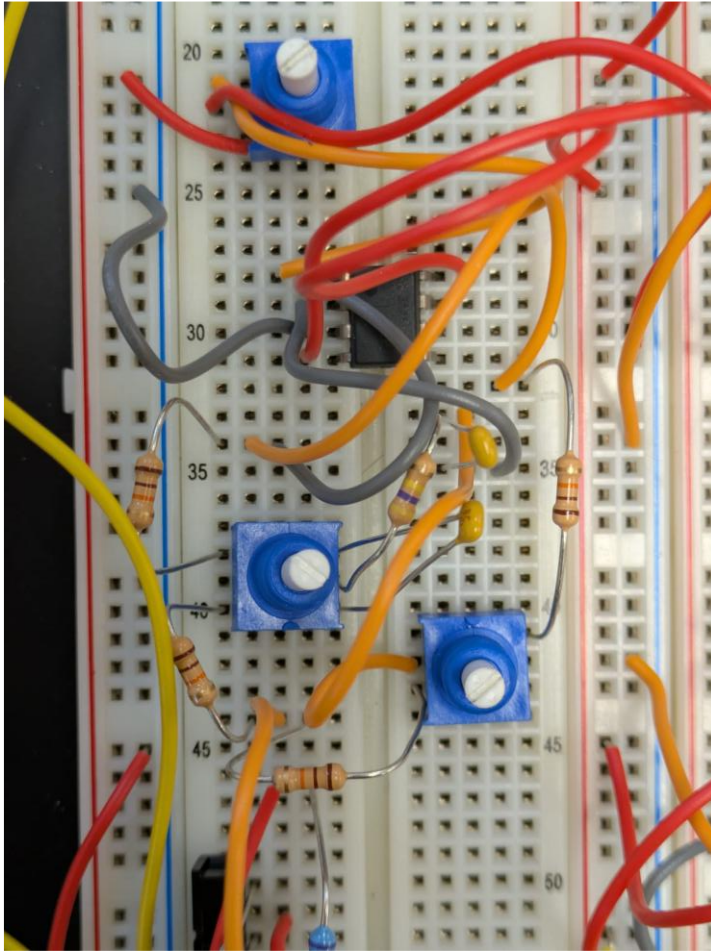
Overall Circuit



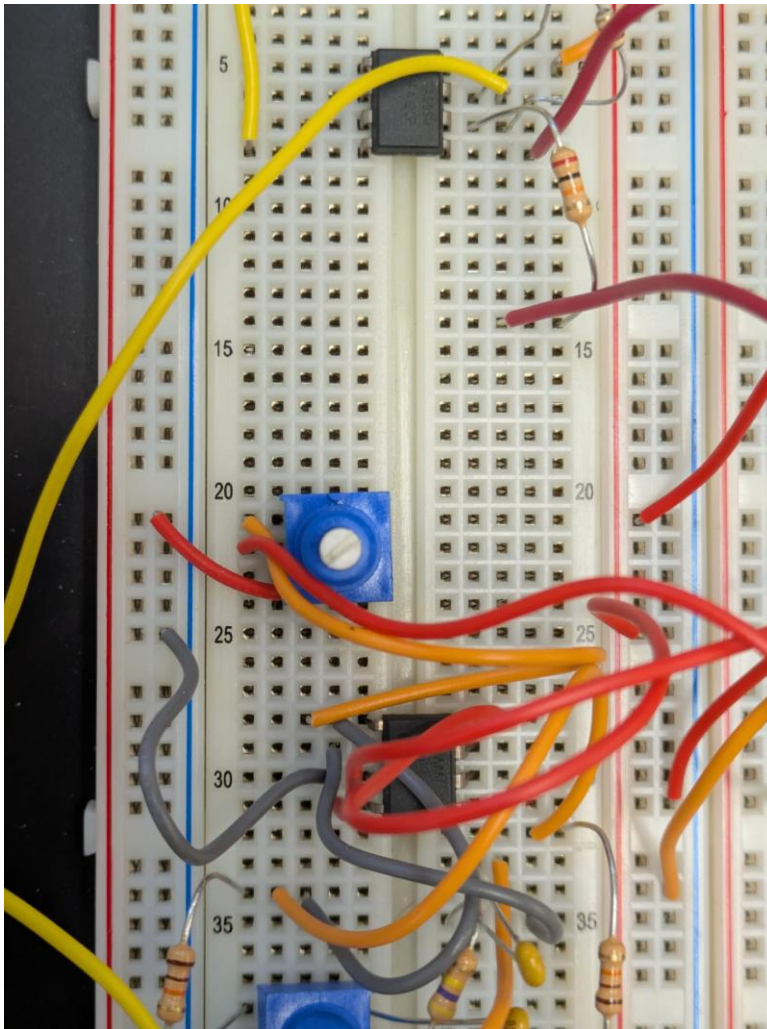
Overall Circuit on breadboard



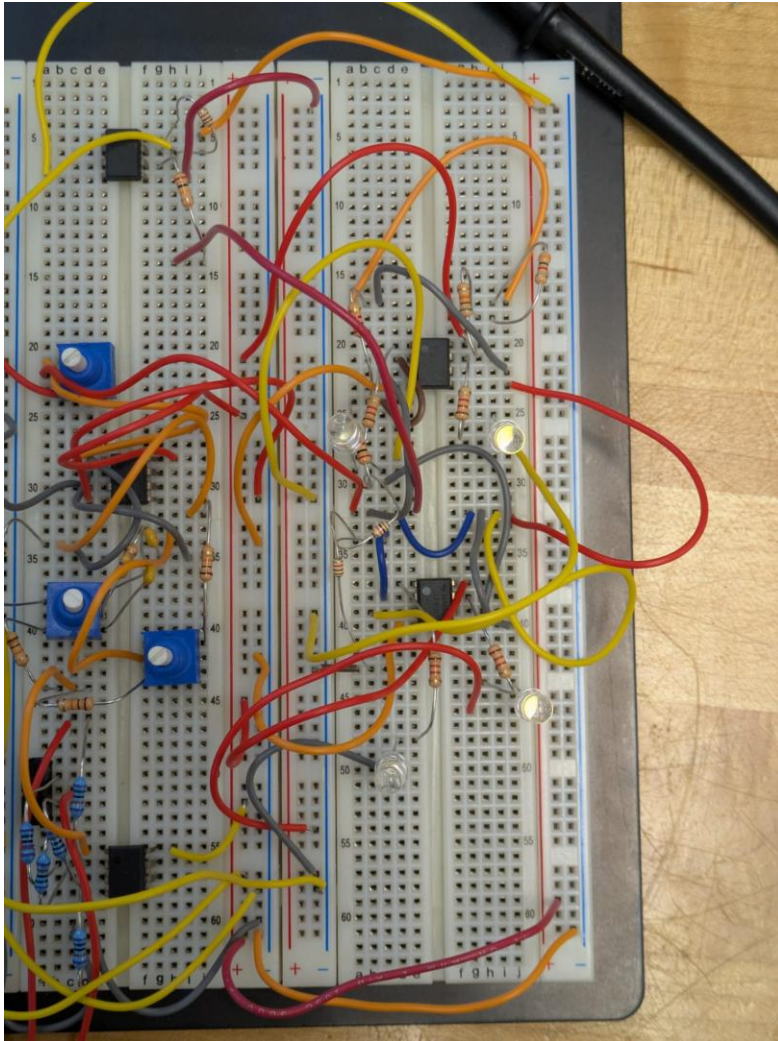
Block 1 on breadboard



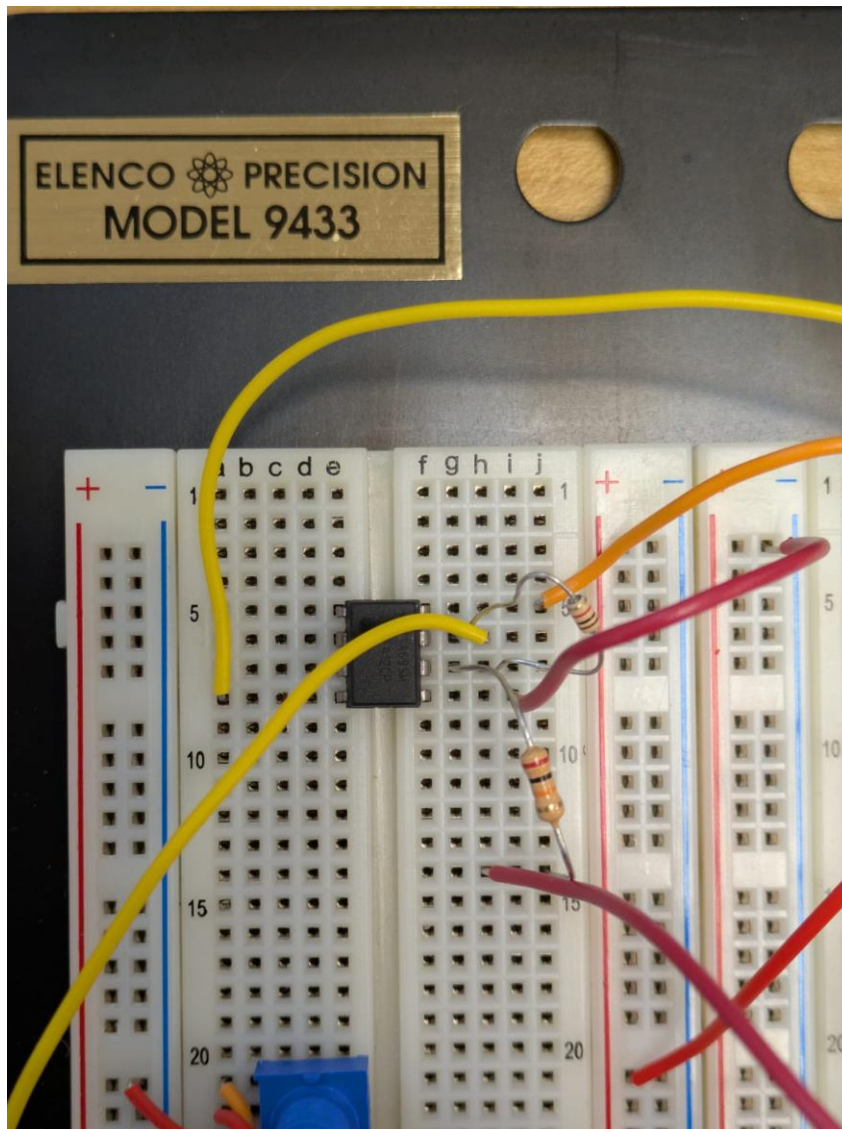
Block 2 on breadboard



Block 3 on breadboard



Block 4 on breadboard (right hand side)



Block 5 on breadboard

Conclusion

The project successfully integrated multiple analog circuit concepts into a functional and interactive audio device. Each block performed its role as expected, and the full system worked well when powered and tested with audio signals. The most challenging parts were tuning resistor values for gain and avoiding loading issues between blocks. Overall, this project was a practical way to apply EE 210 topics and gain confidence working with real audio signals.

