Differences Between the Hi3515C and the Hi3520D

Issue   01

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About This Document

Purpose

This document describes the differences between the Hi3515C and the Hi3520D from the aspects of specifications and pin map.

Related Versions

The following table lists the product versions related to this document.

Product Name	Version
Hi3515C	V100
Hi3520D	V100

Intended Audience

This document is intended for:

- Technical support personnel
- Board hardware development engineers

Change History

Changes between document issues are cumulative. Therefore, the latest document issue contains all changes made in previous issues.

Issue 01 (2013-06-21)

This issue is the first official release.

Contents

Figures

Tables

1 Differences

⚠️ **CAUTION**

This document prevails if there are differences between the Hi3515C and the Hi3520D during development. Except the differences, the documents related to the Hi3520D apply to the Hi3515C.

Table 1-1 describes the differences between the specifications of the Hi3515C and the Hi3520D.

Table 1-1 Differences between the specifications of the Hi3515C and Hi3520D

Item	Hi3515C	Hi3520D
Number of SATA interfaces	One SATA interface (SATA0)	Two SATA interfaces (SATA0 and SATA1)
Pinout	• Only SATA0-related pins • One LED pin (SATA_LED_N0)	• SATA0- and SATA1-related pins • Two LED pins (SATA_LED_N0 and SATA_LED_N1)
Value of the chip ID register (Periphctrl15)	2'b10	2'b11

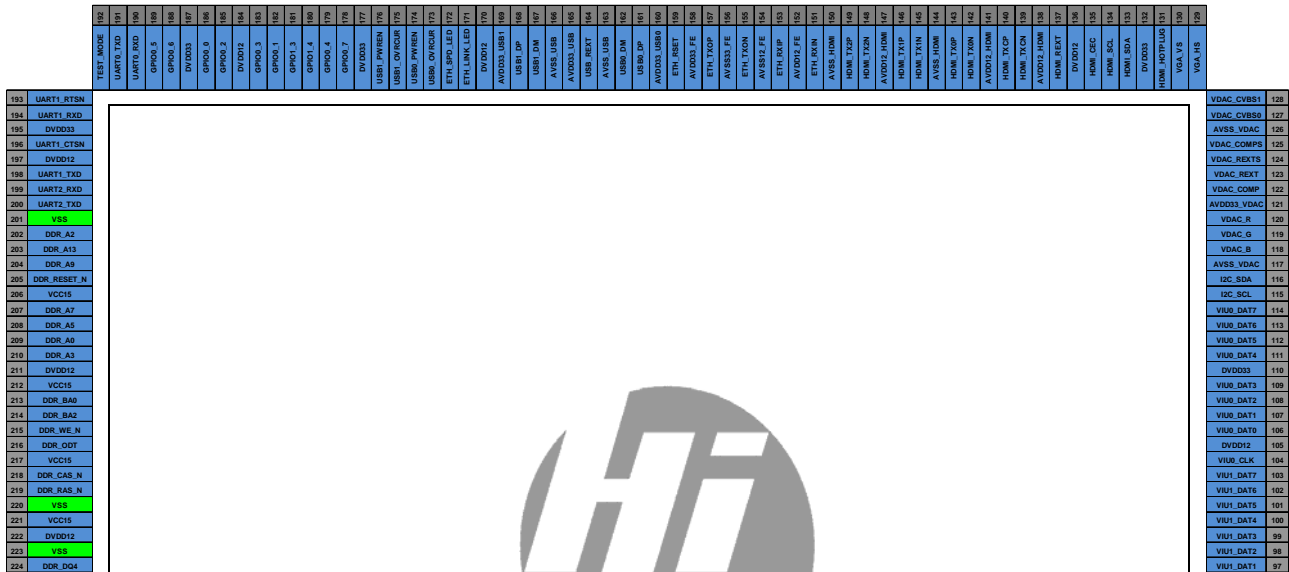Figure 1-2 and Figure 1-3 show the pin maps of the Hi3515C.

Figure 1-2 Pin map of the Hi3515C (1)

Figure 1-3 Pin map of the Hi3515C (2)