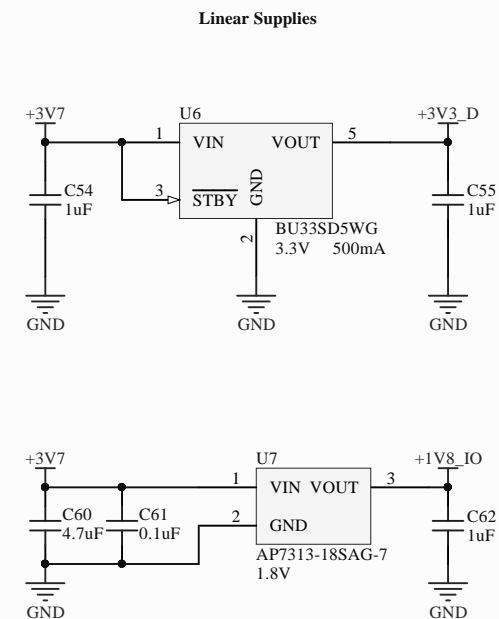


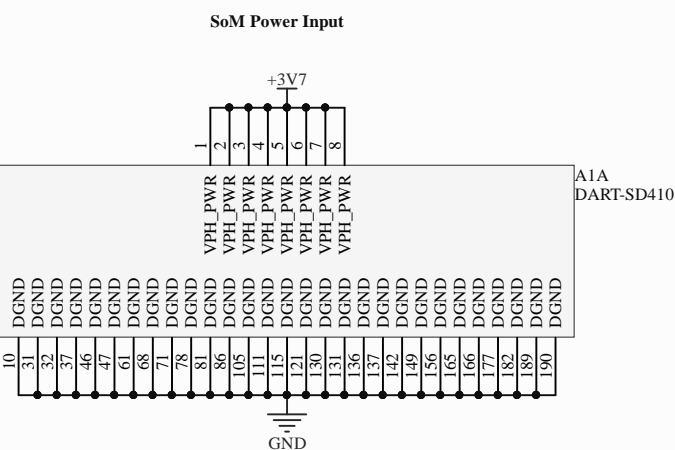
A

A

Battery Charger - Designed for 500mA

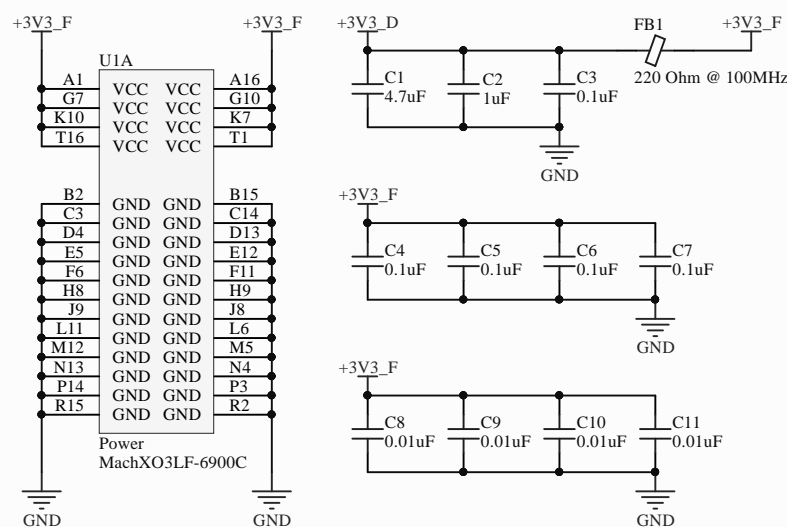




Linear Supplies

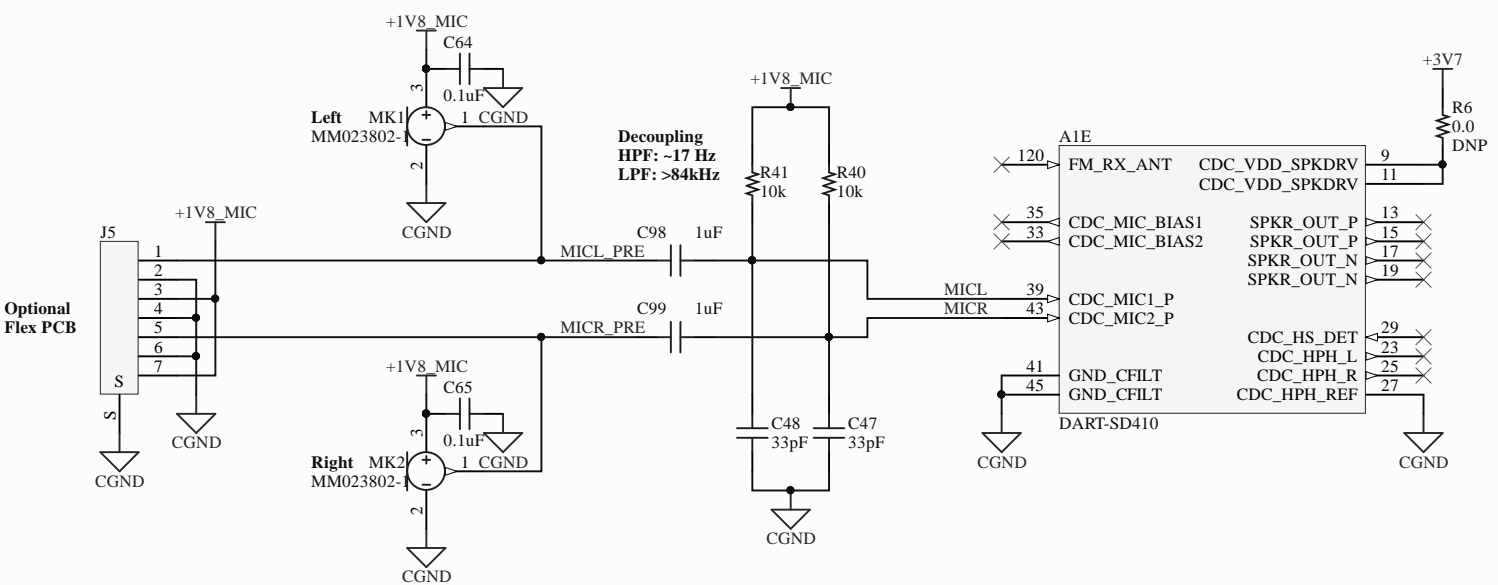


SoM Power Input

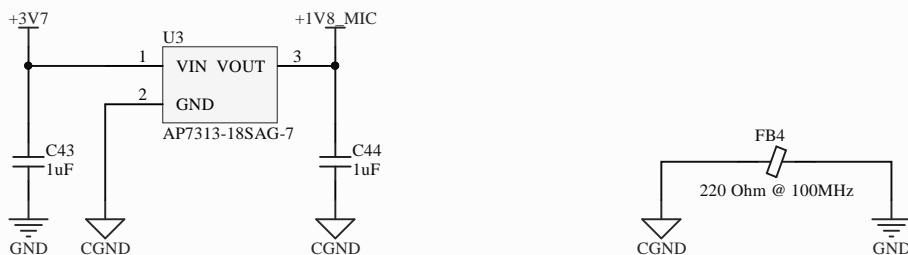
FPGA Power Input

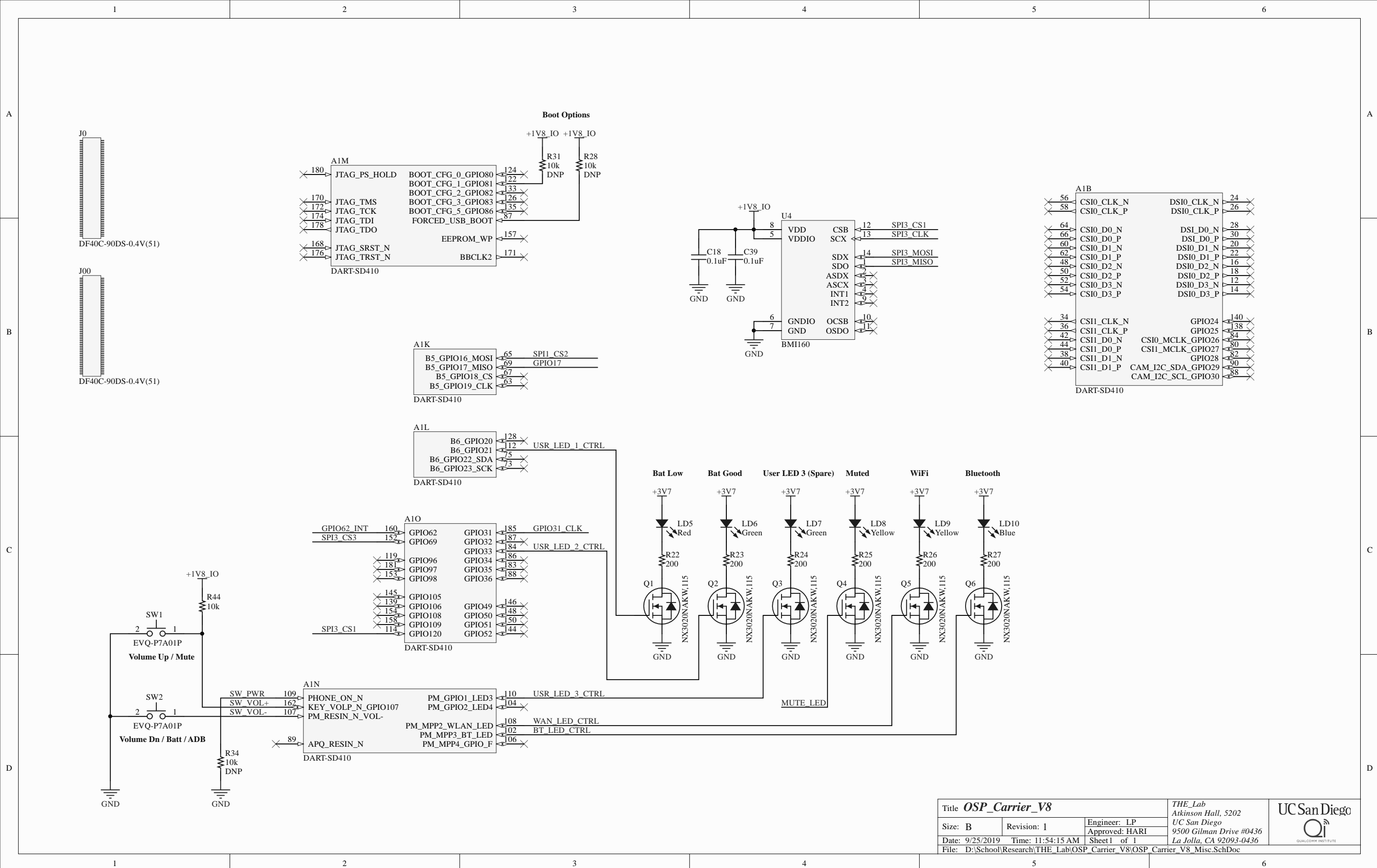


Title OSP_Carrier_V8			THE_Lab Atkinson Hall, 5202		 
Size: B	Revision: 1	Engineer: LP	UC San Diego		
		Approved: HARI	9500 Gilman Drive #0436		
		Sheet 1 of 1	La Jolla, CA 92093-0436		
Date: 9/25/2019 Time: 11:54:15 AM			File: D:\School\Research\THE_Lab\OSP_Carrier_V8\OSP_Carrier_V8_Power.SchDoc		




1.8V Regulator for Mic Power and Bias





	1	2	3	4	5	6
A						
B						
C						
D						
	1	2	3	4	5	6

Title <i>OSP_Carrier_V8</i>			<div>THE_Lab Atkinson Hall, 5202 UC San Diego 9500 Gilman Drive #0436 La Jolla, CA 92093-0436</div> <div>UC San Diego  QUALCOMM INSTITUTE</div>
Size: B	Revision: 1	Engineer: LP	
Date: 9/25/2019	Time: 11:54:16 AM	Approved: HARI	
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