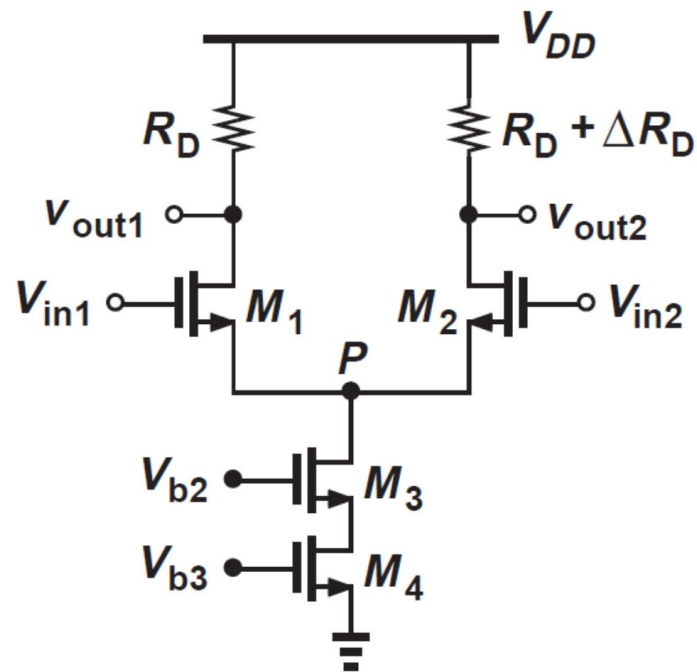
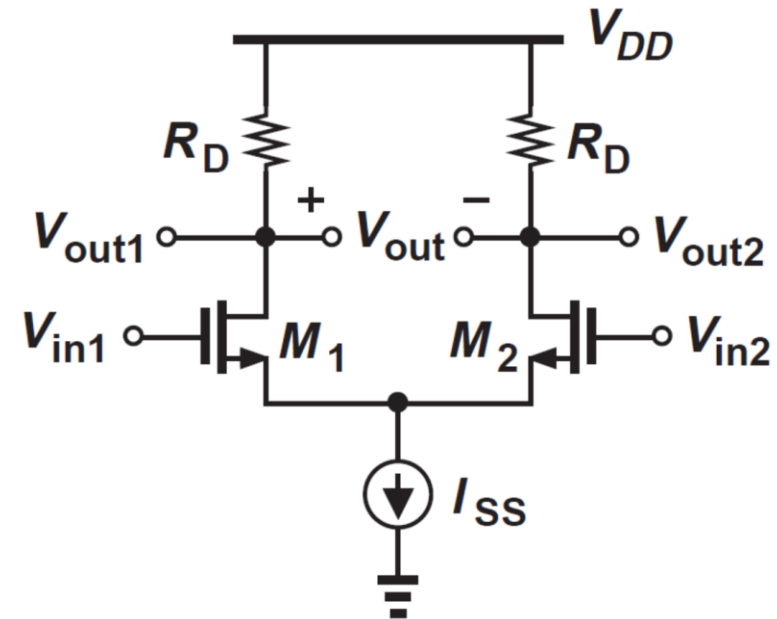


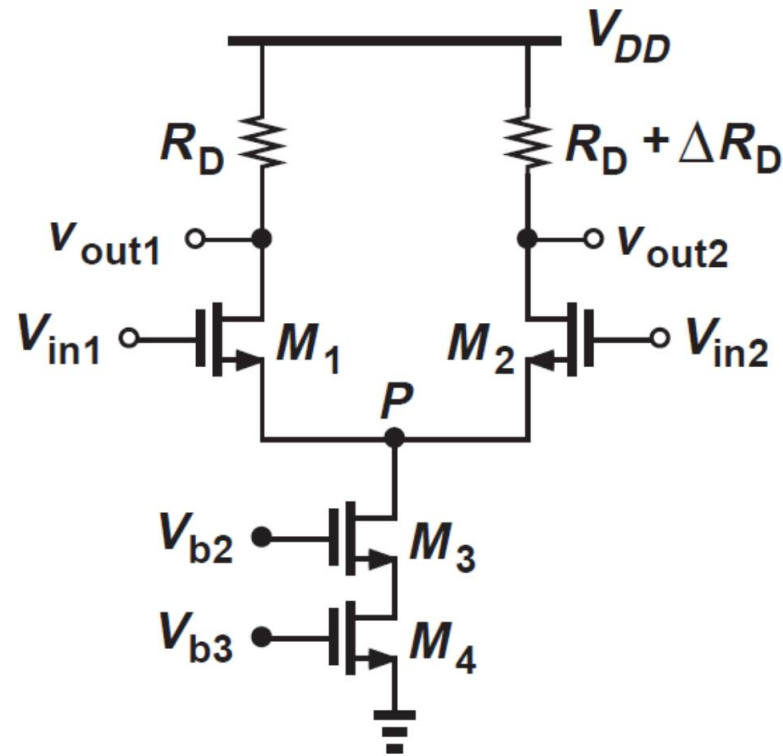
Q1. Compute the common-mode rejection ratio of the stage below. For simplicity, neglect channel-length modulation in M_1 and M_2 but not in other transistors. A half circuit concept can be applied for A_{DM} . $g_m = g_{m1} = g_{m2}$ $\rightarrow |A_{DM}| = g_m R_D$



Q2. Find W/L of M_1 and M_2 of the MOS differential pair for a voltage gain of 5 and a power dissipation of 1 mW if the equilibrium overdrive, $V_{GS} - V_{TH}$, must be 150 mV. Assume $\lambda = 0$, $\mu_n C_{ox} = 100 \mu A/V^2$, $V_{DD} = 1.8V$, and $g_m = g_{m1} = g_{m2}$. $\rightarrow |g_m R_D|$



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