Question 1. Source Follower + CS

[40 points] In this question, we will study the source follower cascaded by a CS stage. Neglect body effect and channel length modulation.

- (a) First we carry out a dc analysis on the CS stage. In the CS stage circuit shown in Figure 1, assume $\mu_n C_{ox} \left(\frac{W}{L}\right)_1 = \mu_p C_{ox} \left(\frac{W}{L}\right)_2 = 1 \text{mA/V}^2$, $V_{TN} = 0.7 \text{V}$, $V_{TP} = -1 \text{V}$, $V_{DD} = 2.5 \text{V}$. Calculate I_1 and V_{OUT} such that M_1 operates in saturation with $V_{GS1} = 0.8 \text{V}$. What is V_{IN} if M_1 is at the edge of saturation?
- (b) Then we do small signal analysis on the CS stage. In the circuit shown in Figure 1, derive the small signal voltage gain $A_v = \frac{v_{out}}{v_{in}}$. What is the maximum dc level of V_{in} for which M1 remains saturated? Express V_{IN} by V_{DD} , gate-source voltages and threshold voltages of M_1 and M_2 .
- (c) To accommodate an input dc level close to V_{DD} , we further cascade it with a source follower as shown in Figure 2. Derive the small signal gain $\frac{v_{out}}{v_{in}}$. If $V_{IN} = V_{DD}$, what relationship between the gate-source voltages of M_2 and M_3 guarantees that M_1 is saturated?

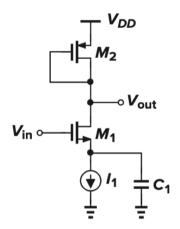


Figure 1. CS Stage

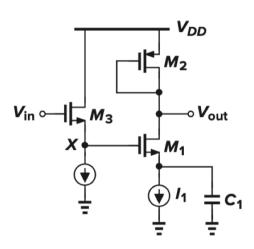
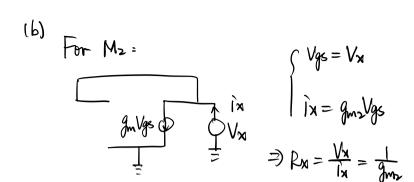
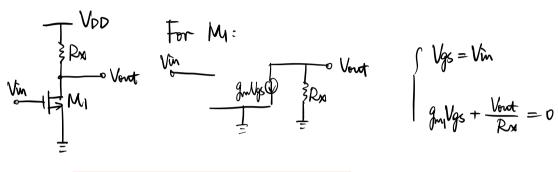


Figure 2. Source Follower + CS

(a) Suppose M2 is on: $Vsg_2 > VTP |$. By observation, $Vsp_2 > Vsg_2 - |VTP|$ $\Rightarrow M_2$ is in saturation.

For M1: $I_1 = ID_1 = \frac{1}{2} \mu \mu Cox(W)_1 (VeG_1 - VTN)^2 = \frac{1}{2} \cdot 1 m \cdot (0.8 - 0.7)^2 = 5 \times 10^{-3} mA$ For M2: $I_{D2} = \frac{1}{2} \mu \mu Cox(W)_2 (V_{DD} - V_{out} - |V_{TP}|)^2 = \frac{1}{2} \cdot 1 m \cdot (2.5 - V_{out} - 1)^2 = 5 \times 10^{-3} mA$ $\Rightarrow V_{out} = 1.4 \text{ V or } 1.6 \text{ V}$ Since $V_{SGD_2} = V_{DD} - V_{out} = 2.5 - V_{out} > |V_{TP}| = 1 \text{ V } \Rightarrow V_{out} = 1.4 \text{ V}$ $\therefore I_1 = 5 \times 10^{-3} mA, V_{out} = 1.4 \text{ V}$ If M₁ is at the edge of saturation, $V_{D1} = V_{O1} - V_{PN}$ $\Rightarrow V_{out} = V_{in} - 0.7 \Rightarrow V_{in} = 1.4 + 0.7 = 2.1 \text{ V}$





$$\Rightarrow A_{V} = \frac{V_{out}}{V_{in}} = -g_{m_{1}}R_{xx} = -\frac{g_{m_{1}}}{g_{m_{2}}}$$

For M to be in saturation,

(C) For Ms source follower part,

$$V_{M} \circ \frac{V_{M}}{V_{M}} = V_{M} - V_{M}$$

$$V_{M} = V_{M} - V_{M}$$

$$\Rightarrow A_{V} = \frac{Vout}{Vin} = \frac{Vout}{Vx} \cdot \frac{Vx}{Vin} = -\frac{g_{mx}}{g_{mx}}$$

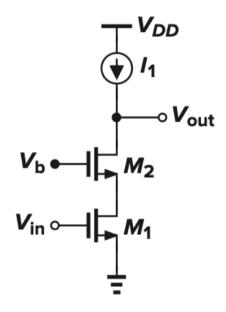
If Vin = Voo, then Va = Voo - Vasz.

For My to be in saturation, VDS1 > VBS1 - VTH1 > Vout > VX - VTH1

Question 2. Cascode Amplifier

[30 points] In the following cascode amplifier, $\mu_n C_{ox} \left(\frac{W}{L} \right)_1 = \mu_n C_{ox} \left(\frac{W}{L} \right)_2 = 1 \text{mA/V}^2$, $V_{TH} = 0.7 \text{V}$, $\lambda = 0.1$, $I_1 = 1 \text{mA}$, $V_b = 3 \text{V}$. No body effect.

- (a) Calculate V_{IN} and V_{OUT} such that M_1 operates at the edge of saturation.
- (b) Draw small signal model and calculate the small signal gain $A_v = \frac{v_{out}}{v_{in}}$.



(a) For M1: $I_{D1} = \frac{1}{2} \text{lln} \text{Cox}(\frac{W}{L})_1 (\text{Vin} - \text{V}_{TH})^2 (1 + \text{Tr} \text{Vx}) = I_1$ ①

Since M1 operates at the edge of saturation, $\text{VDS}_1 = \text{Vor}_1 - \text{V}_{TH}$ $\Rightarrow \text{Vx} = \text{Vin} - \text{V}_{TH} \otimes$

 \Rightarrow $V_x = 1.33 \text{ V}$ (rule out negative results for $V_x = V_{\hat{m}} - V_{TH} > 0$) $V_{\hat{m}} = V_x + V_{TH} = 1.33 + 0.7 = 2.03 \text{ V}$

Assume M2 in socturation, Vout
$$\gg V_b - V_{TH} = 2.3 \text{ V}$$
 $\text{Id}_2 = \frac{1}{2} \mu_0 \text{ Coss} \left(\frac{W}{L}\right)_2 \left(V_b - V_x - V_{TH}\right)^2 \left(1 + 7L\left(V_{\text{out}} - V_x\right)\right)$
 $= \frac{1}{2} \cdot 1 \text{ m} \cdot \left(3 - 1.33 - 0.7\right)^2 \left(1 + 0.1\left(V_{\text{out}} - 1.33\right)\right)$
 $= 1 \text{ mA}$
 $\Rightarrow V_{\text{out}} = 12.59 \text{ V} \quad (\text{assumption satisfied})$
 $\therefore V_{IN} = 2.03 \text{ V}, \quad V_{\text{out}} = 12.59 \text{ V}$

(ds)

Vin o
$$\frac{1}{2}$$
 $\frac{1}{2}$ $\frac{1}{2}$

$$\Rightarrow g_{m_1} V_{\hat{m}} + \frac{V_x}{r_{0_1}} = 0 \Rightarrow V_x = -g_{m_1} r_{0_1} V_{\hat{m}}$$

$$V_{\text{out}} = V_{\text{X}} \left(1 + g_{\text{m}_{\text{X}}} \gamma_{0_{\text{Y}}} \right) = -g_{\text{m}_{\text{Y}}} \gamma_{0_{\text{Y}}} \left(1 + g_{\text{m}_{\text{X}}} \gamma_{0_{\text{Y}}} \right) V_{\text{in}}$$

$$\Rightarrow A_{V} = \frac{Vout}{Vin} = -g_{m_{1}} \gamma_{0}, (1+g_{m_{2}} \gamma_{0})$$

':
$$r_{01} = r_{02} = \frac{1}{\pi I_0} = \frac{1}{0.1 \times 1m} = 10 \text{ k}\Omega$$

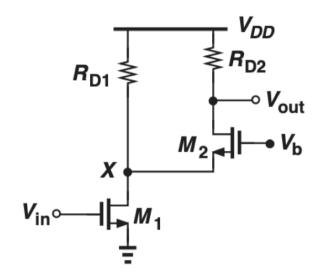
 $q_{m1} = q_{m2} = \sqrt{2\mu n G_{00} \frac{W}{L} I_0} = 1.41 \text{ m}\Omega^{-1}$

$$A_{V} = -g_{m_1} \gamma_{0_1} (1+g_{m_2} \gamma_{0_2}) = -213$$

Question 3. CS + CG

[30 points] In the following circuit, a common-source stage $(M_1 \text{ and } R_{D1})$ is followed by a common-gate stage (M_2 and R_{D2}). Assume $\lambda = 0$.

- (a) Derive the voltage gain $A_v = \frac{v_{out}}{v_{in}}$. (b) Simplify the result obtained in (a) if $R_{D1} \to \infty$.



(b)
$$R_{D1} \rightarrow \infty$$
: $A_V = -g_{m1}g_{m2}R_{D2} \cdot \frac{1}{g_{m2}} = -g_{m1}R_{D2}$