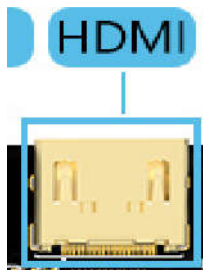
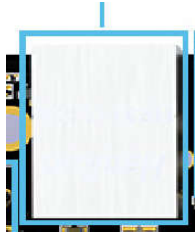

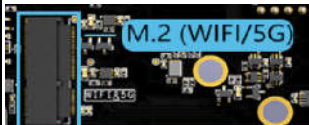















Current Revision	1.0	
Revision History		
Date	Revision	Description
2024/1/22	1.0	Initial Release,WeAct-N002 Pinmux



WeAct-N002 A1 Pinmux

Carrier Board Function(载板)			SOM Connector (核心板)	
Signal Name	Position	Level	Signal Name	Pin
HDMI				
HDMI TXC P		/	DP1_TXD3	83
HDMI TXC N		/	DP1_TXD3	81
HDMI TX0 P		/	DP1_TXD2	77
HDMI TX0 N		/	DP1_TXD2	75
HDMI TX1 P		/	DP1_TXD1	71
HDMI TX1 N		/	DP1_TXD1	69
HDMI TX2 P		/	DP1_TXD0	65
HDMI TX2 N		/	DP1_TXD0	63
HDMI DDC_SCL		/	DP1_AUX_P	100
HDMI DDC_SDA		/	DP1_AUX_N	98
HDMI HPD		/	DP1_HP	96
HDMI CEC		/	HDMI_CEC	94
GigaEthernet				
GBE MDIO P		/	GBE MDIO	186
GBE MDIO N		/	GBE MDIO	184
GBE MDI1 P		/	GBE MDI1	192
GBE MDI1 N		/	GBE MDI1	190
GBE MDI2 P		/	GBE MDI2	198
GBE MDI2 N		/	GBE MDI2	196
GBE MDI3 P		/	GBE MDI3	204
GBE MDI3 N		/	GBE MDI3	202
GBE LINK_ACT		/	GBE LINK_ACT	194
GBE LED_LINK		/	GBE LINK1000	188
PCIE KEYM(SSD)				
PCIE0_CLK_P		/	PCIE0_CLK	162
PCIE0_CLK_N		/	PCIE0_CLK	160
PCIE0_TX0_P		/	PCIE0_TX0	136
PCIE0_TX0_N		/	PCIE0_TX0	134
PCIE0_RX0_P		/	PCIE0_RX0	133
PCIE0_RX0_N		/	PCIE0_RX0	131
PCIE0_TX1_P		/	PCIE0_TX1	142
PCIE0_TX1_N		/	PCIE0_TX1	140
PCIE0_RX1_P		/	PCIE0_RX1	139
PCIE0_RX1_N		/	PCIE0_RX1	137
PCIE0_TX2_P		/	PCIE0_TX2	150
PCIE0_TX2_N		/	PCIE0_TX2	148
PCIE0_RX2_P		/	PCIE0_RX2	151
PCIE0_RX2_N		/	PCIE0_RX2	149
PCIE0_TX3_P		/	PCIE0_TX3	156
PCIE0_TX3_N		/	PCIE0_TX3	154
PCIE0_RX3_P		/	PCIE0_RX3	157
PCIE0_RX3_N		/	PCIE0_RX3	155
PCIE_WAKE#		/	PCIE_WAKE	179
PCIE0_RST#		/	PCIE0_RST	181
PCIE0_CLKREQ#		/	PCIE0_CLKREQ	180
PCIE KEYE(WIFI/4G)				
PCIE1_CLK_P		/	PCIE1_CLK	175
PCIE1_CLK_N		/	PCIE1_CLK	173
PCIE1_TX0_P		/	PCIE1_TX0	174
PCIE1_TX0_N		/	PCIE1_TX0	172
PCIE1_RX0_P		/	PCIE1_RX0	169
PCIE1_RX0_N		/	PCIE1_RX0	167
PCIE_WAKE#		/	PCIE_WAKE	179
PCIE1_RST#		/	PCIE1_RST	183
PCIE1_CLKREQ#		/	PCIE1_CLKREQ	182
TypeC(USB3.2 Gen2)				
USBSS1_RX_P		/	USBSS1_RX	41
USBSS1_RX_N		/	USBSS1_RX	39
USBSS1_TX_P		/	USBSS1_TX	47
USBSS1_TX_N		/	USBSS1_TX	45
USB0_DP		/	USB0_D	111
USB0_DN		/	USB0_D	109
USB0_VBUS_DET		/	GPIO0	87
USB3.0				
USB0_SS_RXP(HUB1)	下层USB		USBSS_RX	163
USB0_SS_RXN(HUB1)			USBSS_RX	161
USB0_SS_TXP(HUB1)			USBSS_TX	168
USB0_SS_TXN(HUB1)			USBSS_TX	166
USB1_DP(HUB1)			USB1_D	111
USB1_DN(HUB1)	USB1_D		109	
USB1_SS_RXP(HUB2)	USBSS_RX		163	
USB1_SS_RXN(HUB2)	USBSS_RX		161	
USB1_SS_TXP(HUB2)	上层USB		USBSS_TX	168

USB1_SS_TXN(HUB2)		/	USBSS_TX	166
USB2_DP(HUB2)		/	USB1_D	111
USB2_DN(HUB2)		/	USB1_D	109
SDCARD				
SDCARD_D0		/	SDCARD_D0	219
SDCARD_D1		/	SDCARD_D1	221
SDCARD_D2		/	SDCARD_D2	223
SDCARD_D3		/	SDCARD_D3	225
SDCARD_CMD		/	SDCARD_CMD	227
SDCARD_CLK		/	SDCARD_CLK	229
SDCARD_CD		/	GPIO10	212
MIPI CSI1 4Lane				
CSI0_D0_P		/	CSI0_D0	6
CSI0_D0_N		/	CSI0_D0	4
CSI0_D1_P		/	CSI0_D1	18
CSI0_D1_N		/	CSI0_D1	16
CSI0_CLK_P		/	CSI0_CLK	12
CSI0_CLK_N		/	CSI0_CLK	10
CSI1_D0_P		/	CSI0_D0	5
CSI1_D0_N		/	CSI0_D0	3
CSI1_D1_P		/	CSI0_D1	17
CSI1_D1_N		/	CSI0_D1	15
CAM0_PWDN		/	CAM0_PWDN	144
CAM0_MCLK		/	CAM0_MCLK	146
CAM_I2C_SCL		/	CAM_I2C_SCL	213
CAM_I2C_SDA		/	CAM_I2C_SDA	215
CAM_MUX_SEL		/	GPIO6	130
MIPI CSI2 4Lane				
CSI2_D0_P		/	CSI2_D0	24
CSI2_D0_N		/	CSI2_D0	22
CSI2_D1_P		/	CSI2_D1	36
CSI2_D1_N		/	CSI2_D1	34
CSI2_CLK_P		/	CSI2_CLK	30
CSI2_CLK_N		/	CSI2_CLK	28
CSI3_D0_P		/	CSI3_D0	23
CSI3_D0_N		/	CSI3_D0	21
CSI3_D1_P		/	CSI3_D1	35
CSI3_D1_N		/	CSI3_D1	33
CAM1_PWDN		/	CAM1_PWDN	120
CAM1_MCLK		/	CAM1_MCLK	122
CAM_I2C_SCL		/	CAM_I2C_SCL	213
CAM_I2C_SDA		/	CAM_I2C_SDA	215
CAM_MUX_SEL		/	GPIO6	130
CAN				
5V(左)		/	/	/
GND		/	/	/
CAN_H		3V3	CAN_RX	143
CAN_L		3V3	CAN_TX	145
Function UART				
3V3(左)		/	/	/
GND		/	/	/
UART1_TX		3V3	UART1_TX	203
UART1_RX		3V3	UART1_RX	205
Debug UART				
3V3(左)		/	/	/
GND		/	/	/
UART2_TX		3V3	UART2_TX	236
UART2_RX		3V3	UART2_RX	238
I2C				
5V(左)		5V	/	/
GND		/	/	/
SCL		3V3	I2C1_SCL	189
SDA		3V3	I2C1_SDA	190
GPIO				
GPIO4(左)		3V3	GPIO13	228
GPIO3		3V3	GPIO12	218
GPIO2		3V3	GPIO09	211
GPIO1		3V3	GPIO01	118
FAN Connector				
GND(上)		5V	GPIO14	230
5V		5V	GPIO8	208
FAN_TACH		5V	/	/
FAN_PWM		/	/	/
RTC后备电源				
1.65V-5.5V(上)		1.65V-5.5V	PMIC_BBAT	235
GND		GND	/	/

Pin	Name
1	GND
2	CSI2_D0_N
3	CSI2_D0_P
4	GND
5	CSI2_D1_N
6	CSI2_D1_P
7	GND
8	CSI2_CLK_N
9	CSI2_CLK_P
10	GND
11	CSI3_D0_N
12	CSI3_D0_P
13	GND
14	CSI3_D1_N
15	CSI3_D1_P
16	GND
17	CAM1_PWDN
18	CAM1_MCLK
19	GND
20	CAM_I2C_SCL
21	CAM_I2C_SDA
22	3V3

Pin	Name
1	GND
2	CSI1_D0_N
3	CSI1_D0_P
4	GND
5	CSI1_D1_N
6	CSI1_D1_P
7	GND
8	CSI0_CLK_N
9	CSI0_CLK_P
10	GND
11	CSI0_D0_N
12	CSI0_D0_P
13	GND
14	CSI0_D1_N
15	CSI0_D1_P
16	GND
17	CAM0_PWDN
18	CAM0_MCLK
19	GND
20	CAM_I2C_SCL
21	CAM_I2C_SDA
22	3V3

	从上往下		从上往下
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