Current Revision	1.0	
Revision History		
Date	Revision	Description
		Initial Release, WeAct-N002

WeAct-N002 A1 Pinmux Carrier Board Function SOM Connector				
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Signal Name	Position	Level HDMI	Signal Name	Pin
HDMI TXC P		/	DP1 TXD3	83
HDMI TXC N	HDMI	/	DP1 TXD3	81
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HDMI TX0 N		/	DP1 TXD2	75
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HDMI_TX1_N		/	DP1_TXD1	69
HDMI_TX2_P		/	DP1_TXD0	65
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HDMI_DDC_SDA	<u> </u>	/	DP1_AUX_N	98
HDMI_HPD	and the street of the street o	/	DP1_HPD	96
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GBE_MDI1_P		/	GBE MDI1	192
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GBE_MDI2_P	4	/	GBE MDI2	198
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GBE MDI3 N		/	GBE MDI3	202
GBE LINK ACT		/	GBE LINK ACT	194
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PCIE0_CLK_N	<u> </u>	/	PCIE0_CLK	160
PCIE0_TX0_P		/	PCIE0_TX0	136
PCIE0_TX0_N		/	PCIE0_TX0	134
PCIE0_RX0_P		/	PCIE0 RX0	133
PCIE0_RX0_N	_	/	PCIE0_RX0	131
PCIEO_TX1_P	<u> </u>	/	PCIE0_TX1	142
PCIEO TX1 N	M.2 (SSD) -	/	PCIEO_TX1	140
PCIEO_RX1_P	W.2 (33D)	/	PCIEO_RX1	139
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PCIE0 RX2 N	<del> </del>	/	PCIE0 RX2	149
PCIEO TX3 P	<u> </u>	/	PCIE0 TX3	156
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PCIE0_RX3_N	T	/	PCIE0_RX3	155
PCIE_WAKE#	Ī	/	PCIE_WAKE	179
PCIE0_RST#	<u> </u>	/	PCIE0_RST	181
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PCIE1_CLK_N		/	PCIE1_CLK	173
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USBSS1 TX P		/	USBSS1 TX	47
USBSS1_TX_N	TypeC(USB3)	/	USBSS1_TX	45
USB0_DP	- (J) pec(0303)	/	USB0_D	111
USB0_DN	j	/	USB0_D	109
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SB0_SS_TXN(HUB1)	[	/	USBSS_TX	166
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USB1_DN(HUB1)			USB1_D	109
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CSI0_D1_N		/	CSI0_D1	16
CSIO_CLK_P		/	CSI0_CLK	12
CSIO_CLK_N		/	CSI0_CLK	10
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CAM I2C SDA CAM MUX SEL  5V(左) GND CAN H CAN L  3V3(左) GND UART1_TX UART1_RX  3V3(左) GND UART2_TX UART2_TX UART2_RX  5V(左) GND		/	CAM I2C SDA GPIO6  / / / CAN RX CAN TX  / UART1 TX UART1 RX  / UART2 TX UART2 RX  / / / / / / / / / / / / / / / / / /	215 130 / / 143 145 / / 203 205 / / 236 238
CAM I2C SDA CAM MUX SEL  5V(左) GND CAN H CAN L  3V3(左) GND UART1_TX UART1_RX  3V3(左) GND UART2_TX UART2_TX UART2_RX  5V(左) GND SCL		/	CAM I2C SDA GPIO6  / / CAN RX CAN RX CAN TX  / UART1 TX UART1 RX  / UART2 TX UART2 RX  / / / / I2C1 SCL	215 130 / / / 143 145 / / 203 205
CAM I2C SDA CAM MUX SEL  5V(左) GND CAN H CAN L  3V3(左) GND UART1_TX UART1_RX  3V3(左) GND UART2_TX UART2_TX UART2_RX  5V(左) GND		/	CAM I2C SDA GPIO6  / / / CAN RX CAN TX  / UART1 TX UART1 RX  / UART2 TX UART2 RX  / / / / / / / / / / / / / / / / / /	215 130 / / 143 145 / / 203 205 / / 236 238 / / / 189
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CAM I2C SDA CAM MUX SEL  5V(左) GND CAN H CAN L  3V3(左) GND UART1 TX UART1 RX  3V3(左) GND UART2 TX UART2 TX UART2 RX  5V(左) GND SCL SDA		/	CAM I2C SDA GPIO6  / / // CAN RX CAN TX  / / UART1 TX UART1 RX  / / / UART2 TX UART2 TX UART2 TX UART2 SDA  GPIO13	215 130  / / / 143 145  / / 203 205  / / 236 238  / / 189 190
CAM I2C SDA CAM MUX SEL  5V(左) GND CAN H CAN L  3V3(左) GND UART1 TX UART1 RX  3V3(左) GND UART2 TX UART2 TX UART2 RX  5V(左) GND SCL SDA		/	CAM I2C SDA GPIO6  / / / CAN RX CAN TX  / / UART1 TX UART1 RX  / / / UART2 TX UART2 TX UART2 SCL I2C1 SDA  GPIO13 GPIO12	215 130  / / / 143 145  / / 203 205  / / / 236 238  / / / 189 190  228 218
CAM I2C SDA CAM MUX SEL  5V(左) GND CAN H CAN L  3V3(左) GND UART1 TX UART1 RX  3V3(左) GND UART2 TX UART2 TX UART2 RX  5V(左) GND SCL SDA  GPIO4(左) GPIO3 GPIO2		/	CAM I2C SDA GPIO6  / / / CAN RX CAN TX  / UART1 TX UART1 TX UART2 TX UART2 TX UART2 TX UART2 SCL I2C1 SDA  GPIO13 GPIO12 GPIO09	215 130  / / / 143 145  / / 203 205  / / / 236 238  / / / 189 190  228 218 211
CAM I2C SDA CAM MUX SEL  5V(左) GND CAN H CAN L  3V3(左) GND UART1 TX UART1 RX  3V3(左) GND UART2 TX UART2 TX UART2 RX  5V(左) GND SCL SDA		/	CAM I2C SDA GPIO6  / / / CAN RX CAN TX  / / UART1 TX UART1 RX  / / / UART2 TX UART2 TX UART2 SCL I2C1 SDA  GPIO13 GPIO12	215 130  / / / 143 145  / / 203 205  / / / 236 238  / / / 189 190  228 218
CAM I2C SDA CAM MUX SEL  5V(左) GND CAN H CAN L  3V3(左) GND UART1 TX UART1 TX UART2 TX UART2 TX UART2 TX UART2 RX  5V(左) GND SCL SDA  GPIO4(左) GPIO3 GPIO2 GPIO1		/	CAM I2C SDA GPIO6  / / // CAN RX CAN TX  / / UART1 TX UART1 RX  / / / UART2 TX UART2 RX  / / I2C1 SCL I2C1 SDA  GPIO13 GPIO12 GPIO09 GPIO01	215 130  / / / 143 145  / / 203 205  / / / 236 238  / / / 189 190  228 218 211 118
CAM I2C SDA CAM MUX SEL  5V(左) GND CAN H CAN L  3V3(左) GND UART1 TX UART1 RX  3V3(左) GND UART2 TX UART2 TX UART2 RX  5V(左) GND SCL SDA  GPIO4(左) GPIO3 GPIO2		/	CAM I2C SDA GPIO6  / / / CAN RX CAN TX  / / UART1 TX UART1 RX  / / UART2 TX UART2 TX UART2 SCL I2C1 SCL I2C1 SDA  GPIO13 GPIO10 GPIO01	215 130  / / / 143 145  / / 203 205  / / / 236 238  / / / 189 190  228 218 211 118
CAM I2C SDA CAM MUX SEL  5V(左) GND CAN H CAN L  3V3(左) GND UART1 TX UART1 TX UART1 RX  5V(左) GND UART2 TX UART2 TX UART2 RX  5V(左) GND GND SCL SDA  GPIO4(左) GPIO3 GPIO2 GPIO1  GND(上) 5V		/	CAM I2C SDA GPIO6  / / // CAN RX CAN TX  / / UART1 TX UART1 RX  / / / UART2 TX UART2 RX  / / I2C1 SCL I2C1 SDA  GPIO13 GPIO12 GPIO09 GPIO01	215 130  / / / 143 145  / / 203 205  / / / 236 238  / / / 189 190  228 218 211 118
CAM I2C SDA CAM MUX SEL  5V(左) GND CAN H CAN L  3V3(左) GND UART1 TX UART1 RX  3V3(左) GND UART2 TX UART2 TX UART2 RX  5V(左) GND SCL SDA  GPIO4(左) GPIO3 GPIO2 GPIO1 GND(上) 5V FAN TACH		/	CAM I2C SDA GPIO6  / / / CAN RX CAN TX  / / UART1 TX UART1 RX  / / UART2 TX UART2 TX UART2 SCL I2C1 SCL I2C1 SDA  GPIO13 GPIO10 GPIO01	215 130  / / / 143 145  / / 203 205  / / / 236 238  / / / 189 190  228 218 211 118
CAM I2C SDA CAM MUX SEL  5V(左) GND CAN H CAN L  3V3(左) GND UART1 TX UART1 TX UART1 RX  5V(左) GND UART2 TX UART2 TX UART2 RX  5V(左) GND GND SCL SDA  GPIO4(左) GPIO3 GPIO2 GPIO1  GND(上) 5V		/	CAM I2C SDA GPIO6  / / / CAN RX CAN TX  / / UART1 TX UART1 RX  / / UART2 TX UART2 TX UART2 SCL I2C1 SCL I2C1 SDA  GPIO13 GPIO10 GPIO01	215 130  / / / 143 145  / / 203 205  / / / 236 238  / / / 189 190  228 218 211 118
CAM I2C SDA CAM MUX SEL  5V(左) GND CAN H CAN L  3V3(左) GND UART1 TX UART1 TX UART1 RX  3V3(左) GND UART2 TX UART2 TX UART2 TX UART2 RX  5V(左) GND SCL SDA  GPIO4(左) GPIO3 GPIO2 GPIO1  GND(上) 5V FAN TACH FAN PWM		/	CAM I2C SDA GPIO6  / / CAN RX CAN RX CAN TX  / UART1 TX UART1 RX  / UART2 TX UART2 TX UART2 RX  GPIO13 GPIO13 GPIO12 GPIO09 GPIO01  GPIO14 GPIO8 / / / / / / / / / / / / / / / / GPIO8 / / / / / / / / / / / / / / / / / / /	215 130  / / / 143 145  / / 203 205  / / / 236 238  / / / 189 190  228 218 211 118  230 208 / / / / / / / / / / / / / / / / / / /
CAM I2C SDA CAM MUX SEL  5V(左) GND CAN H CAN L  3V3(左) GND UART1 TX UART1 RX  3V3(左) GND UART2 TX UART2 TX UART2 RX  5V(左) GND SCL SDA  GPIO4(左) GPIO3 GPIO2 GPIO1 GND(上) 5V FAN TACH		/	CAM I2C SDA GPIO6  / / / CAN RX CAN TX  / / UART1 TX UART1 RX  / / UART2 TX UART2 TX UART2 SCL I2C1 SCL I2C1 SDA  GPIO13 GPIO10 GPIO01	215 130  / / / 143 145  / / 203 205  / / / 236 238  / / / 189 190  228 218 211 118
CAM I2C SDA CAM MUX SEL  5V(左) GND CAN H CAN L  3V3(左) GND UART1 TX UART1 TX UART1 RX  3V3(左) GND UART2 TX UART2 TX UART2 TX UART2 RX  5V(左) GND SCL SDA  GPIO4(左) GPIO3 GPIO2 GPIO1  GND(上) 5V FAN TACH FAN PWM		/	CAM I2C SDA GPIO6  / / CAN RX CAN RX CAN TX  / UART1 TX UART1 RX  / UART2 TX UART2 TX UART2 RX  GPIO13 GPIO13 GPIO12 GPIO09 GPIO01  GPIO14 GPIO8 / / / / / / / / / / / / / / / / GPIO8 / / / / / / / / / / / / / / / / / / /	215 130  / / / 143 145  / / 203 205  / / / 236 238  / / / 189 190  228 218 211 118  230 208 / / / / / / / / / / / / / / / / / / /

Pin	Name
1	GND
2	CSI2_D0_N
3	CSI2_D0_P
4	GND
5	CSI2_D1_N
5 6 7	CSI2_D1_P
	GND
8	CSI2_CLK_N
9	CSI2_CLK_P
10	GND
11	CSI3_D0_N
12	CSI3_D0_P
13	GND
14	CSI3_D1_N
15	CSI3_D1_P
16	GND
17	CAM1_PWDN
18	CAM1_MCLK
19	GND
20	CAM_I2C_SCL
21	CAM_I2C_SDA
22	3V3

Pin	Name
1	GND
2 3 4	CSI1_D0_N
3	CSI1_D0_P
	GND
5	CSI1_D1_N
6	CSI1_D1_P
7	GND
8	CSI0_CLK_N
9	CSIO_CLK_P
10	GND
11	CSI0_D0_N
12	CSI0_D0_P
13	GND
14	CSI0_D1_N
15	CSI0_D1_P
16	GND
17	CAM0_PWDN
18	CAM0_MCLK
19	GND
20	CAM_I2C_SCL
21	CAM_I2C_SDA
22	3V3

	From		From
— CSI2 4Lane	Up	CSI1 4Lane	Up
	To		To

